

Real Time Clock Silicon Laboratories

August 2004

P1_ Title Page
P2_ Electrical scheme
P3_ Mechanical Drawing

Version 1.0 Revision B

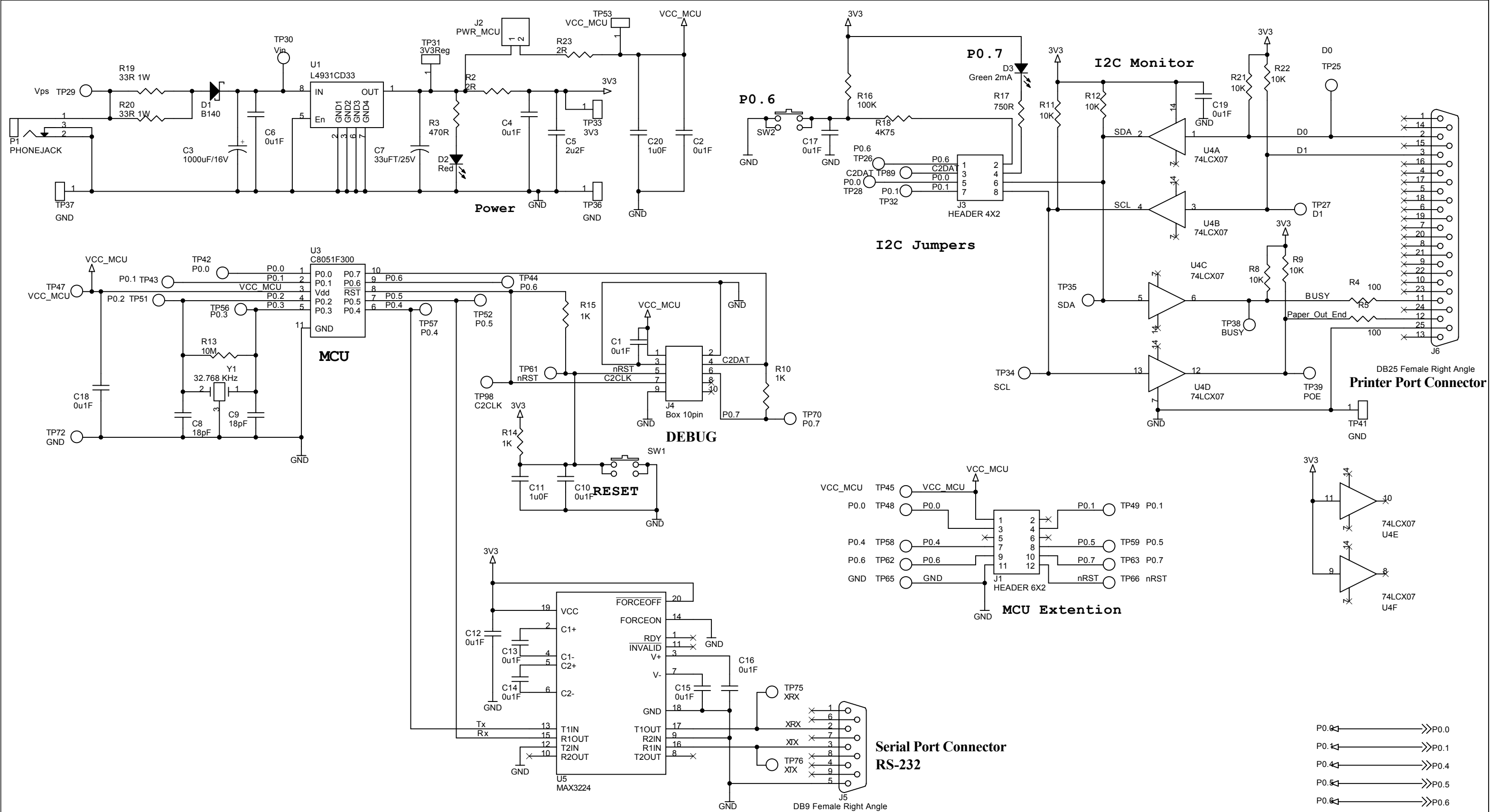
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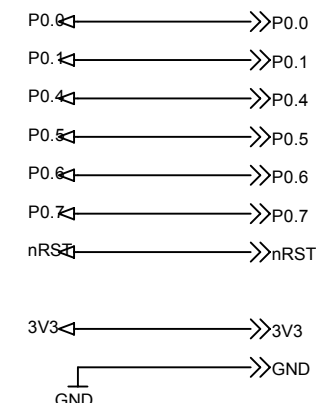
URL: www.sytron-tech.com

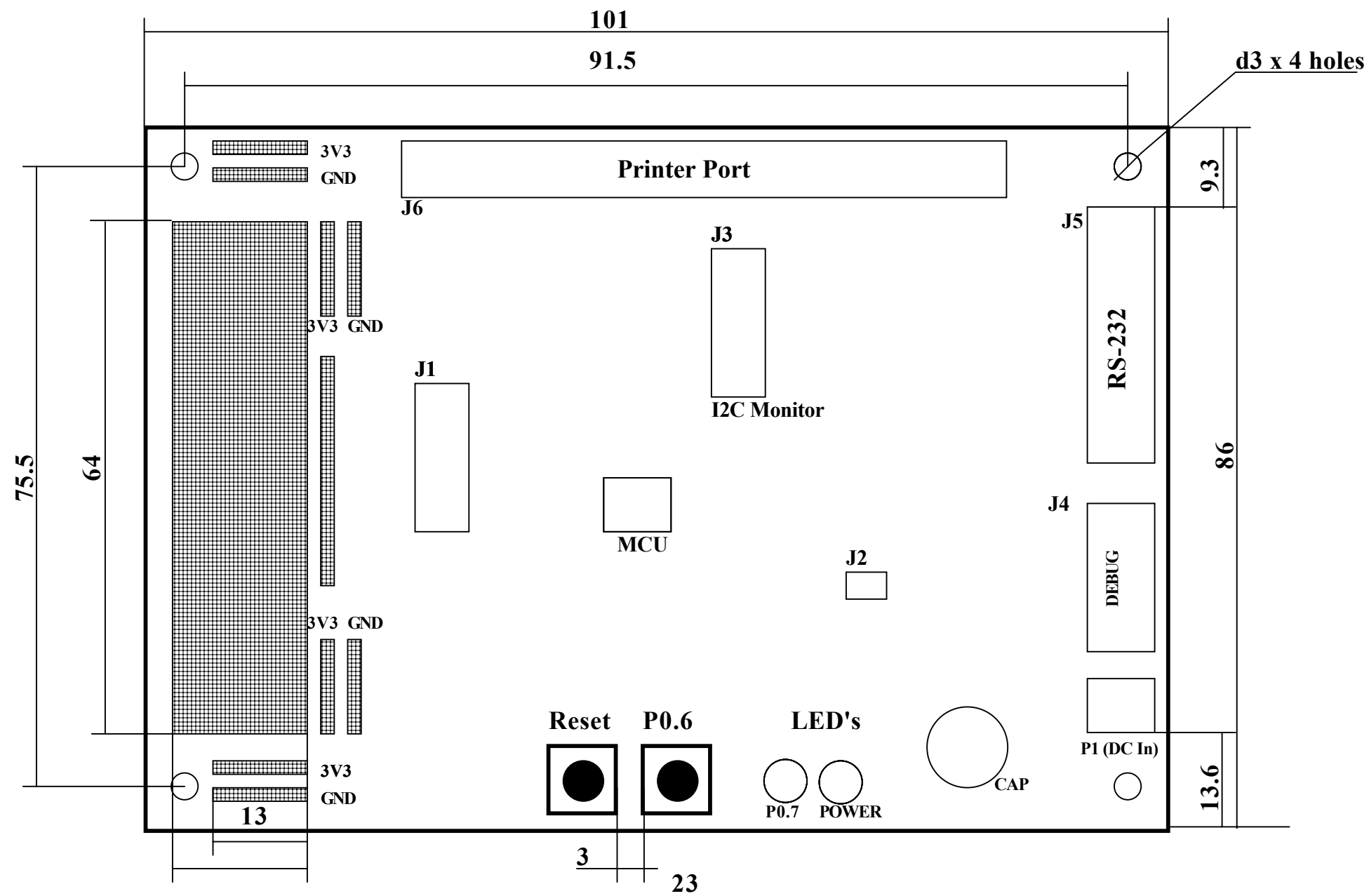
E-mail: general@sytron-tech.co.il



Note :

1. All resistors are 0.125W if another is not market on the scheme.
2. All resistors 0805 footprints if another is not market on the scheme.
3. All ceramic capacitors are X7R type and 0805 footprint if another is not market on the scheme.





P0.0 TP50 → P0.0 → P0.0

P0.1 TP55 → P0.1 → P0.1

P0.4 TP67 → P0.4 → P0.4

P0.5 TP68 → P0.5 → P0.5

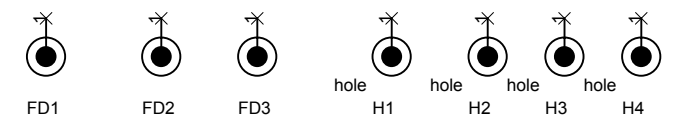
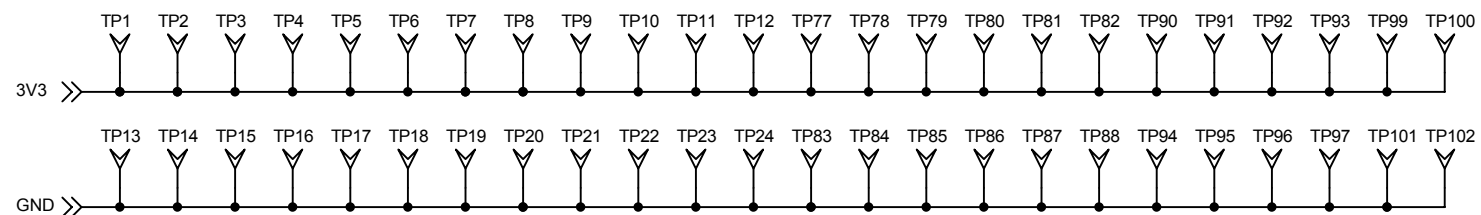
P0.6 TP69 → P0.6 → P0.6

P0.7 TP71 → P0.7 → P0.7

nRST TP74 → nRST → nRST

3V3 TP46 → 3V3

GND TP73 → GND



Note : All dimentions are in mm

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