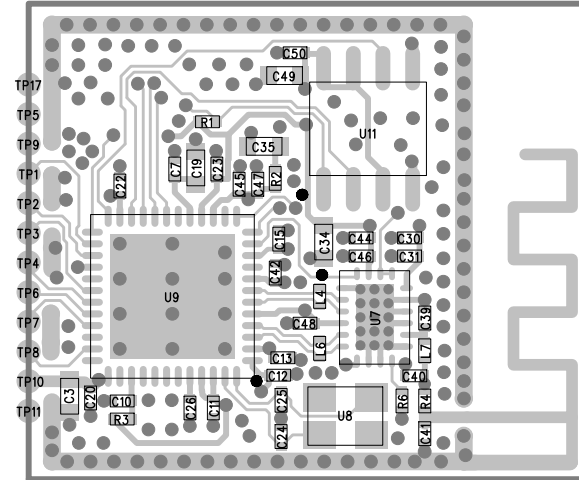
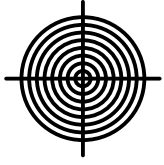
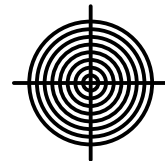
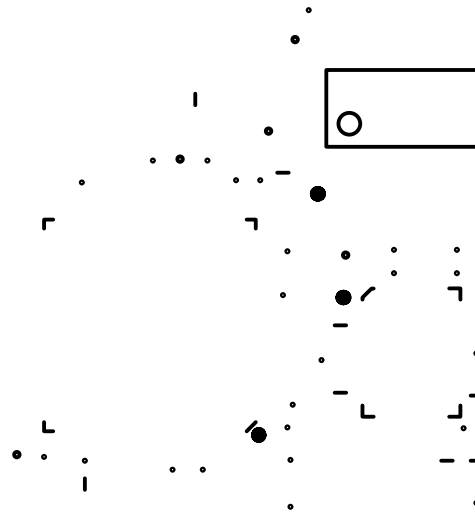
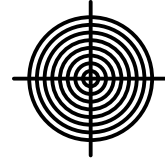


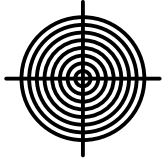
PRIMARY ASSEMBLY



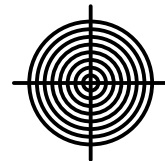
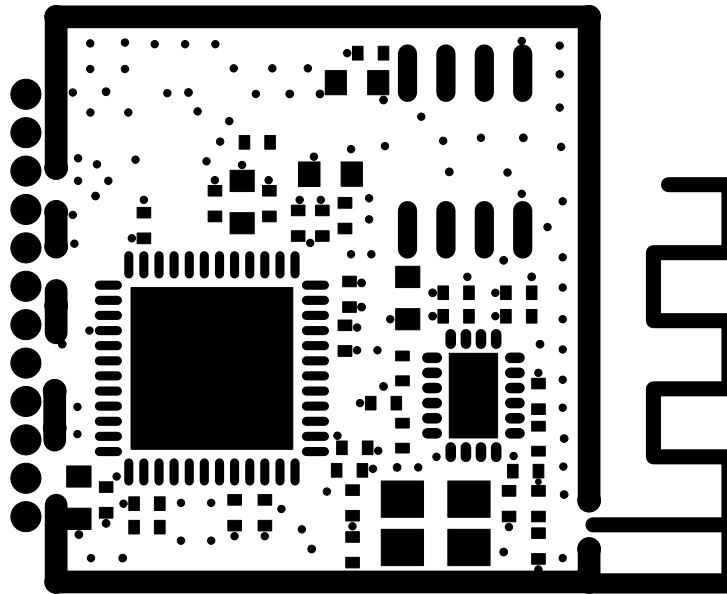
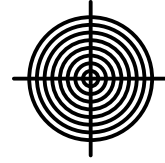


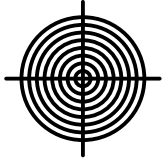
PRIMARY SILKSCREEN



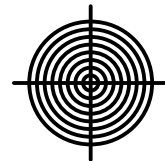
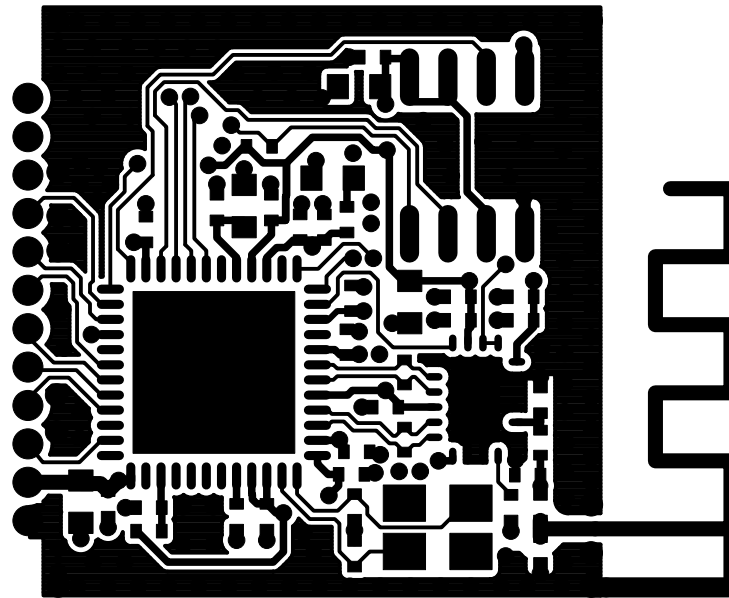
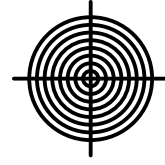


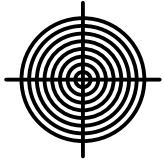
PRIMARY SOLDER MASK



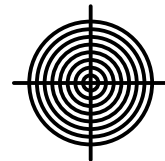
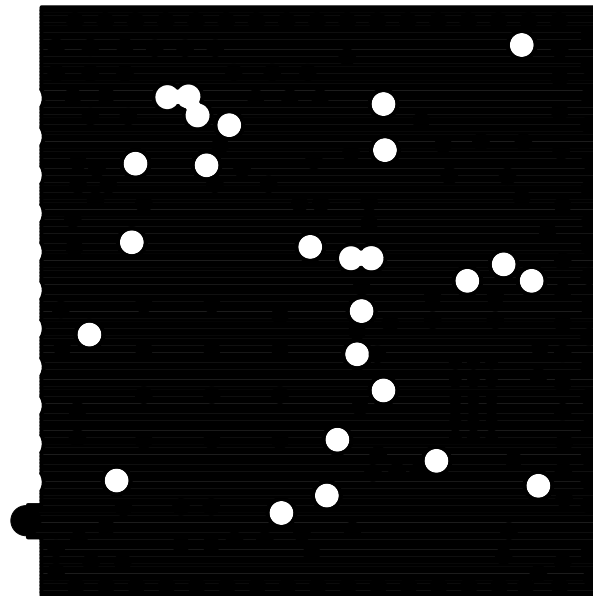
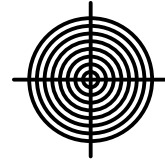


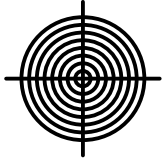
PRIMARY SIDE



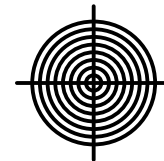
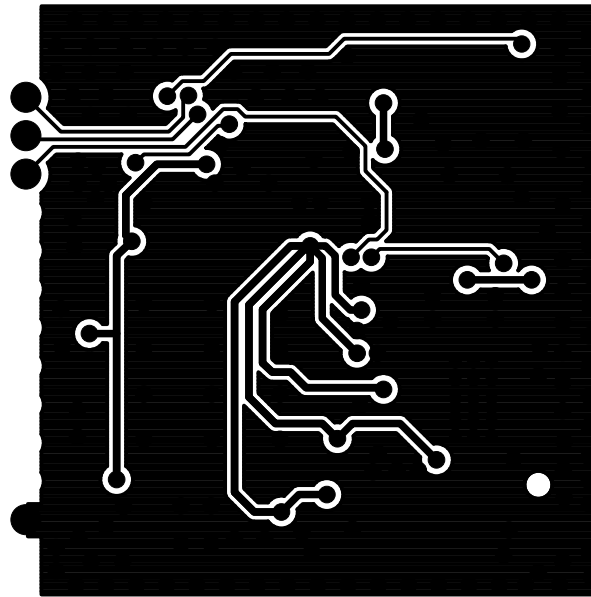
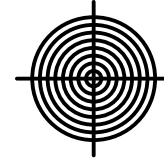


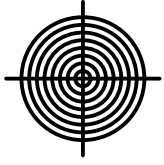
GROUND PLANE



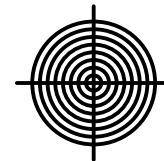
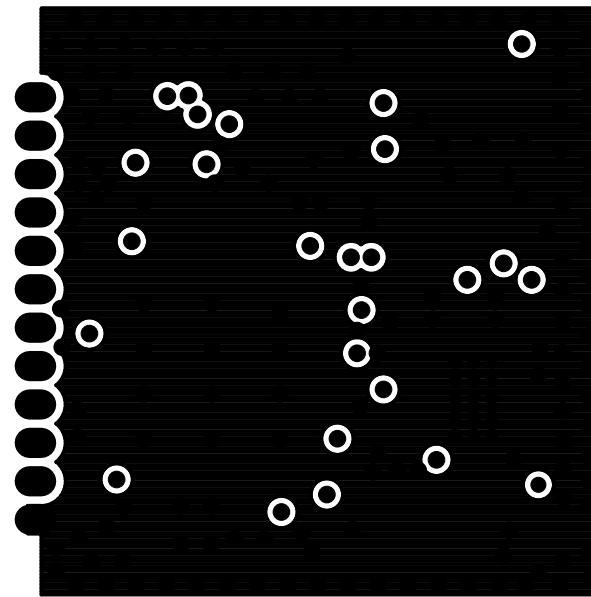
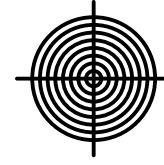


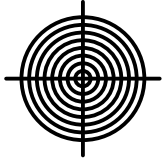
POWER PLANE



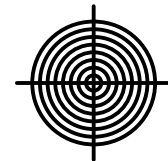
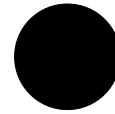
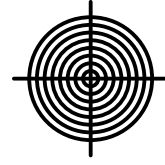


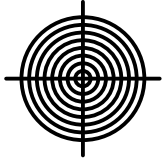
SECONDARY SIDE



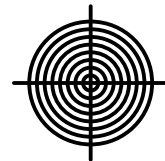
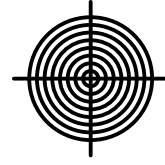


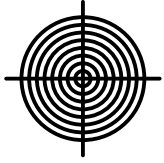
SECONDARY SOLDER MASK



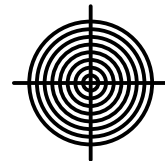
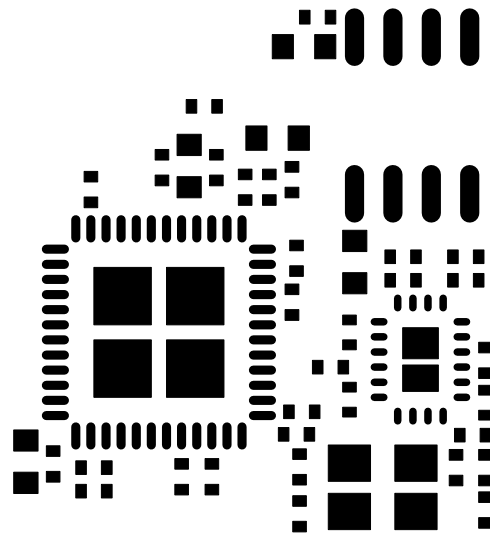
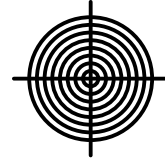


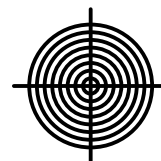
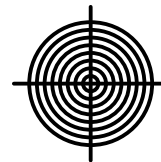
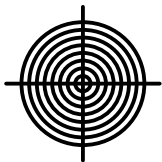
SECONDARY SILKSCREEN

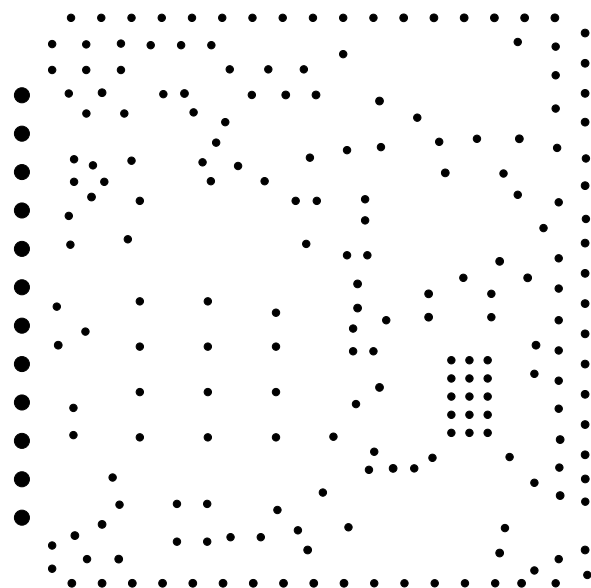


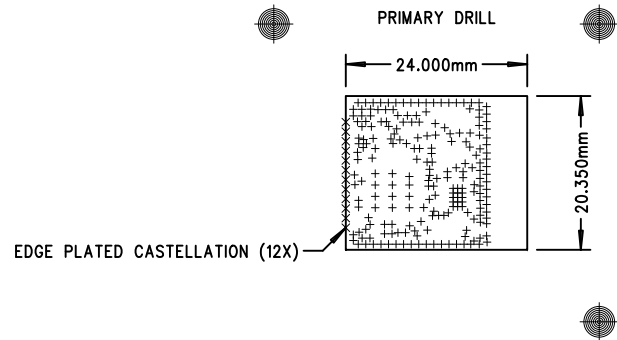


PRIMARY SOLDER PASTE









NOTES : UNLESS OTHERWISE SPECIFIED

1. MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
2. END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
3. MATERIAL SHALL BE COPPER CLAD FR-4, NEMA GRADE PER IPC-4101/26, COLOR NATURAL.
4. COPPER WEIGHT SHALL BE 0.5 OZ./SQ. FT. BEFORE PLATING.
5. ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
6. DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ± 0.003 ".
7. MINIMUM ANNULAR RING SHALL BE 0.001".
8. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
9. FINAL PCB THICKNESS SHALL BE 0.031" $\pm 10\%$.
10. WARP/TWIST SHALL NOT EXCEED 1.0%
11. FINISH SHALL BE LPI, BLACK SMOBC, ENIG BOTH SIDES.
12. SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
13. REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT



LAYER STACKUP

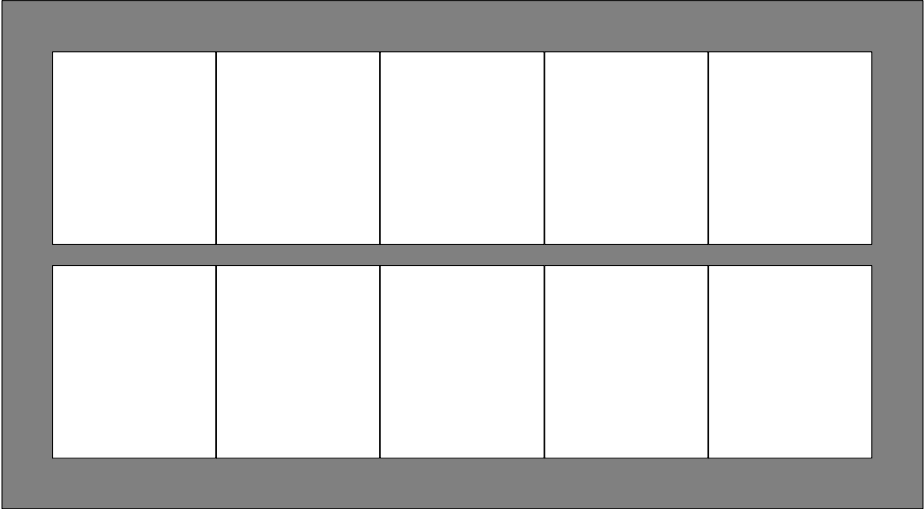
FILE NAMES

PRIMARY SILKSCREEN	A20_PSS.PHO
PRIMARY SOLDERMASK	A20_PSM.PHO
PRIMARY SIDE	A20_PRI.PHO
GROUND PLANE	A20_L02.PHO
GROUND/ROUTE	A20_L03.PHO
SECONDARY SIDE	A20_SEC.PHO
SECONDARY SOLDERMASK	A20_SSM.PHO

SCALE: NONE

SIZE	QTY	SYM	PLT	TOOL	TOL
0.25	206	+	P	1	+0/-0.25
0.50	12	X	P	2	+0/-0.50

UNLESS OTHERWISE SPECIFIED			THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT THE WRITTEN CONSENT OF SILICON LABORATORIES, INC..			COMPANY:  400 W Cesar Chavez AUSTIN, TX 78701 (512)416-8500 www.silabs.com		
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-1000			TOLERANCES			NAME: IST-A20		
HOLE TOLERANCES PER 78027			DESIGN			SIZE		
DECIMALS .XX +/- .XXX +/-	ANGLES +/-	SURFACES MICROINCHES 	CD	26APR2015	LAYOUT	CT	27APR2015	REV : 3.0
PART TO BE FREE OF BURRS			DO NOT SCALE DRAWING			PART NUMBER:		
BREAK EDGES MAX	BEND RADIUS MAX	BEND RELIEF MAX	SCALE 1:1			FABRICATION DRAWING		
						SHEET 1 OF 1		



IST-A20 REV 3.0
Size:
Array: 4.505906 x 2.489764
Part: 0.801181 x 0.944882
Parts On Array:
10 Parts
Matrix:
5 x 2
Spacing:
0.0 x 0.1
Array Borders:
Left: 0.25 Right: 0.25
Top: 0.25 Bottom: 0.25

Notes:

Please add 4, 0.125" NP tooling holes located 0.125" from tab corners and 3, 40/120 fiduials to each side of array located 0.25" from tooling holes.