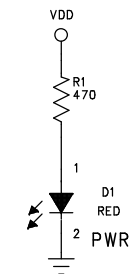
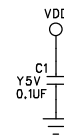
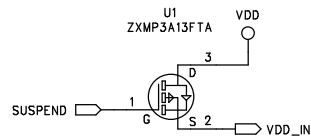
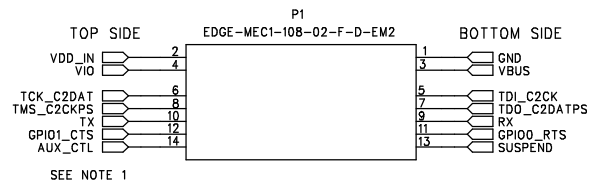



CARD EDGE CONNECTOR



NOTES:

1. THE VIO PIN MUST BE CONNECTED TO THE SAME VOLTAGE AS THE TARGET MCU'S PORT I/O VOLTAGE. TYPICALLY DONE BY CONNECTING PIN 2 AND PIN 4.
2. TYPICAL VOLTAGES:
VDD - 3.3V, CAN SUPPLY UP TO 70mA
VBUS - 5.0V, CAN SUPPLY UP TO 150mA.

		SILICON LABS	
TITLE:		TOOLSTICK Template DB	
PART NUMBER:		SA-TSxxxPCB-000	
DRAWN BY:	PAUL BARRIOS	ENGINEER:	ENGINEER
DATE:	03/30/2009	SIZE:	B
		SHEET:	1 OF 1