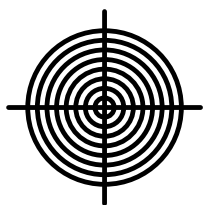
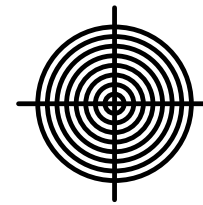
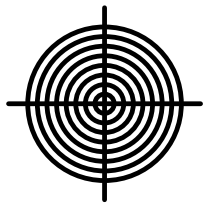
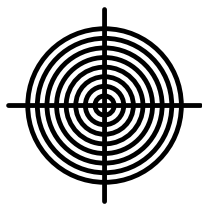
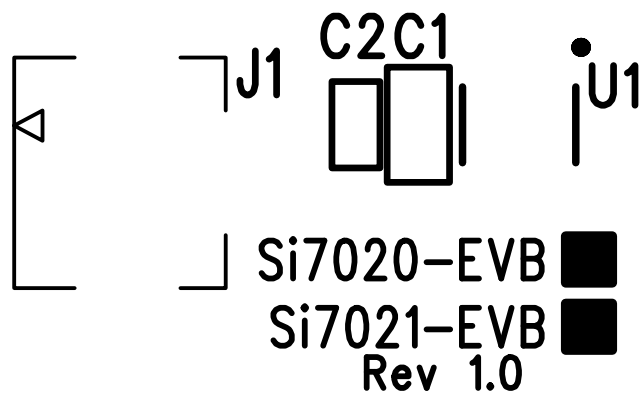
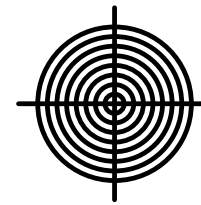
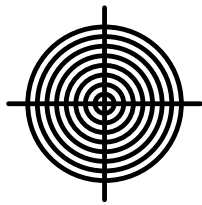


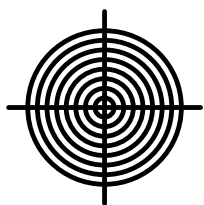
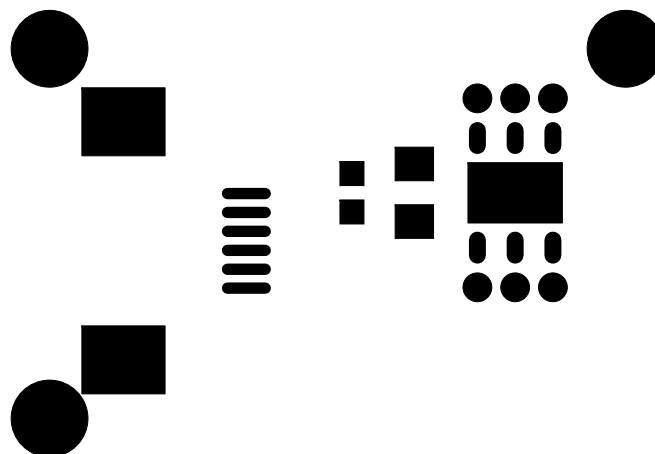
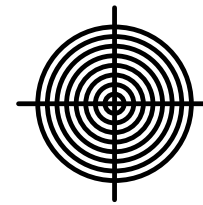
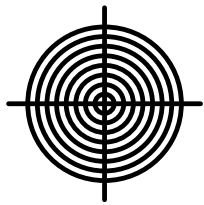
PRIMARY SILKSCREEN



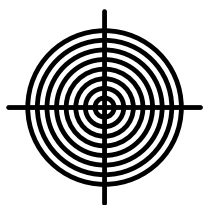
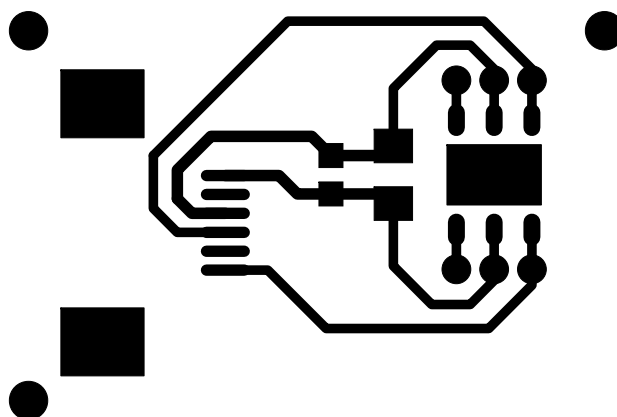
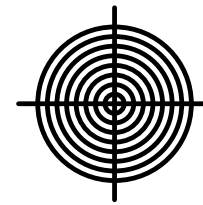
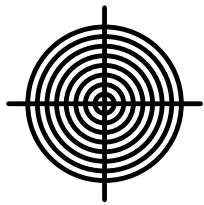
SECONDARY SILKSCREEN



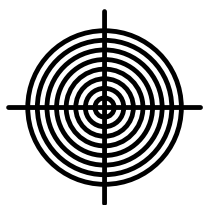
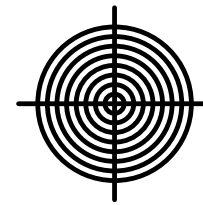
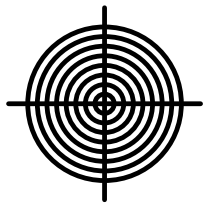
PRIMARY SILKSCREEN



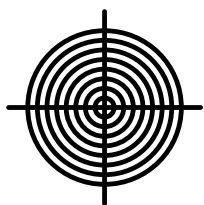
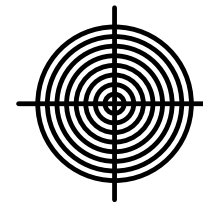
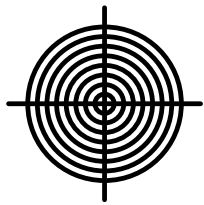
PRIMARY SOLDER MASK



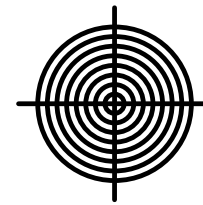
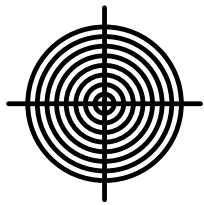
PRIMARY SIDE



SECONDARY SIDE

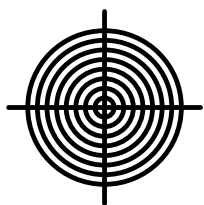


SECONDARY SOLDER MASK

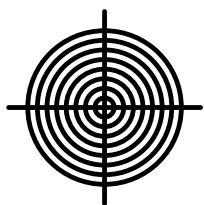
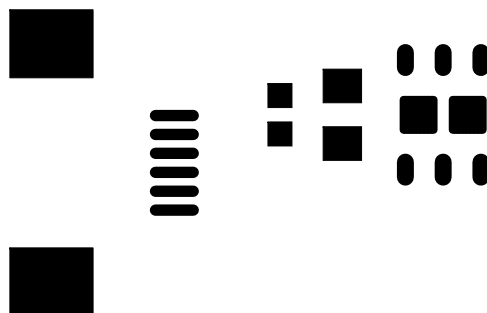
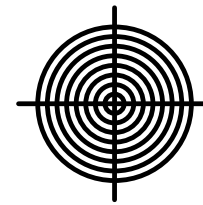
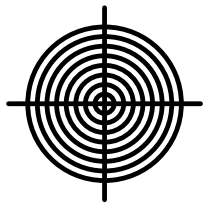


AD2
QND

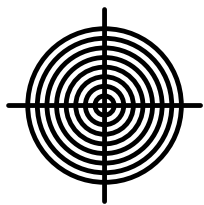
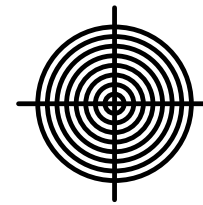
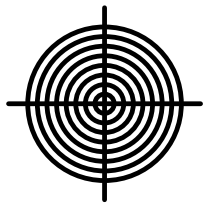
JC2
QDV



SECONDARY SILKSCREEN



PRIMARY SOLDER PASTE



SECONDARY SOLDER PASTE



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NOTES : UNLESS OTHERWISE SPECIFIED

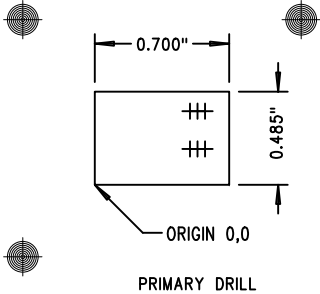
- 1. MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
- 2. END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
- 3. LAMINATE AND PREPREG SHALL BE AS PER IPC-4101/26,83,98 WITH A DECOMPOSITION TEMPERATURE >= 345°C, COLOR, NATURAL.
- 4. COPPER WEIGHT SHALL BE 1.0 OZ./SQ. FT. BEFORE PLATING.
- 5. ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
- 6. DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ±0.003".
- 7. MINIMUM ANNULAR RING SHALL BE 0.001".
- 8. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
- 9. FINAL PCB THICKNESS SHALL BE 0.062" ±10%.
- 10. WARP/TWIST SHALL NOT EXCEED 1.0%
- 11. FINISH SHALL BE LPI, GREEN S.M.O.B.C., BALANCE ENIG.
- 12. SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
- 13. REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT


LAYER STACKUP FILE NAMES

PRIMARY SILKSCREEN	Si702x_PSS.PHO
PRIMARY SOLDERMASK	Si702x_PSM.PHO
PRIMARY SIDE	Si702x_PRI.PHO
SECONDARY SIDE	Si702x_SEC.PHO
SECONDARY SOLDERMASK	Si702x_SSM.PHO
SECONDARY SILKSCREEN	Si702x_SSS.PHO

SCALE: NONE

SIZE	QTY	SYM	PLT	TOOL	TOL
0.015	6	+	P	1	+0/-0.015



UNLESS OTHERWISE SPECIFIED				COMPANY:	
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-1000				 400 W Cesar Chavez AUSTIN, TX 78701 (512)416-8500 www.silabs.com	
TOLERANCES				NAME:	
HOLE TOLERANCES PER 78027				Si702x-EVB	
DECIMALS .XX +/- .XXX +/-	ANGLES +/-	SURFACES MICROINCHES	DESIGN JG 25FEB2013	SIZE A	REV : 1.0
PART TO BE FREE OF BURRS				PART NUMBER: .	
BREAK EDGES MAX	BEND RADIUS MAX	BEND RELIEF MAX	LAYOUT AA 25FEB2013	DO NOT SCALE DRAWING	
SCALE 1:1		FABRICATION DRAWING			SHEET 1 OF 1