

1. Pin Descriptions

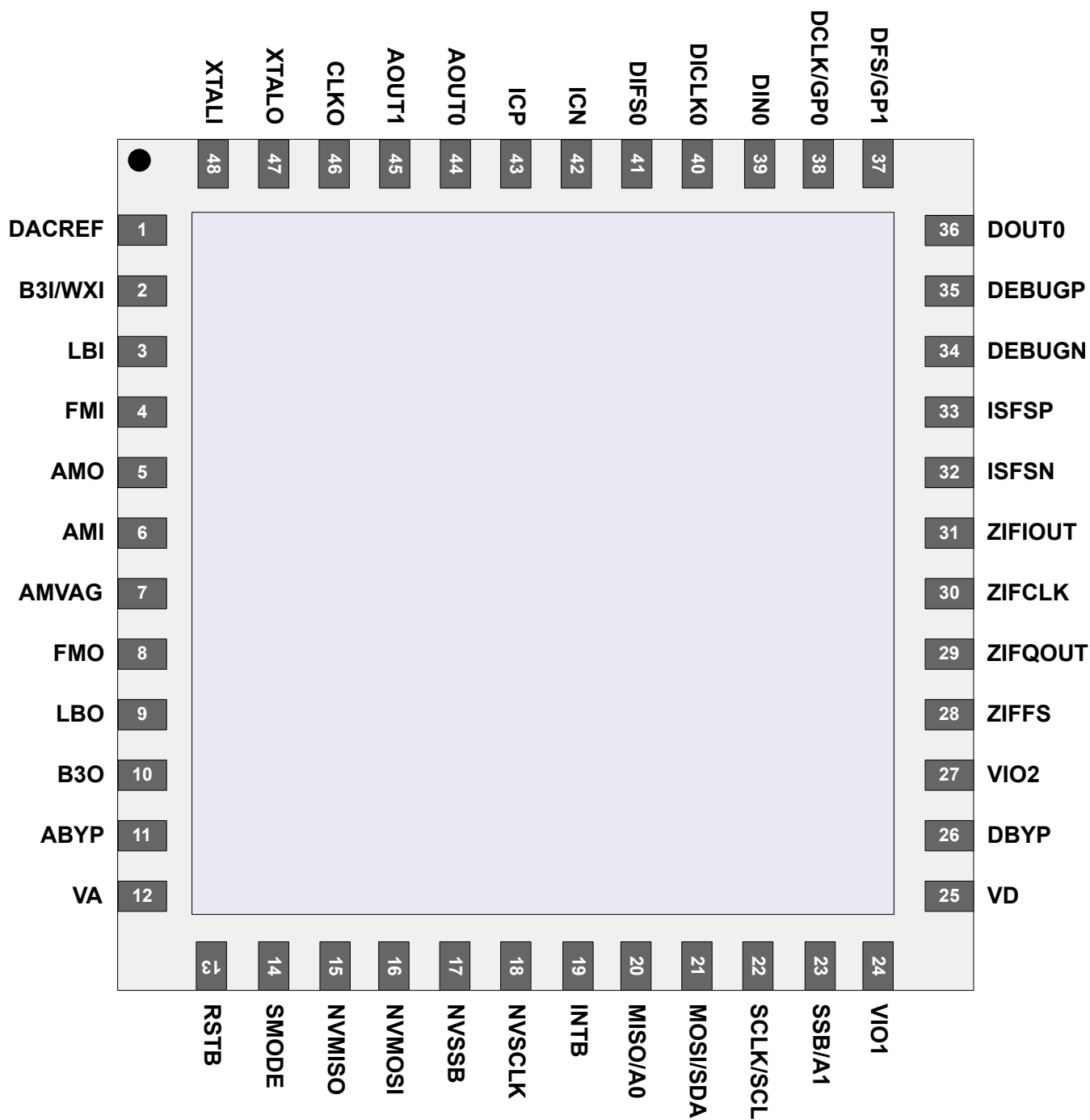


Figure 1.1. Si47901-02 Pinout Diagram

2. Package Outline

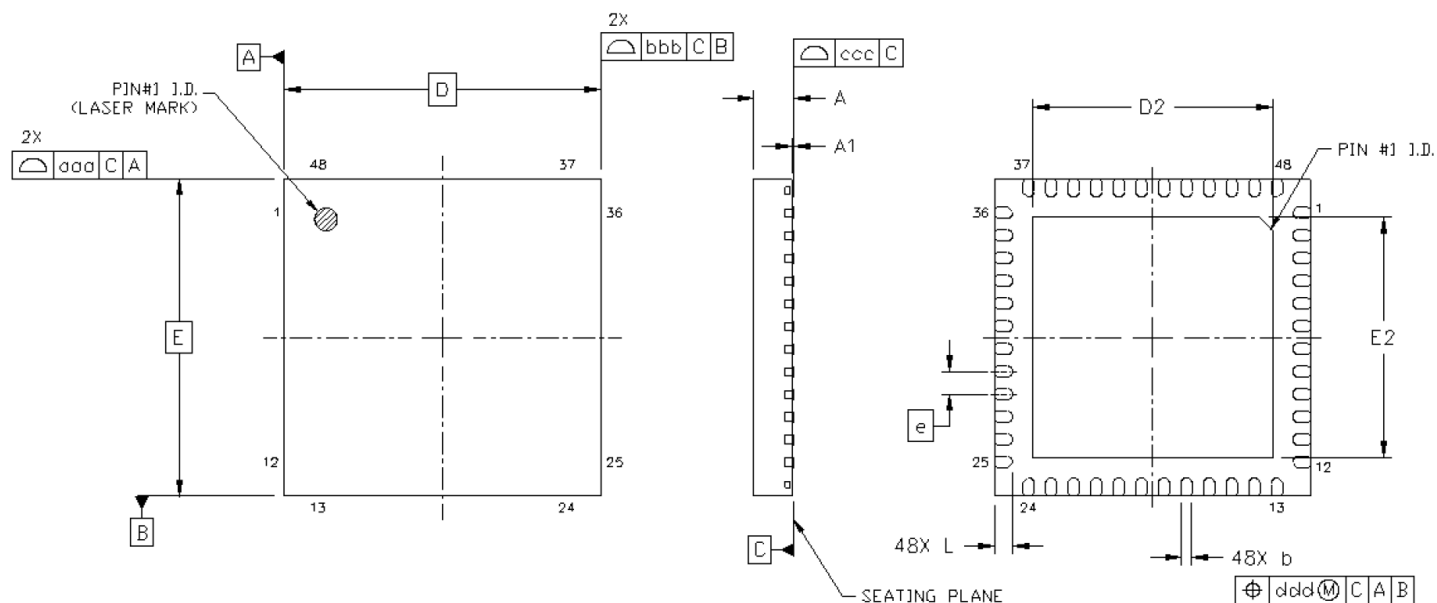


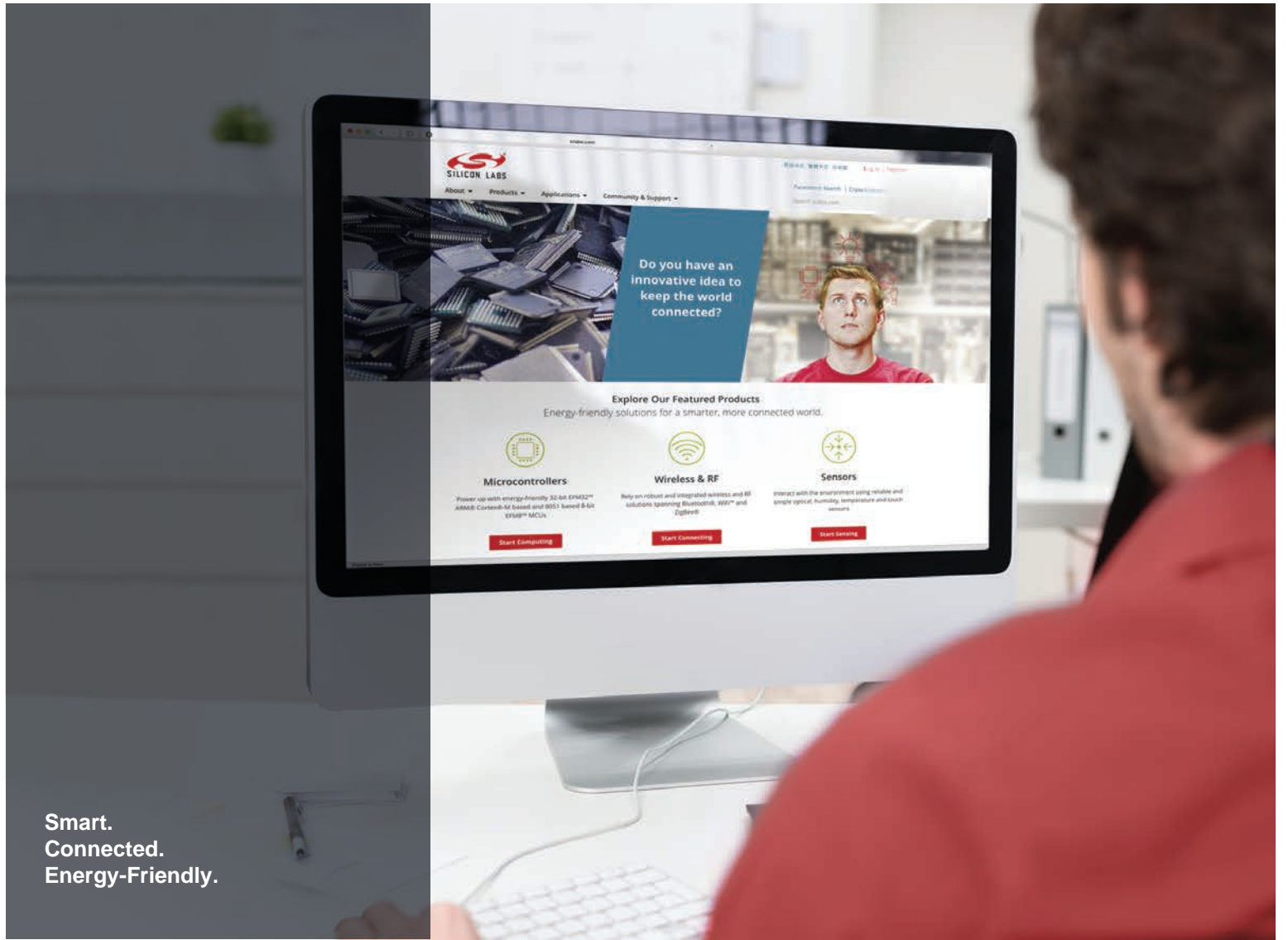
Figure 2.1. 48-Pin QFN

Table 2.1. Package Dimensions

Dimensions	Min	Nom	Max
A	0.80	0.85	0.90
A1	0.00	0.03	0.05
b	0.20	0.25	0.30
D	7.00 BSC		
D2	5.20	5.30	5.40
e	0.50 BSC		
E	7.00 BSC.		
E2	5.20	5.30	5.40
L	0.35	0.40	0.45
aaa	—	—	0.10
bbb	—	—	0.10
ccc	—	—	0.08
ddd	—	—	0.10

Note:

1. All dimensions shown are in millimeters (mm) unless otherwise noted.
2. Dimensioning and Tolerancing per ANSI Y14.5M-1994.
3. This drawing conforms to JEDEC outline MO-220, Variation VJJD-2.
4. Recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components.



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