This document contains information on the errata of product revision D of Si5380.

The device data sheet explains how to identify product revision, either from package marking or electronically.

Errata effective date: 29 July 2016.

Note: This document applies to Ordering Part Numbers (OPNs) which refer to product revision D. For example: Si5345A-D-GM or Si5345-Dxxxxx-GM, where xxxxx is the custom OPN ID, and D refers to the product revision.

### Table 1. Errata Status Summary

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<td>INTRb pin activity while RSTb pin held low</td>
<td>Information</td>
<td>Yes</td>
<td>Will be fixed in a future silicon revision.</td>
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1. INTRb_pin_activity_while_RSTb_pin_held_low

Description
The INTRb pin voltage varies at approximately 5 MHz while the RSTb reset input pin is held low (active).

Impact
Activity on the INTRb pin while RSTb is held low may appear as an interrupt to the system.

Note: The INTRb pin is driven low (active) when exiting the reset state.

Workaround
The INTRb signal can be ignored or masked by the system until RSTb is released or driven high.

Resolution
This erratum will be fixed in the next silicon revision.
2. Document History

v1.0: July 2016

- Initial release of this document for Si5380-D errata, documented separately from other Si534x family members.
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