

SiWN917 NCP Errata



This document contains information on the SiWN917 NCP Errata. The latest available revision of this device is revision B. Errata that has been resolved remain documented and can be referenced for previous revisions of this device. The device datasheet explains how to identify the chip revision from the package marking.

Errata effective date: December 3rd, 2024.

Errata Summary

Designator	Title/Problem	Workarounds	Exists on Revision A	Exists on Revision B
OSC_32kHz_E321	Recommendation for External XTAL as mandatory requirement.	No	X	X

1. OSC_32kHz_321: Recommendation for an External XTAL as Mandatory Requirement

Description of Errata:

The SiWN917 datasheet 0.7 and earlier revision provide options for two types of external clock, an external 32 kHz oscillator or an external 32 kHz XTAL. In future revisions of the datasheet, an external 32 kHz XTAL will be mandatory for Wi-Fi, BLE, and Coex power saving use cases.

Affected Condition/Impacts:

Wireless power consumption will be impacted.

Workaround

No available workaround.

Resolution

Hardware Design Change- Use an external 32 kHz XTAL for high timing accuracy use case on pins XTAL_32KHz_P and XTAL_32KHz_N.

2. Revision History

Revision 0.1

December, 2024

- Initial release.

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