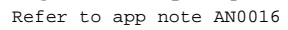


Refer to app note AN0002



Pin configuration diagram for the P1 header. The header has 20 pins. Pin 1 is VMCU. Pin 2 is GND. Pins 3-6 are unconnected. Pin 7 is DBG\_SWCLK. Pin 8 is GND. Pins 9-10 are DBG\_SWDIO. Pin 11 is GND. Pin 12 is MCUBDBG\_RESET. Pins 13-14 are unconnected. Pin 15 is DBG\_SWO. Pin 16 is GND. Pins 17-18 are unconnected. Pin 19 is HEADER\_2X10\_2.54MM\_TH. Pin 20 is GND.