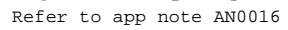


Refer to app note AN0002



Pin configuration diagram for the P1 header. The header is a 2x10 pin connector. Pin 1 is VMCU. Pin 2 is GND. Pins 3-4 are DBG_SWDIO. Pins 5-6 are DBG_SWCLK. Pins 7-8 are DBG_SWO. Pins 9-10 are MCUDBG_#RESET. Pins 11-12 are GND. Pins 13-14 are GND. Pins 15-16 are GND. Pins 17-18 are GND. Pins 19-20 are GND. The header is labeled P1 and has a label HEADER_2X10_2.54MM_TH below it.