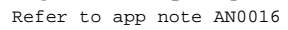


Refer to app note AN0002



Pin configuration diagram for the P1 header. The header is a 2x20 pin connector. The connections are as follows:

- Pin 1: VMCU
- Pins 2-4: Unconnected
- Pins 5-8: Unconnected
- Pins 9-10: DBG\_SWCLK
- Pins 11-12: Unconnected
- Pins 13-14: DBG\_SWO
- Pins 15-16: Unconnected
- Pins 17-18: MCUDBG\_#RESET
- Pins 19-20: HEADER\_2X10\_2.54MM\_TH

A GND symbol is shown at the bottom right.