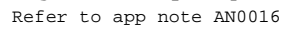


Refer to app note AN0002



Pin 19 connection diagram for the P1 header. The diagram shows a 20-pin header labeled P1. Pin 19 is connected to GND. Other pins are connected to various signals: Pin 1 to VMCU, Pin 2 to DBG\_SWIO, Pin 3 to DBG\_SWCLK, Pin 4 to DBG\_SWO, Pin 5 to MCUDBG\_#RESET, and Pin 6 to HEADER\_2X10\_2.54MM\_TH. Pins 7 through 18 are shown but not connected to any signal.