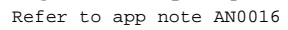


Refer to app note AN0002



Pin configuration diagram for the P1 header. The diagram shows a 2x10 pin header with pins numbered 1 to 20. Pin 1 is labeled VMCU. Pins 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20 are labeled with their respective functions: DBG_SWDIO, DBG_SWCLK, DBG_SWO, MCUDBG_#RESET, and HEADER_2X10_2.54MM_TH. The GND pin is indicated by a triangle symbol.