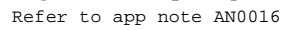


Refer to app note AN0002



Pin configuration diagram for the P1 header. The header is a 2x10 pin connector. Pin 1 is VMCU (red arrow). Pin 2 is GND (red arrow). Pins 3-4 are DBG\_SWIO (red arrow). Pins 5-6 are DBG\_SWCLK (red arrow). Pins 7-8 are DBG\_SWO (red arrow). Pins 9-10 are MCUDBG\_#RESET (red arrow). Pins 11-12 are GND (red arrow). Pins 13-14 are GND (red arrow). Pins 15-16 are GND (red arrow). Pins 17-18 are GND (red arrow). Pins 19-20 are GND (red arrow). The header is labeled P1 and HEADER\_2X10\_2.54MM\_TH.