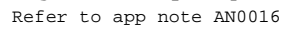


Refer to app note AN0002



Pin 1 assignment diagram for the P1 header. The diagram shows a 20-pin header labeled P1. Pin 1 is connected to VMCU. Pins 2 through 19 are connected to the VMCU header. Pin 20 is connected to GND. The connections are as follows: Pin 1 to VMCU, Pin 2 to VMCU, Pin 3 to VMCU, Pin 4 to VMCU, Pin 5 to VMCU, Pin 6 to VMCU, Pin 7 to VMCU, Pin 8 to VMCU, Pin 9 to VMCU, Pin 10 to VMCU, Pin 11 to VMCU, Pin 12 to VMCU, Pin 13 to VMCU, Pin 14 to VMCU, Pin 15 to VMCU, Pin 16 to VMCU, Pin 17 to VMCU, Pin 18 to VMCU, Pin 19 to VMCU, and Pin 20 to GND.