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| Bulletin Date:    | 3/18/2013                                    |        | Bulletin Effectiv | e Date: | 3/21/2013 |
|-------------------|--|--------|-------------------|---------|-----------|
| Title:            | SiM3U1xx/SiM3C1xx Errata Update Notification |        |                   |         |           |
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|                   |  |        |                   |         |           |

## **Bulletin Details**

## Description:

An updated version of the SiM3U1xx/SiM3C1xx revision B errata is now available (SiM3U1xx-SiM3C1xxERB030513). Item H7 "Disabled VDD Monitor does not prevent flash write or erase operations" was added.

## Reason:

Devices with date codes before 1311 will execute flash write and erase operations if the VMON0 supply monitor is disabled or disabled as a reset source.

## Product Identification:

The following orderable part numbers are affected:

SiM3C167-B-GM

SiM3C167-B-GQ

SiM3C166-B-GM

SiM3C166-B-GQ

SiM3C164-B-GM

SiM3C157-B-GM

SiM3C157-B-GQ

SiM3C156-B-GM

SiM3C156-B-GQ SiM3C154-B-GM

SiM3C146-B-GM

SiM3C146-B-GO

SiM3C144-B-GM

SiM3C136-B-GM

SiM3C136-B-GQ

SiM3C134-B-GM

SiM3U167-B-GM

SiM3U167-B-GQ

SiM3U166-B-GM

SiM3U166-B-GQ

SiM3U164-B-GM

SiM3U157-B-GM

SiM3U157-B-GO

SiM3U156-B-GM

SiM3U156-B-GQ

SiM3U154-B-GM

SiM3U146-B-GM

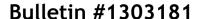
SiM3U146-B-GQ

SiM3U144-B-GM

SiM3U136-B-GM

SiM3U136-B-GQ

SiM3U134-B-GM





**Customer Actions Needed:** 

Devices with firmware modifying flash write or erase operatins using the FLASHCTRL module must ensure that the VMONO supply monitor is enabled as a reset source prior to executing the flash modification operation.

Devices with date codes later than 1311 will behave as described in revision 1.0 of the SiM3U1xx/SiM3C1xx Reference Manual. These devices will ignore flash write and erase operations if the VMON supply monitor (VMON0) is not enabled as a reset source.

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