



Revision Change Notice #17041224

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PCN Date: 4/12/2017	Effective Date: 7/14/2017												
Title: CP2112 Revision F03													
PCN Type: ☒ Datasheet ☒ Product Revision													
PCN Details													
Description of Change: Silicon Labs is pleased to announce revision F03 of the CP2112 devices and revision 1.3 of the corresponding datasheet for these products.													
Revision F03 resolves the following issues that were present in Revision F02. Please refer to the F02 errata document for additional details regarding these errata.													
<table border="1"><thead><tr><th>Errata ID</th><th>Description</th></tr></thead><tbody><tr><td>CP2112_E101</td><td>GetReadResponse may return incorrect data</td></tr><tr><td>CP2112_E103</td><td>No enumeration if I2C stuck low</td></tr><tr><td>CP2112_E104</td><td>Address Read Request with NAK</td></tr><tr><td>CP2112_E105</td><td>Occasional double START on Read request</td></tr><tr><td>CP2112_E108</td><td>Failure to enumerate</td></tr></tbody></table>		Errata ID	Description	CP2112_E101	GetReadResponse may return incorrect data	CP2112_E103	No enumeration if I2C stuck low	CP2112_E104	Address Read Request with NAK	CP2112_E105	Occasional double START on Read request	CP2112_E108	Failure to enumerate
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The datasheet revision 1.3 includes the following changes - Updated orderable part number to CP2112-F03-GM. - Updated all references from VDD to VIO when discussing minimum voltage requirements when programming the One-Time Programmable ROM. - Updated document formatting. - Removed specific operating system versions supported. - Added 3.1.6 SMBus Timing. - Added Operating Junction Temperature to Table 3.7 Absolute Maximum Ratings. - Swapped the pin number and name columns in Table 4.1 Pin Definitions for CP2112 QFN24 and reordered the rows based on pin number. Also added the GND center pad to the table. - Added 5.3 QFN24 Package Marking. - Added the Description column to Table 9.1 Default USB Configuration Data - Updated Figure 10.1 Typical Bus-Powered Connection Diagram and Figure 10.2 Typical Self-Powered Connection Diagram (Regulator Bypass) to remove an extra, erroneous GPIO. - Updated all references of CP2112_SetIDs to Xpress Configurator in Simplicity Studio. - Combined the CP2112 Interface Specification and Windows Interface DLL and 11. Relevant Application Notes and Software into one section. - Removed the Device Specific Behavior section and moved this information to the device errata history.													
After the effective date of this PCN, Silicon Labs reserves the right to deliver CP2112-F03-GM for customers ordering CP2112-F02-GM.													



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Reason for Change:

CP2112 Revision F03 release
Datasheet version 1.3 release

Impact on Form, Fit, Function, Quality, Reliability:

There is no impact to form, fit, quality, or reliability.

Product Identification:

Existing Part Number	Replacement Part Number	Drop in Compatible Indicator
CP2112-F02-GM	CP2112-F03-GM	Yes
CP2112-F02-GMR	CP2112-F03-GMR	Yes

Note: The part numbers above include tape and reel variants which are denoted with an "R" at the end of the orderable part number.

Last Date of Unchanged Product: 7/14/2017

Qualification Samples:

Samples are available now. Please contact your Silicon Labs sales representative to order samples. A list of Silicon Labs sales representatives is available at www.silabs.com.

Specific conditions of acceptance of this change will be considered on a case by case basis if written notice is submitted within 30 days of this notice. To request further data or inquire about this notification, please contact your local Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com.

In some cases rejection of a change notice may impact Silicon Labs product pricing, delivery, quality, or reliability.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: Date: _____

Name: _____

Company: _____

Email your early Acceptance approval to: katherine.hagggar@silabs.com

Qualification Data:

Qualification report is available upon request