190315471 CP2102N Product Revision with Datasheet and Errata Update

**PRCN Issue Date:** 3/15/2019  |  **Effective Date:** 6/20/2019

**PCN Type:** Datasheet; Product Revision

**Description of Change**

Silicon Labs is pleased to announce revision A02 of the CP2102N. A detailed description of new features can be found in the errata and updated datasheet.

Revision A02 of the device resolves the following issues that were present in revision A01. Please refer to the A02 errata document version 0.4 for additional details regarding these errata.

- CP2102N_E104 - Exception in .NET Applications when Manually Controlling RTS
- CP2102N_E105 - RS485 DE Signal Not Held Long Enough
- CP2102N_E106 - DTR/RTS behavior when port is closed
- CP2102N_E107 - SUSPEND and SUSPENDb state during enumeration
- CP2102N_E108 - Failure to Enumerate
- CP2102N_E109 - Bad behavior if product string is 64 bytes

The errata update also adds the following errata.

- CP2102N_E110 - USB D+/D- Power-On Reset

CP2102N revision A02 includes the following new features:
- * Send break and stop break command signalling
- * Receiver Timeout vendor command to reduce CP2102N-to-host latency

Version 1.3 of the CP2102N datasheet includes the following changes:
- * Added section describing A02 functionality for sending break signaling
- * Added section describing the receiver timeout custom vendor command
- * Changed RS485 and software handshaking sections to cover A02 behavior

The previous revision of this product, CP2102N-A01, is not recommended for new designs.

Please see the revision notes of the datasheet for more details.

**Reason for Change**

Updated device for feature enhancements and robustness improvements. Updated documentation to track those features and improvements.

**Impact on Form, Fit, Function, Quality, Reliability**

Function is impacted as described in errata. No change to form, fit, quality, or reliability.

**Product Identification**

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<th>Existing Part #</th>
<th>Replacement Part #</th>
<th>DropInCompInd.</th>
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</table>
Last Date of Unchanged Product: 6/20/2019

Qualification Samples
Samples available upon request.

Customer Response
Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

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Qualification Data
Qualification report is available upon request.
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