



210302994 Si1141/42/43 Datasheet Release v1.42

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Effective Date: 3/2/2021

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.42 for Si1141/42/43 devices.

Reason for Change

Revision 1.41 to Revision 1.42

- Added a missing ACK time period in Figure 12, "Host Interface Burst Read,".
- The description of INT operation in the "2.4. Host Interface" section is corrected.
- An ALWAYS_ACTIVE mode is added and described in the "3. Operational Modes" section.
- The Limitation of MEAS_RATE and the formula to use is corrected.
- The limitations of command write timing are explained in a new subsection in "3. Operational Modes" .
- Table 5, "Command Register Summary," on page 21 in the "4.2. Command Protocol" section contained references to an incorrect table number, which have been corrected.
- Table 15, "I2C Register Summary," on page 30 in the "4.6. I2C Registers" section has the HW_KEY register corrected. The value takes up all eight bits at register address (0x07).
- A new section, "3.7. Avoiding an Unexpected Reset", was added describing a reset that may happen if all the cautions in the other sections are not followed.
- Updated "6. Ordering Guide" on page 70.
- Removed the "Not Recommended for New Designs" watermark.

Product Identification

Existing Part #

SI1141-A11-GM

SI1141-A11-GMR

SI1142-A11-GM

SI1142-A11-GMR

SI1143-A11-GM

SI1143-A11-GMR

SI1141-A11-YM0

SI1141-A11-YM0R

SI1143-A11-YM0

SI1143-A11-YM0R

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Customer Actions Needed:

None.

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