2404171522 Shipping Media Change for Si401x from Tube to Cut Tape

PCN Issue Date: Apr 17, 2024   **Effective Date:** Jul 23, 2024

PCN Type: Packing

Description of Change

Silicon Labs is pleased to announce the change of shipping media for Si401x (10-MSOP package) products from tube to cut tape. The cut tape packing contains sealed carrier tape pockets without leader and trailer empty pockets. There is no impact for orders shipped in reel.

As a result of this change, the Minimum Order Quantity (MOQ) for Si401x will change from 250 (5 tubes) to 200 (1 cut tape).

Reason for Change

To standardize cut tape packing method for all factories

Impact on Form, Fit, Function, Quality, Reliability

There is no impact on form, fit, function, quality, and reliability.

Product Identification

```
Existing Part #
SI4010-C2003AT
SI4010-C2004AT
SI4010-C200FGT
SI4010-C200RGT
SI4010-C200YGT
SI4010-C2019GT
SI4010-C201AGT
SI4010-C201BGT
SI4010-C201LGT
SI4010-C201MGT
SI4010-C201QGT
SI4010-C201SGT
SI4010-C201TGT
SI4010-C2020GT
SI4010-C2022GT
SI4010-C2024GT
SI4010-C2025GT
SI4010-C2026GT
SI4010-C2027GT
SI4010-C2029GT
SI4010-C2-AT
SI4010-C2-GT
SI4010-C2M0UGT
SI4010-C2M20GT
SI4010-C2P02GT
SI4010-C2-ZT1
SI4011-C1001GT
SI4011-C100GGT
SI4011-C100JGT
SI4011-C100MGT
SI4011-C100NGT
SI4012-C1001AT
```

SI4012-C1001GT

Last Date of Unchanged Product: Jul 23, 2024

Qualification Samples

Samples ready upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at http://www.silabs.com.
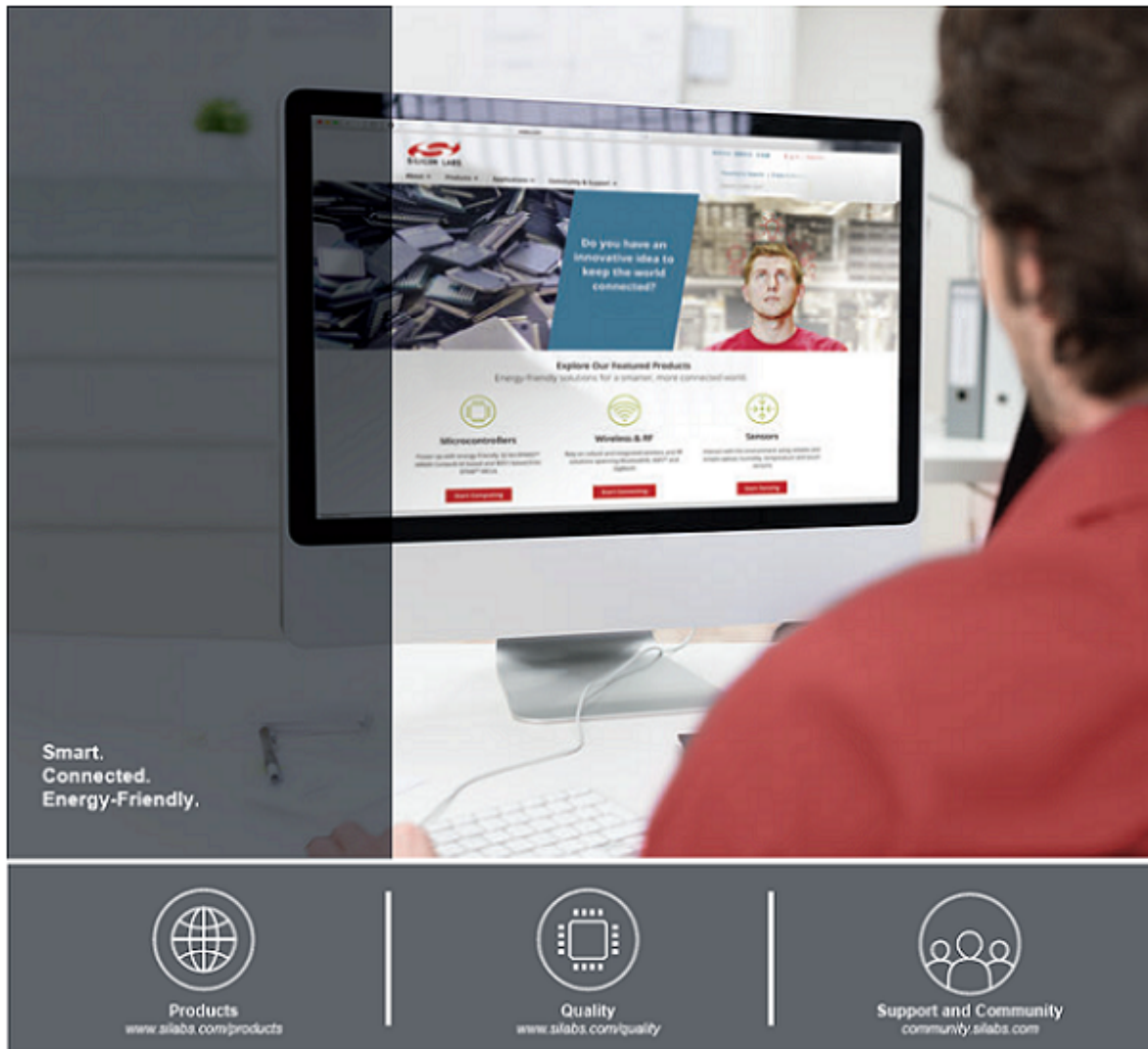
Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCN@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. **http://www.silabs.com/profile**

Qualification Data

Changes to standard cut tape packing. Qual report not applicable.

Smart.
Connected.
Energy-Friendly.

Products
www.silabs.com/products

Quality
www.silabs.com/quality

Support and Community
community.silabs.com

Disclaimer
Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information
Silicon Laboratories Inc.® , Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOmodem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.

Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

http://www.silabs.com