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Bulletin Date: 9/1/2015		Bulletin Effective Date: 9/1/2015	
Title: EFM8 Errata PCN (BB1, SB1, SB2, UB2)			
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Bulletin Details			
Description: EFM8BB1, EFM8SB1, EFM8SB2, EFM8UB2 Revision B Errata has been updated and are available at www.silabs.com .			
Reason: A "Bootloader Signature Byte" has been added to the EFM8BB1, EFM8SB1, EFM8SB2, EFM8UB2 to support a factory programmed bootloader. More details can be found in the Errata associated with the device family. - Each device will use the byte immediately before the Lock Byte as a Bootloader Signature Byte to determine if the bootloader is present in flash. - Systems using the device should not write the Bootloader Signature Byte to 0xA5 to enable normal operation.			
Product Identification: EFM8BB10F2G-A-QFN20 EFM8BB10F2G-A-QFN20R EFM8BB10F4G-A-QFN20 EFM8BB10F4G-A-QFN20R EFM8BB10F8G-A-QFN20 EFM8BB10F8G-A-QFN20R EFM8BB10F8G-A-QSOP24 EFM8BB10F8G-A-QSOP24R EFM8BB10F8G-A-SOIC16 EFM8BB10F8G-A-SOIC16R EFM8SB10F2G-A-QFN20 EFM8SB10F2G-A-QFN20R EFM8SB10F4G-A-QFN20 EFM8SB10F4G-A-QFN20R EFM8SB10F8G-A-CSP16 EFM8SB10F8G-A-CSP16R EFM8SB10F8G-A-QFN20 EFM8SB10F8G-A-QFN20R EFM8SB10F8G-A-QFN24 EFM8SB10F8G-A-QFN24R EFM8SB10F8G-A-QSOP24 EFM8SB10F8G-A-QSOP24R EFM8SB20F16G-A-QFN24 EFM8SB20F16G-A-QFN24R			

EFM8SB20F32G-A-QFN24
EFM8SB20F32G-A-QFN24R
EFM8SB20F32G-A-QFN32
EFM8SB20F32G-A-QFN32R
EFM8SB20F32G-A-QFP32
EFM8SB20F32G-A-QFP32R
EFM8SB20F64G-A-QFN24
EFM8SB20F64G-A-QFN24R
EFM8SB20F64G-A-QFN32
EFM8SB20F64G-A-QFN32R
EFM8SB20F64G-A-QFP32
EFM8SB20F64G-A-QFP32R
EFM8UB20F32G-A-QFN32
EFM8UB20F32G-A-QFN32R
EFM8UB20F32G-A-QFP32
EFM8UB20F32G-A-QFP32R
EFM8UB20F32G-A-QFP48
EFM8UB20F32G-A-QFP48R
EFM8UB20F64G-A-QFN32
EFM8UB20F64G-A-QFN32R
EFM8UB20F64G-A-QFP32
EFM8UB20F64G-A-QFP32R
EFM8UB20F64G-A-QFP48
EFM8UB20F64G-A-QFP48R

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Customer Actions Needed:

Care should be taken not to write the value 0xA5 to the "Bootloader Signature Byte" location. The value 0xA5 will be used to activate the pre-programmed bootloader.