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Bulletin Date: 6/10/2016	Bulletin Effective Date: 6/10/2016																								
Title: CP2114 Errata Update																									
Bulletin Details																									
Description: Silicon Labs is pleased to announce an errata document for CP2114 has been issued and is available at www.silabs.com																									
Reason: The CP2114 Errata for B01 includes the following errata for B01. The issues will be solved in CP2114 B02 revision. <table border="1" data-bbox="284 932 1469 1417"> <thead> <tr> <th>Errata ID</th><th>Title</th></tr> </thead> <tbody> <tr> <td>CP2114_E101</td><td>Dropouts in Asynchronous -mode playback audio on 64-bit systems</td></tr> <tr> <td>CP2114_E102</td><td>Record sample values intermittently corrupted</td></tr> <tr> <td>CP2114_E103</td><td>Playback mute-by-zero can cause record corruption</td></tr> <tr> <td>CP2114_E104</td><td>USB descriptor not correct for synchronous mode</td></tr> <tr> <td>CP2114_E105</td><td>Resume may fail on externally-clocked systems</td></tr> <tr> <td>CP2114_E106</td><td>UART is unusable if GPIO-.9 is reconfigured</td></tr> <tr> <td>CP2114_E107</td><td>Record can fail after rebooting PC</td></tr> <tr> <td>CP2114_E108</td><td>UART can deliver duplicated RX character</td></tr> <tr> <td>CP2114_E109</td><td>Failure to enumerate with unprogrammed configuration when external clock is missing</td></tr> <tr> <td>CP2114_E110</td><td>Host PC record mute icon controls CP2114 playback muting</td></tr> <tr> <td>CP2114_E111</td><td>GPIO pins are briefly set to open drain, drive low mode when entering suspend mode.</td></tr> </tbody> </table>		Errata ID	Title	CP2114_E101	Dropouts in Asynchronous -mode playback audio on 64-bit systems	CP2114_E102	Record sample values intermittently corrupted	CP2114_E103	Playback mute-by-zero can cause record corruption	CP2114_E104	USB descriptor not correct for synchronous mode	CP2114_E105	Resume may fail on externally-clocked systems	CP2114_E106	UART is unusable if GPIO-.9 is reconfigured	CP2114_E107	Record can fail after rebooting PC	CP2114_E108	UART can deliver duplicated RX character	CP2114_E109	Failure to enumerate with unprogrammed configuration when external clock is missing	CP2114_E110	Host PC record mute icon controls CP2114 playback muting	CP2114_E111	GPIO pins are briefly set to open drain, drive low mode when entering suspend mode.
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This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy. Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at www.silabs.com																									
Customer Actions Needed: Review the errata document and take corrective action if applicable.																									