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| Bulletin Date: 6/10/2016 | Bulletin Effective Date: 6/10/2016 |
| Title: CP2103 Errata Update | |
| Bulletin Details | |
| Description: Silicon Labs is pleased to announce an errata document for CP2103 rev D has been issued and is available at www.silabs.com | |
| Reason: Dynamic Suspend error is an optional feature that, when enabled, allows for GPIO pins or modem signals to assume a pre-programmed state during USB SUSPEND and then return to their pre-SUSPEND states on exit of USB SUSPEND. The potentially affected pins are RXD, CTS, DSR, DCD, RI and any GPIO pin configured as an input. If the Dynamic Suspend feature is enabled, the pin state may switch from Input mode to Output mode and drive a logic '0'. The pin will remain an output until either power is cycled or the device receives a reset from the /RST pin. Dynamic Suspend is an optional feature that affects only the customers using this feature. There is currently no planned resolution for this issue. | |
| Product Identification: CP2103-GM CP2103-GMR | |
| This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy. Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at www.silabs.com | |
| Customer Actions Needed: Review the errata document and take corrective action if applicable. Please contact your Silicon Labs sales representative if you have questions. A list of Silicon Labs representatives is available at www.silabs.com . | |