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PCN Date: 10/16/2013		Effective Date: 10/16/2013	
Title: Si4455 Datasheet PCN			
Originator: Vivek Mohan	**Phone:** 408-702-1251		**Dept:** Marketing
Customer Contact: Kathy Haggar	**Phone:** 512-532-5261		**Dept:** Sales
PCN Type:			
☒ Datasheet	☐ Foundry		☐ Packing
☐ Product Revision	☐ Assembly		☐ Labeling
☐ Discontinuance	☐ Test		☐ Other
Last Order Date: Not Applicable.			

PCN Details
Description of Change: Version 1.1 of the Si4455 datasheet is now available. This datasheet applies to Si4455-B1A-FM(R). Please see document change list at the end of this PCN for a summary of changes. The datasheet is available at www.silabs.com. If you have questions, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is also available at www.silabs.com.
Reason for Change: The Si4455 datasheet changes include improved descriptions of features and updates to spurious emissions specifications which are dependent on individual board design considerations. A summary of changes to datasheet version 1.1 can be found in Appendix A below.
Impact on Form, Fit, Function, Quality, Reliability: There is no impact to form, fit, quality or reliability of the part. There are no functional changes to existing device features. Datasheet version 1.1 describes enhanced features that are included in Appendix A below.
Product Identification: Si4455-B1A-FM
Last Date of Unchanged Product: Not Applicable.
Qualification Samples: Samples are available upon request
Customer Early Acceptance Sign Off: **Customers may approve early PCN acceptance by completing the information below:** Early Acceptance: Date: _____ Name: _____ Company: _____
Email your early Acceptance approval to: katherine.haggar@silabs.com
Qualification Data: Not Applicable.

Appendix A

Revision 1.0 to Revision 1.1

- Removed API section and updated references to EZRadio API documentation in various sections to reflect the latest revision.
- Changed references to EZConfig Setup to Radio Configuration Application (RCA) in various sections.
- Updated " Functional Block Diagram" on page 2.
  - Removed Vdd connection to PA
- Updated Table 3, "Synthesizer AC Electrical Characteristics" on page 5.
  - Removed Synthesizer settling time (covered in state transition timing in Table 12).
- Updated Table 4, "Receiver AC Electrical Characteristics" on page 6.
  - Changed Image Rejection and Spurious emissions spec
  - Added footnote to highlight that emissions are layout-dependent and not only an IC specification.
- Updated Table 5, "Transmitter AC Electrical Characteristics" on page 7.
  - Changed Emissions and Harmonics from Max to Typical specs.
  - Added footnote to highlight that emissions are layout dependent and not only an IC specification.
- Updated Table 7 on page 9.
  - Changed Rise and Fall time test conditions.
  - Updated drive strength levels.
- Updated Table 8 on page 10.
  - Updated Junction Temperature specification
- Updated "1.1. Definition of Test Conditions" on page 11.
  - Added comments
- Updated "3.2.1. Received Signal Strength Indicator" on page 14.
  - Updated RSSI description.
- Updated Figure 5 on page 18.
- Updated Figure 7 on page 21.
- Updated Table 11 on page 22.
  - Changed SDN to SDI.
- Updated "5.3. Interrupts" on page 29.
  - Added Interrupt options.
- Section "5.4. GPIO" on page 30.
  - Updated Table 14 to include SCLK and NSEL.
- Updated "6. Data Handling and Packet Handler" on page 31.
  - Added "6.3. Direct Mode" on page 31.
- Updated "7. Pin Descriptions" on page 32.
  - Updated XIN pin description and added ground paddle description.
- Updated Figure 19 on page 37 to show top view of the package.