



Revision Change Notice #1608045

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users.

<http://www.silabs.com/profile>

PCN Date: 8/4/2016	Effective Date: 11/10/2016
Title: CP2114 Revision B02	
PCN Type: <input checked="" type="checkbox"/> Datasheet <input checked="" type="checkbox"/> Product Revision	
PCN Details	

Description of Change:

Silicon Labs is pleased to announce revision B02 of CP2114 devices and revision 1.2 of the corresponding datasheet for these products.

Revision B02 resolves the following issues that were present in Revision B01. Please refer to the B01 errata document for additional details regarding these errata.

Errata ID	Title
CP2114_E101	Dropouts in Asynchronous -mode playback audio on 64-bit systems
CP2114_E102	Record sample values intermittently corrupted
CP2114_E103	Playback mute-by-zero can cause record corruption
CP2114_E104	USB descriptor not correct for synchronous mode
CP2114_E105	Resume may fail on externally-clocked systems
CP2114_E106	UART is unusable if GPIO-.9 is reconfigured
CP2114_E107	Record can fail after rebooting PC
CP2114_E108	UART can deliver duplicated RX character
CP2114_E109	Failure to enumerate with unprogrammed configuration when external clock is missing
CP2114_E110	Host PC record mute icon controls CP2114 playback muting
CP2114_E111	GPIO pins are briefly set to open drain, drive low mode when entering suspend mode.

The B02 device also provides the following features that were not present in the B01 device:

- Support for 24-bit samples in Playback-only and Record-only modes.
- Support I²C communication with other (non-codec) slave devices.
- Additional configurable parameters:
 - Asynchronous audio feedback endpoint rate (USB 'bRefresh').
 - Delay for external System Clock oscillator startup.
 - Initial settings for Playback Volume and Playback/Record Mute.
 - Playback/Record USB 'wTerminalType' (e.g. Speaker, Headphones, Line Out, etc.)
- Support for Record-only operation.
- Support codecs with 16-bit register addresses.

Datasheet revision 1.2 describes the new features of revision B02. Note that the B02 audio configuration block is a superset of the B01 audio configuration block; therefore, any existing customer-developed B01 audio configurations cannot be used as-is with the B02.

The CP2114-B01-GM is not recommended for new designs

Reason for Change:

CP2114-B02-GM release
CP2114 datasheet revision 1.2

Impact on Form, Fit, Function, Quality, Reliability:

There is no impact to form, fit, quality, or reliability. The B02 provides additional functionality described elsewhere in this document and in the B02 datasheet. Customers using CP2114-B01-GM will not need to make any changes to their application when switching to CP2114-B02-GM, unless they want to use new features only provided by the B02 (e.g. arbitrary I²C Write or Read transactions with other slave devices in the system)



Revision Change Notice #1608045

Product Identification:

Existing Part Number	Replacement Part Number	Drop in Compatible Indicator
CP2114-B01-GM	CP2114-B02-GM	See Datasheet
CP2114-B01-GMR	CP2114-B02-GMR	See Datasheet

Notes:

- The B02 is drop-in compatible only if using one of the three factory-programmed audio configurations. If using a customized audio configuration, the configuration must be adapted to the B02 format. See device datasheet for details.
- The part numbers above include tape and reel variants which are denoted with an "R" at the end of the orderable part number.

Last Date of Unchanged Product: 11/10/2016

Qualification Samples:

Samples are available now.

Specific conditions of acceptance of this change will be considered on a case by case basis if written notice is submitted within 30 days of this notice. To request further data or inquire about this notification, please contact your local Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com.

In some cases rejection of a change notice may impact Silicon Labs product pricing, delivery, quality, or reliability.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: **Date:** _____

Name: _____

Company: _____

Email your early Acceptance approval to: katherine.hagggar@silabs.com

Qualification Data:

Qualification report is available upon request