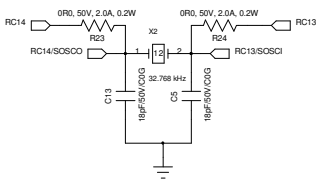
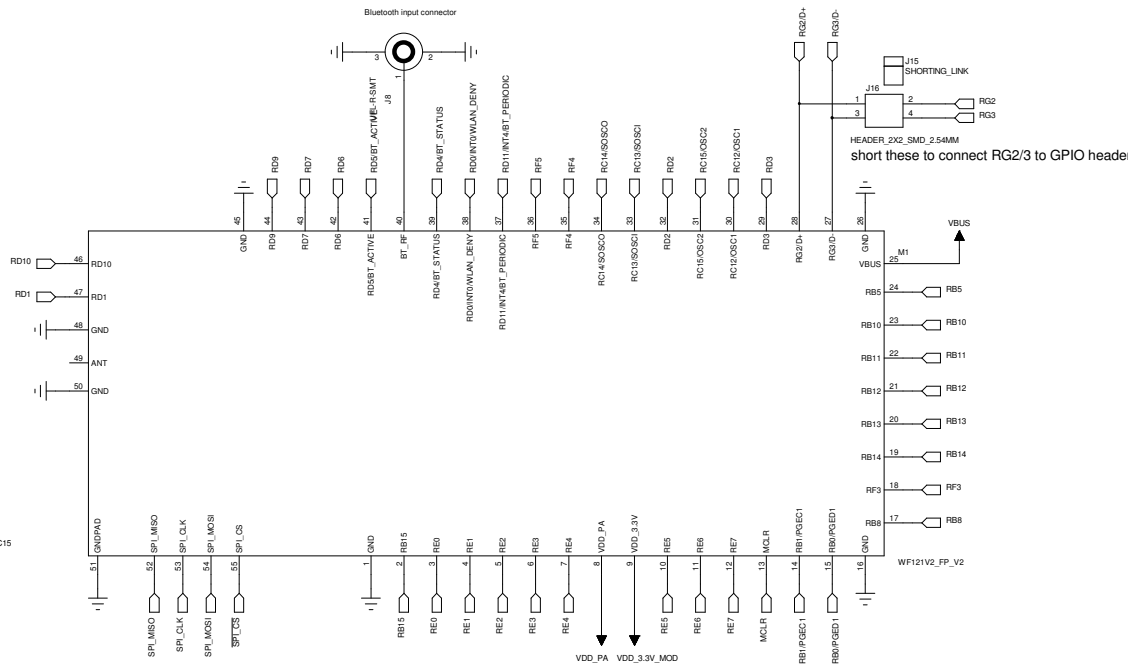
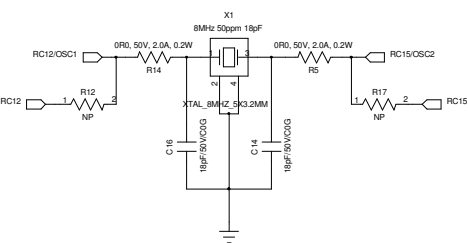
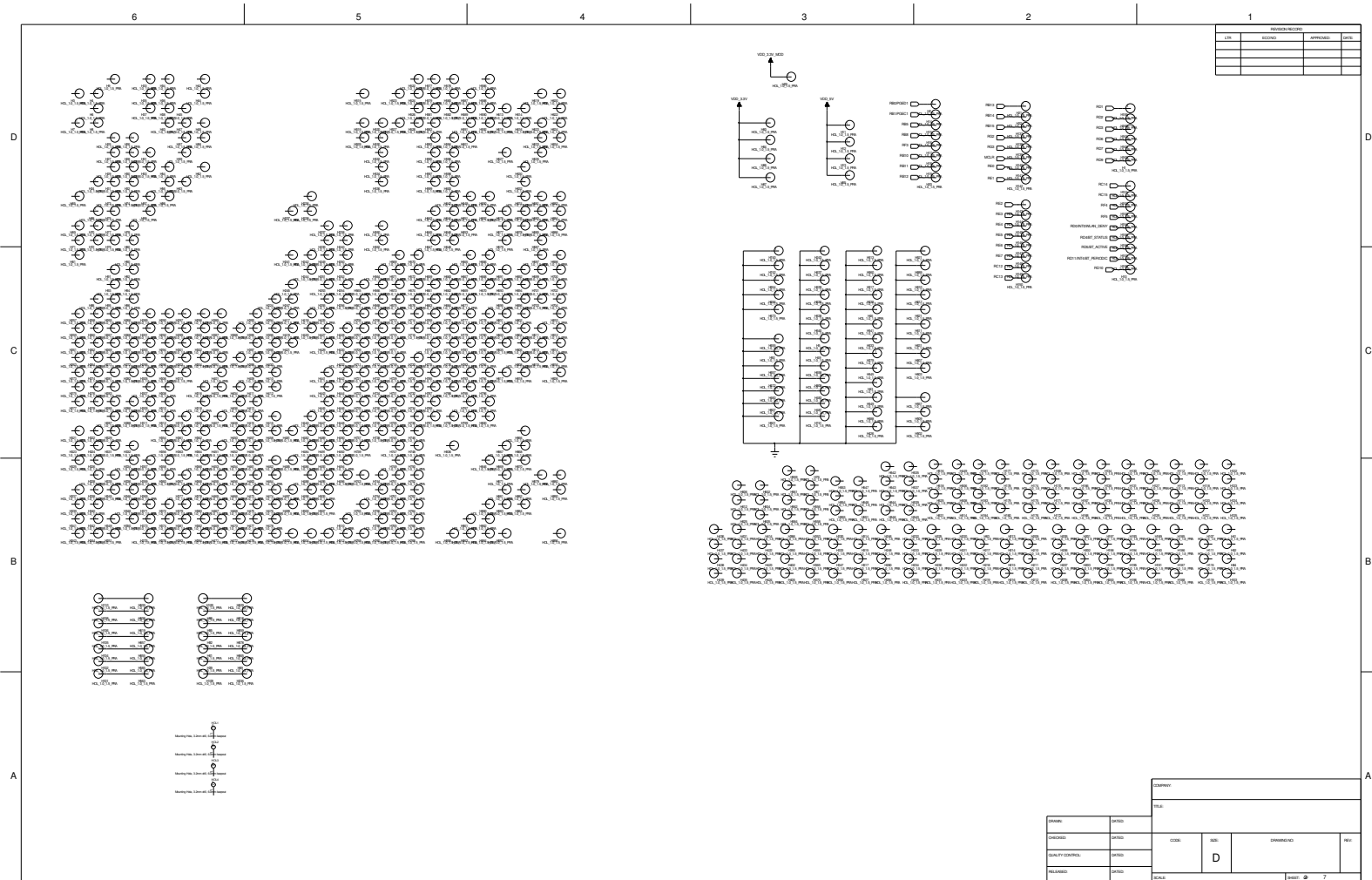


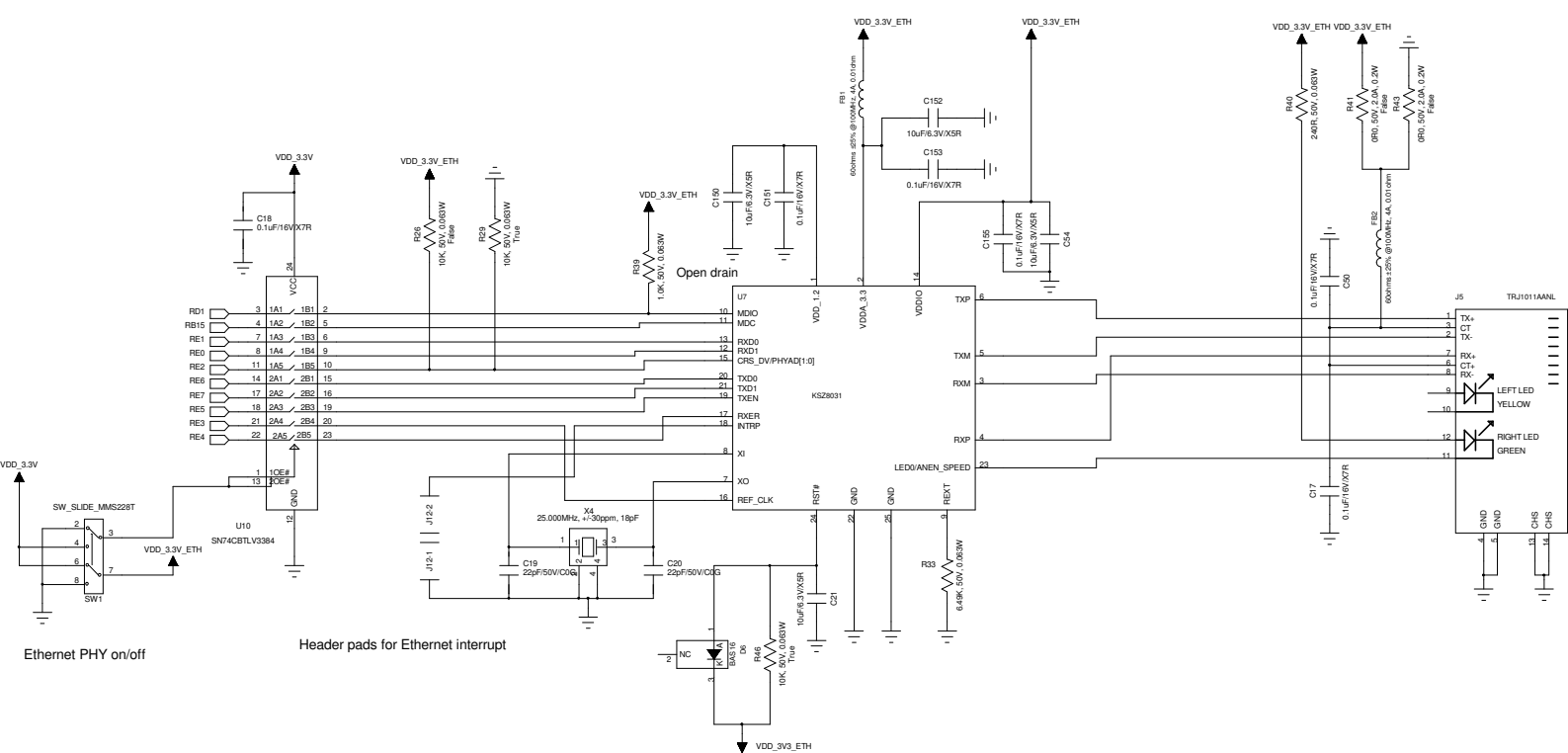
if pins are needed for GPIO, remove crystal and add 0ohm resistors

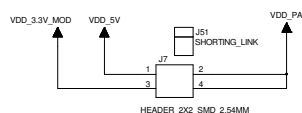
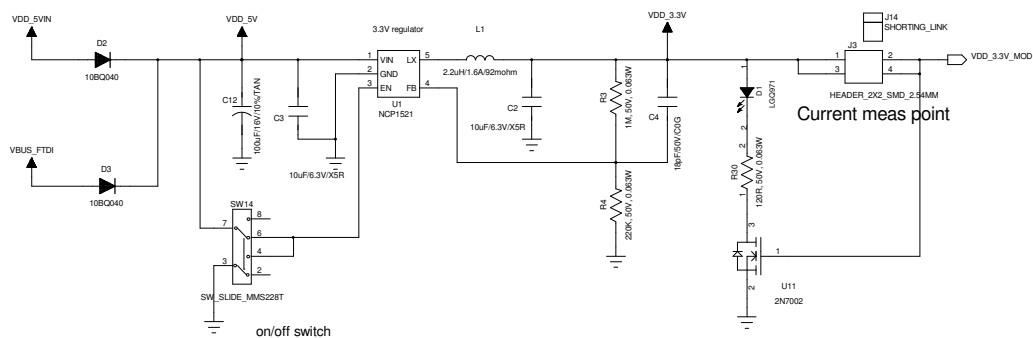
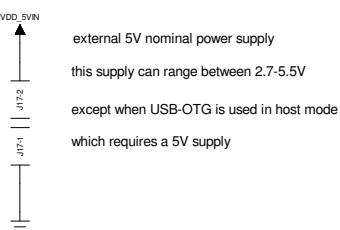


in crystal is not needed and the pins are,
remove the 0ohm resistors & add 0ohm to the pads marked NP



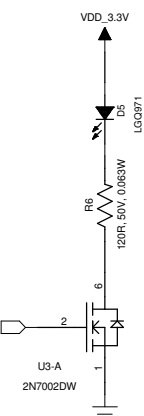
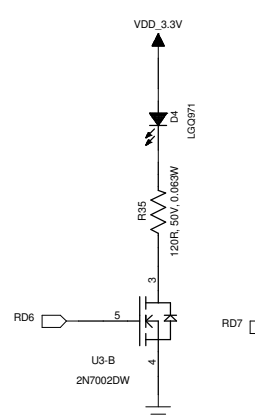
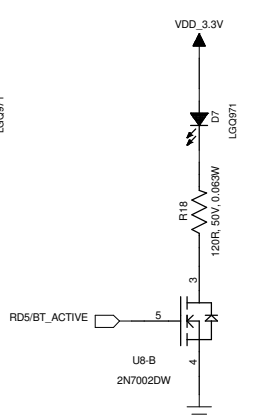
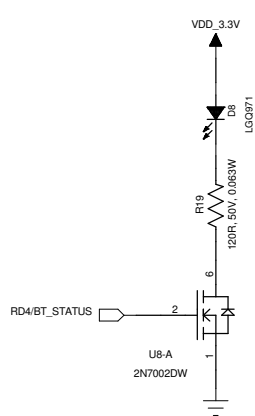
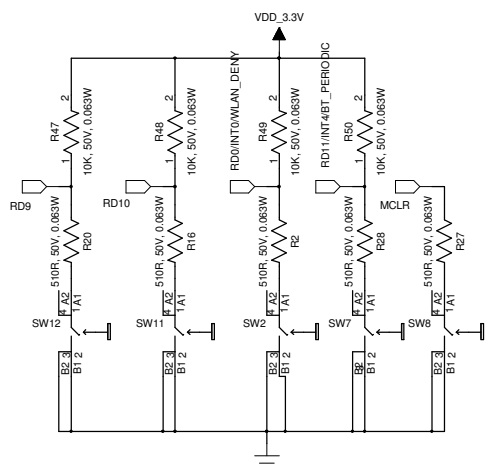
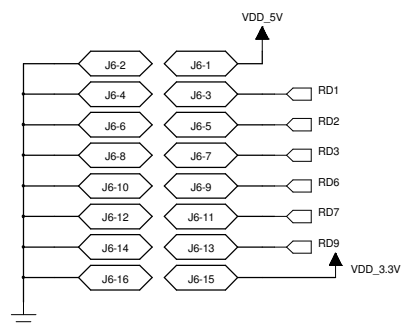
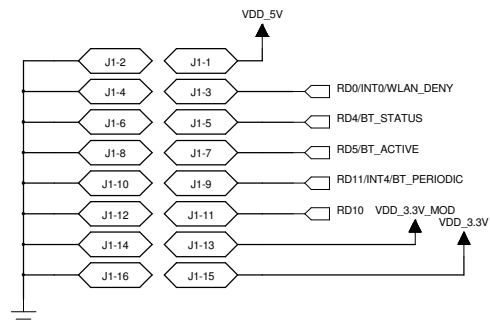
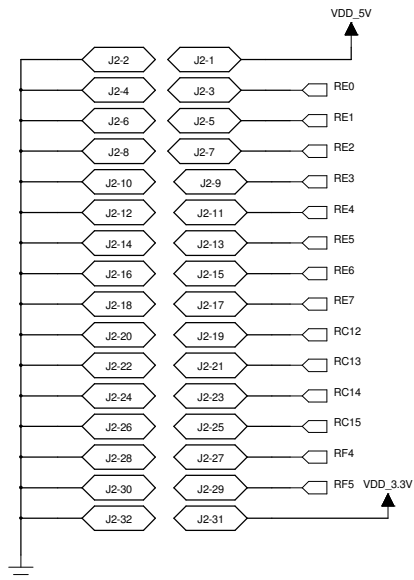
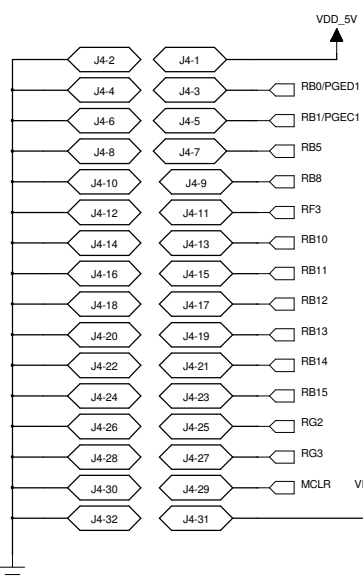


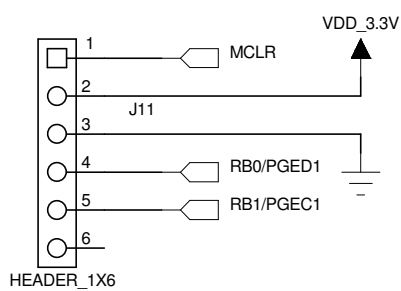




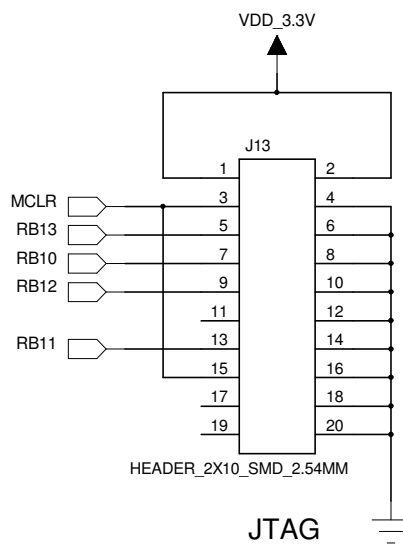
PA can be supplied with either regulated 3.3V or unregulated input from 2.7V to 4.8V (abs. max. 5.5V) this jumper allows choosing the input

NOTE: WiFi core SMPS is also powered from VDD_PA

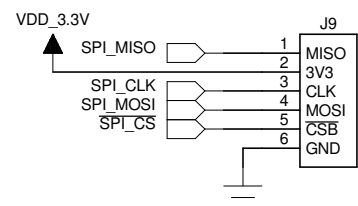




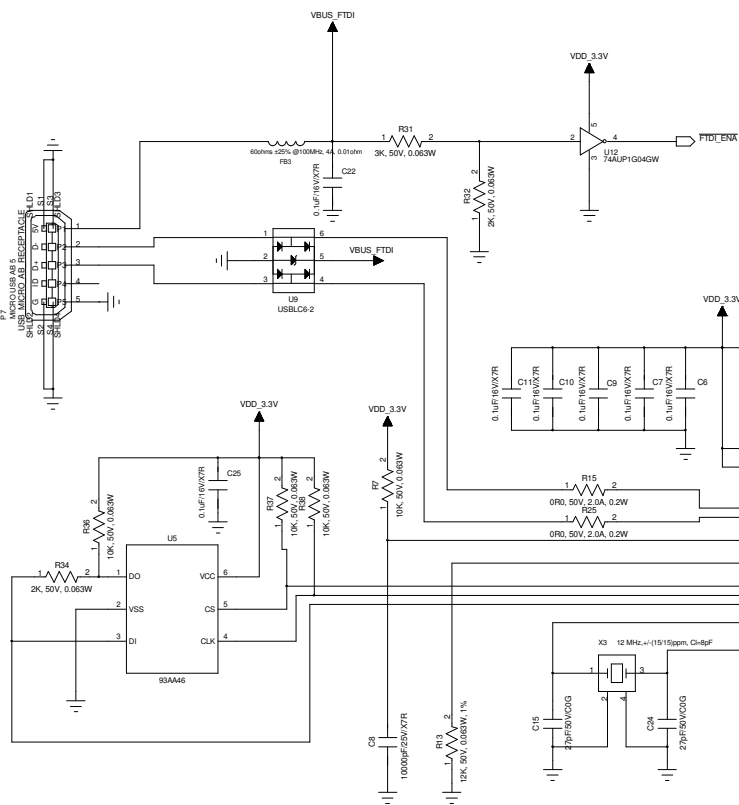
PicKit3 programming connector for module processor



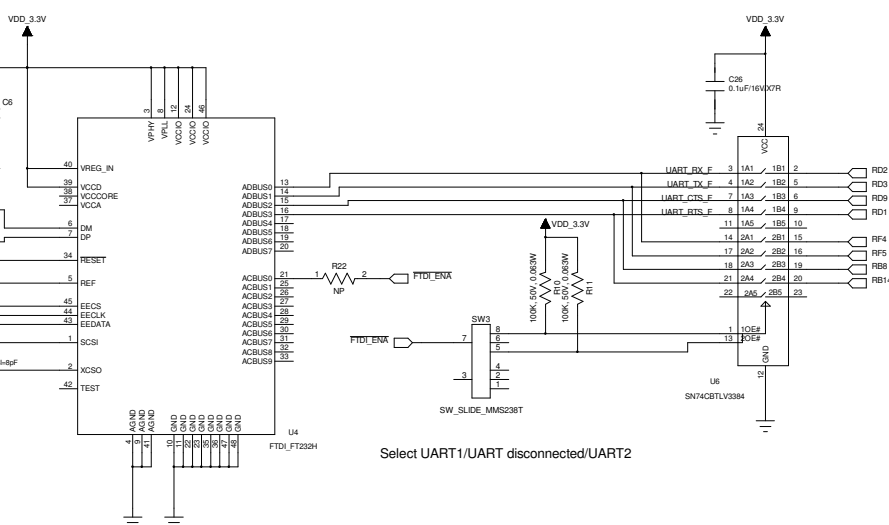
JTAG



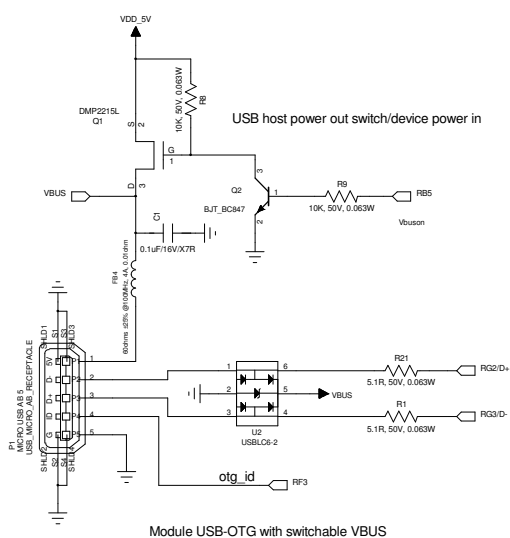
WiFi debug SPI



USB to UART interface converter



Select UART1/UART disconnected/UART2



Module USB-OTG with switchable VBUS