Instruction

Manufacture Z-Wave 700 product in volume

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REVISION RECORD				
Doc. Rev	**Date**	**By**	**Pages Affected**	**Brief Description of Changes**
1	20190130	JFR	All	Initial draft
2	20190207	JFR	2	Clarified Lock Bit Page setting to protect IP and security material.
3	20190711	SGANGULY JFR	2	Split manufacturing flow for end devices and gateways. Also added details about bootloader and signing keys etc.
4	20190712	SCBROWNI	All	Minor typos
5	20190903	JFR	2.1 & 2.2	RAILTest replacing ApplicationTestPoll used in 500 Series
6	20190911	SCBROWNI	2.1 & 2.2	Edited and reviewed the changed sections.
7	20200327	JESMILJA	2.2	Updated instruction what keys to use for EFR32ZG14
8	20200427	JFR	2.1 & 2.2	Clarified that ZGM130S is based on EFR32FG13 and EFR32ZG14 is based on EFR32FG14.

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1 INTRODUCTION

This document describes the manufacturing test flow for Z-Wave 700 SoC-based products.

2 MANUFACTURING FLOW

The manufacturing flow differs for end devices (ZGM130S, etc.) and gateways using EFR32ZG14 because the Serial API Bridge Controller application is made by Silicon Labs for EFR32ZG14. Therefore, the bootloader on EFR32ZG14 must be flashed together with the Silicon Labs public signing key and encryption key to support firmware update of a new application image (.gbl) file from Silicon Labs. For end devices the public signing key and encryption key will be manage by the vendor in question. In addition, it is also mandatory to adjust/calibrate Xtal crystal on EFR32ZG14-based products.

2.1 End Devices

The manufacturing production test flow for end devices ZGM130S (based on EFR32FG13) must incorporate the following steps:

- Perform product-specific testing such as I/O, etc. Refer to *UG409: RAILtest User's Guide* under the SDK documentation section in the Simplicity Studio distribution.
- Perform RF testing, etc. Use RAILtest. Refer to [7] regarding RF testing. The 500 Series ApplicationTestPoll function is not available in the 700.
- Set the manufacturing codes:
    1. Download an OTA bootloader to the SoC target via the Serial Wire Debug (SWD) interface.
    2. Write your own public signing key and encryption key to the SoC target via the SWD interface. A readme.txt file in the Z-Wave SDK release describes how to generate your own keys and write them to the device Lock Bits Page. The path to the readme file in the Z-Wave SDK release is:

       `<Your ZWAVE Installation Directory>\BootLoader\sample-keys\`
    3. Download the application firmware to the SoC target via the SWD interface. Do not set the Lock Bit in this step.

4. The application in the SoC signals when the security materials, etc., are in place in the Lock Bit Page via the manufacturing token TOKEN_MFG_ZW_INITIALIZED. The following steps are performed in the SoC at the application startup:

    a. If public/private keypair and QR code are already present in the Lock Bit page (check manufacturing token TOKEN_MFG_ZW_INITIALIZED), jump to the last step continuing normal operation. Refer to [1] for details about manufacturing tokens.

    b. Calculate the public/private key based on Curve25519.

    c. Construct the QR code using the public key, product type, and product ID (latter two from the application) as described in [2].

    d. Calculate the SHA-1 checksum as per [2] and incorporate it in the QR code.

    e. Write the QR code to the Lock Bit Page as manufacturing token TOKEN_MFG_ZW_QR_CODE.

    f. Write the private/public keypair to the Lock Bit Page as manufacturing tokens TOKEN_MFG_ZW_PRK and TOKEN_MFG_ZW_PUK.

    g. Write completion of the Lock Bit Page initialization as manufacturing token TOKEN_MFG_ZW_INITIALIZED. This token can be used to sync completion of data to Lock Bits Page in a production system.

    h. Continue normal startup.

5. Read the QR code from the SoC.

6. Set the Lock Bit Page [6] to protect IP and security material against untrusted entities.

7. Label the product with the QR code. Refer to [5] for details.

    The QR code format enables customization of the QR code with extra TLVs (e.g., MaxInclusionRequestInterval, proprietary serial number, etc.) instead of using the internally generated one. The manufacturing line programmer must then read out the public key, etc., compose the wanted QR code, and print it to a label. The new QR code can also be stored in the User Data Page, for example.

    Set the following registers in the Lock Bit Page [6] as a minimum to protect IP and security material:

    DLW = Disable the debug port by clearing the four LSBs
    ULW = Ignore
    MLW = Optional (disable mass erase through MSC)
    ALW = Optional (disallow a mass erase operation)
    CLW1 = Ignore
    CLW2 = Ignore
    PLW[0…121] = Ignore

2.2 Gateways

The manufacturing production test flow for gateways using EFR32ZG14 (based on EFR32FG14) must incorporate the following steps:

1. Product-specific testing such as I/O, etc. Refer to 'Using RAIL Test' under the SDK documentation section in the Simplicity Studio distribution.
2. Optional application for EFR32ZG14-based products performs crystal adjustment. Refer to [4] for details.
3. For RF testing, etc., use RAILTest. Refer to [7] regarding RF testing. The 500 Series ApplicationTestPoll function is not available in 700.
4. Download OTW bootloader to the SoC target via Serial Wire Debug (SWD) interface.
5. Generate your own public signing key and encryption key and write them to the SoC target via the Serial Wire Debug (SWD) interface. These keys are necessary for upgrading the firmware in the field. Following simplicity commander commands will be used for writing keys into the device's Lock Bits Page.
    a. commander flash --tokengroup znet --tokenfile zg14_encrypt.key --tokenfile zg14_sign.key-tokens.txt -d EFR32ZG14
    b. The key files are locked in the Z-Wave release following path in your SDK installation

    ```
    <Your ZWAVE Installation Directory>\BootLoader\ZG14-keys\
    ```
6. Download the application firmware to the SoC target via the Serial Wire Debug (SWD) interface. Do not set the Lock Bit in this step.
7. The application in the SoC signals when security materials, etc., are in place in the Lock Bit Page via manufacturing token TOKEN_MFG_ZW_INITIALIZED. The following steps are performed in the SoC at the application startup:
    a. If the public/private keypair and QR code are already present in the Lock Bit page (Check manufacturing token TOKEN_MFG_ZW_INITIALIZED), jump to the last step continuing normal operation. Refer to [1] for details about manufacturing tokens.
    b. Calculate the public/private key based on Curve25519.
    c. Construct the QR code using public key, product type, and product ID (latter two from application) as described in [2].
    d. Calculate SHA-1 checksum as per [2] and incorporate it in the QR code.
    e. Write the QR code to Lock Bit Page as manufacturing token TOKEN_MFG_ZW_QR_CODE.
    f. Write private/public keypair to the Lock Bit Page as manufacturing tokens TOKEN_MFG_ZW_PRK and TOKEN_MFG_ZW_PUK.

   g. Write completion of Lock Bit Page initialization as manufacturing token TOKEN_MFG_ZW_INITIALIZED. This token can be used to sync completion of data to the Lock Bits Page in a production system.

   h. Continue normal startup.

8. Read the QR code from the SoC.

9. Set the Lock Bit Page [6] to protect IP and security material against untrusted entities.

10. Label the product with the QR code. Refer to [5] for details.

    The QR code format enables customization of the QR code with extra TLVs (e.g., MaxInclusionRequestInterval, proprietary serial number, etc.) instead of using the internally generated one. The manufacturing line programmer must then read out the public key, etc., and compose the wanted QR code and print it to a label. The new QR code can also be stored in, e.g., the User Data Page.

    Set the following registers in the Lock Bit Page [6] as a minimum to protect IP and security material:

    DLW = Disable the debug port by clearing the four LSBs
    ULW = Ignore
    MLW = Optional (disable mass erase through MSC)
    ALW = Optional (disallow a mass erase operation)
    CLW1 = Ignore
    CLW2 = Ignore
    PLW[0…121] = Ignore

REFERENCES

[1] Silicon Labs, SDS14306, Software Design Specification, Z-Wave 700 Lock Bits and User Data Page Contents.
[2] Silicon Labs, INS13975, Instruction, SmartStart Production Control.
[3] Silicon Labs, SDS13937, Software Design Specification, Node Provisioning QR Code Format.
[4] Silicon Labs, INS14498, Instruction, Mandatory crystal adjustment for EFR32ZG14 based products.
[5] Z-Wave Alliance, Z-Wave Security 2 (S2) and SmartStart Product Labeling Requirements.
[6] Silicon Labs, EFR32xG1 Wireless Gecko Reference Manual. Rev. 1.1.
[7] Silicon Labs, INS14283, Instruction, Bring-up/test HW development.