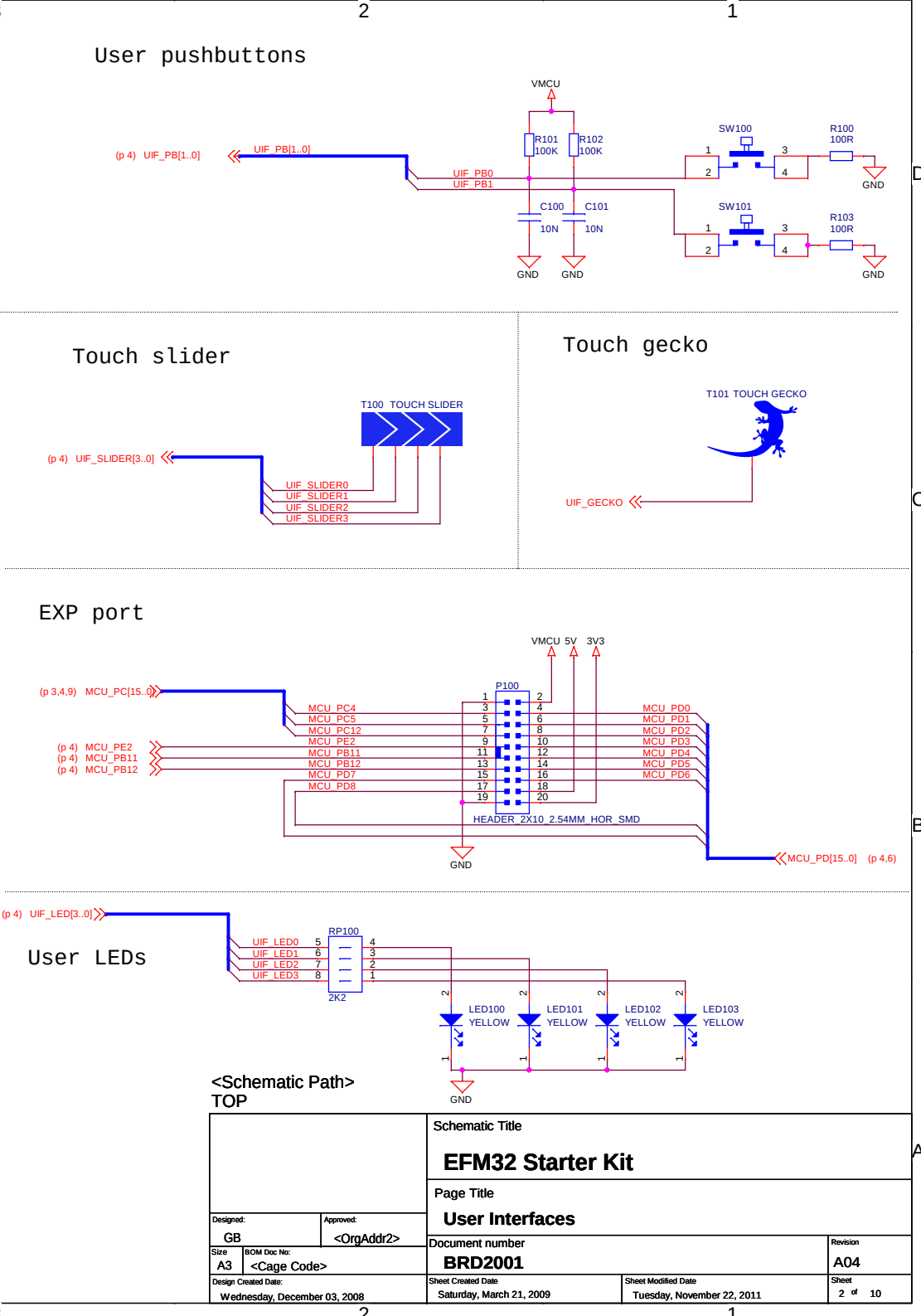
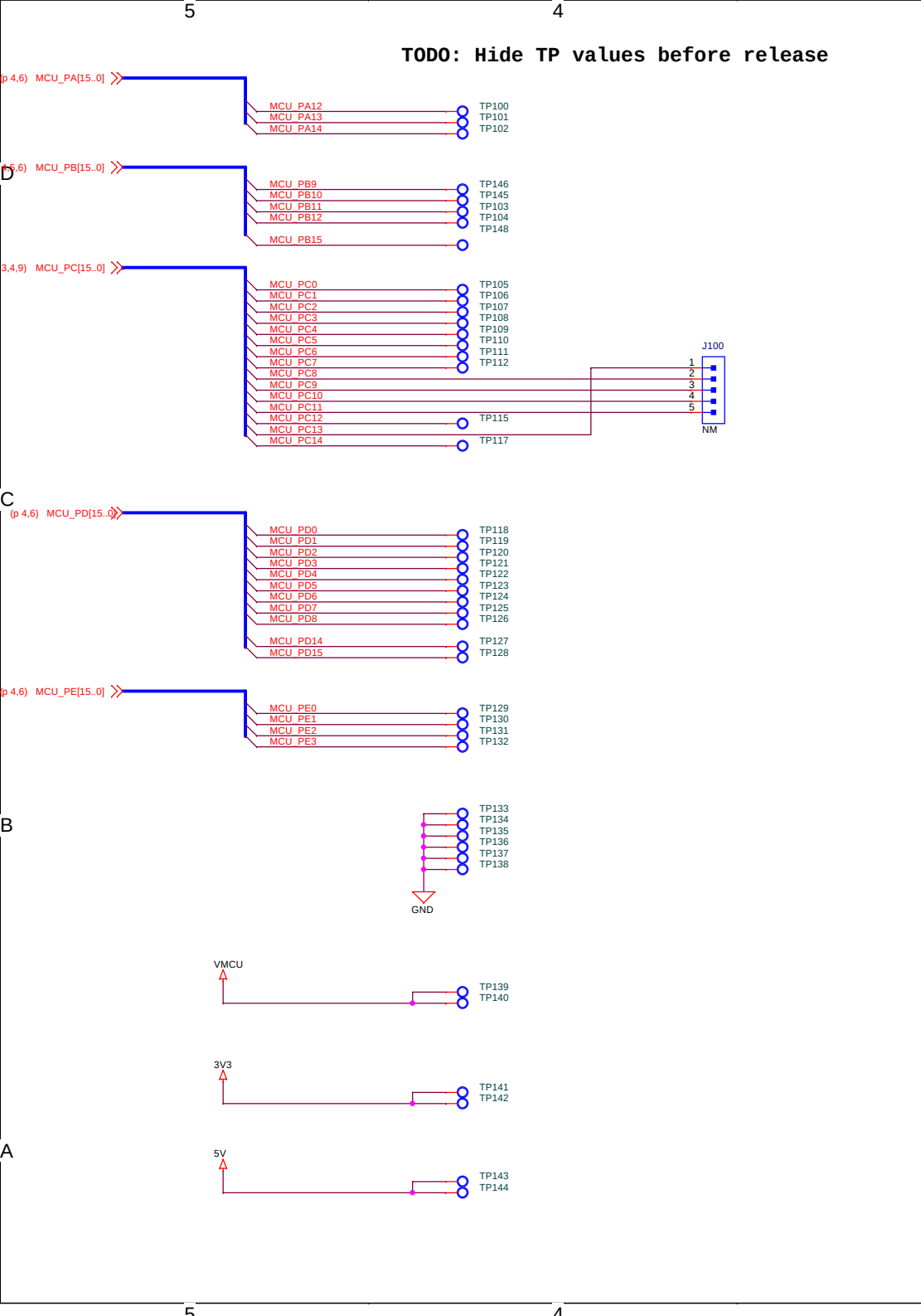


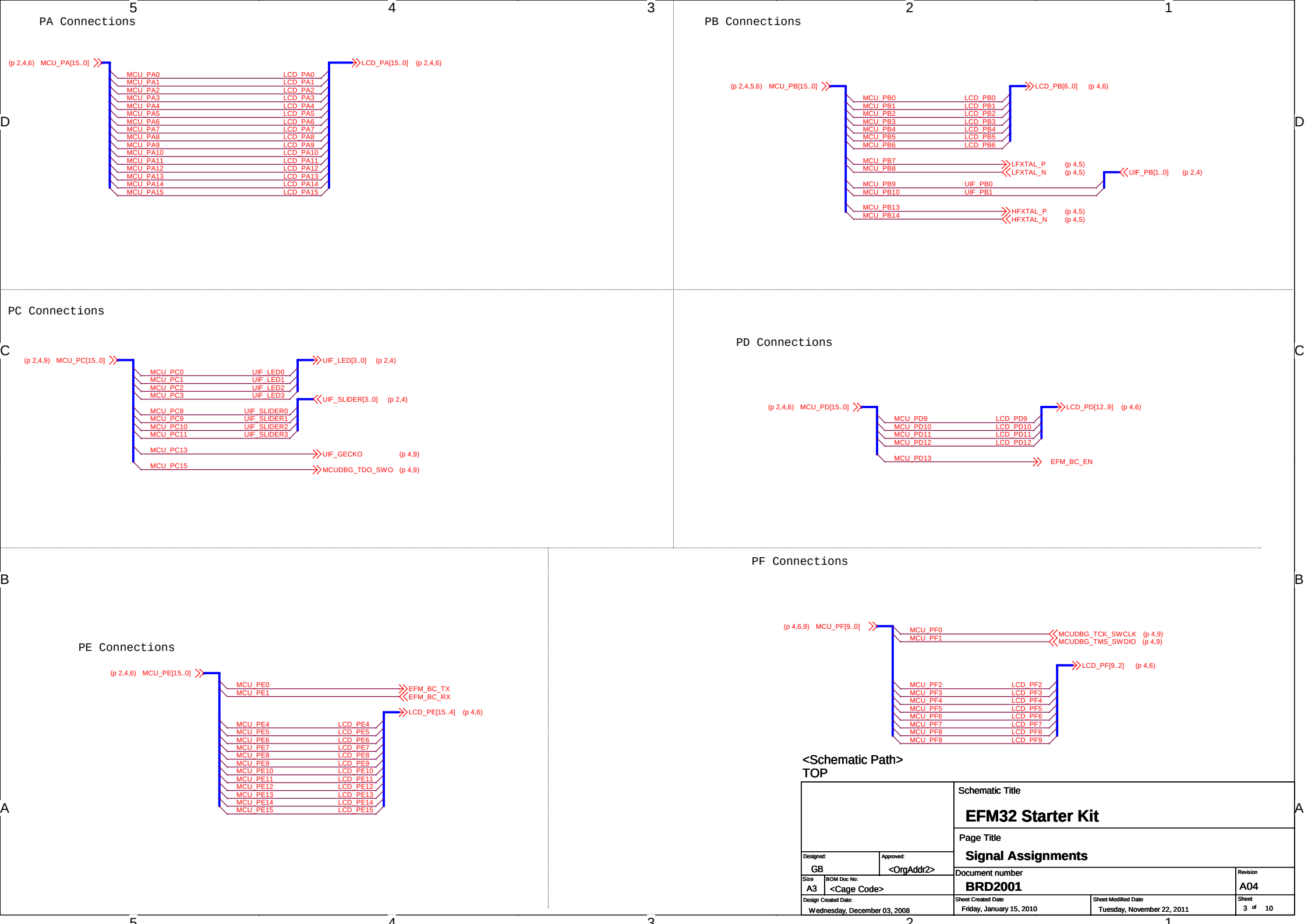


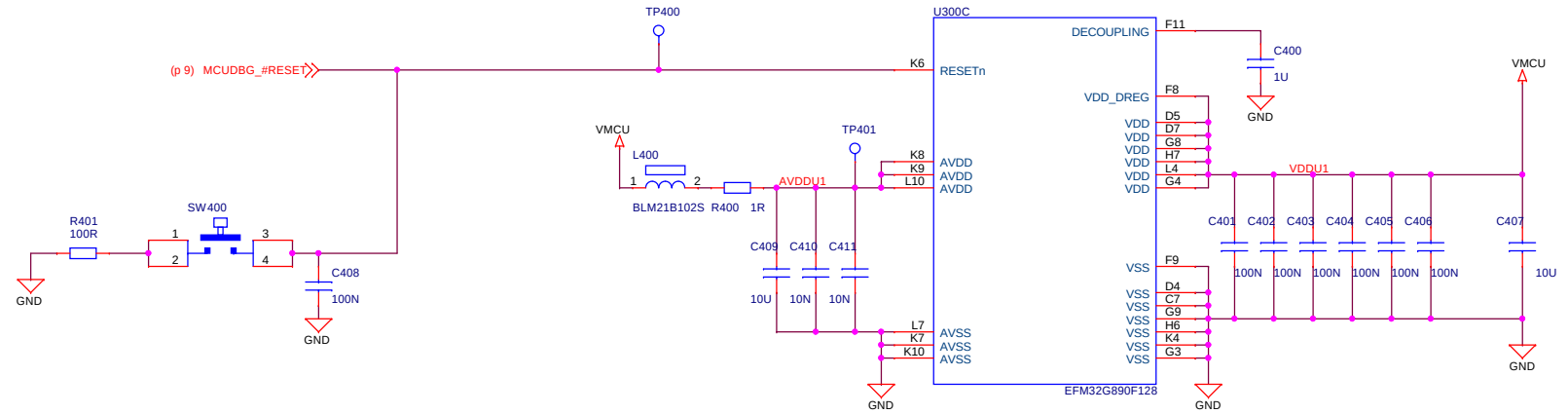
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Revision History	
Rev.	Description
A00	A lot of bug fixes and improvements.
A01	Added #RST to the debug header and added a separate header (J100) for the touch segment pins.
A02	Fixed renaming of 3V3 to PCB_REV0
A03	Swapped two pins on D600 to simplify routing.
A04	Changed to LCD with stoppers

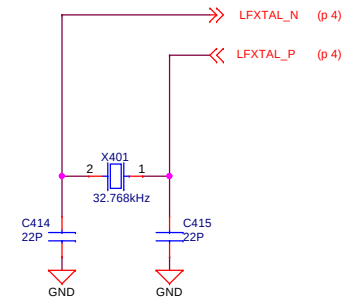
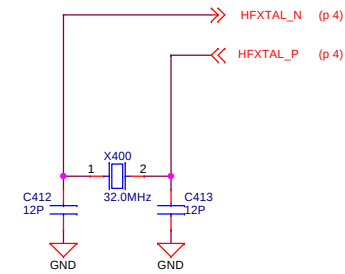
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		Revision A04	
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Low Frequency Clock



<Schematic Path>
TOP

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		EFM32 Starter Kit	
Designed: GB Approved: <OrgAddr2>		Page Title	
		EFM32 Power	
Size A3 BOM Doc No: <Cage Code>		Document number	Revision
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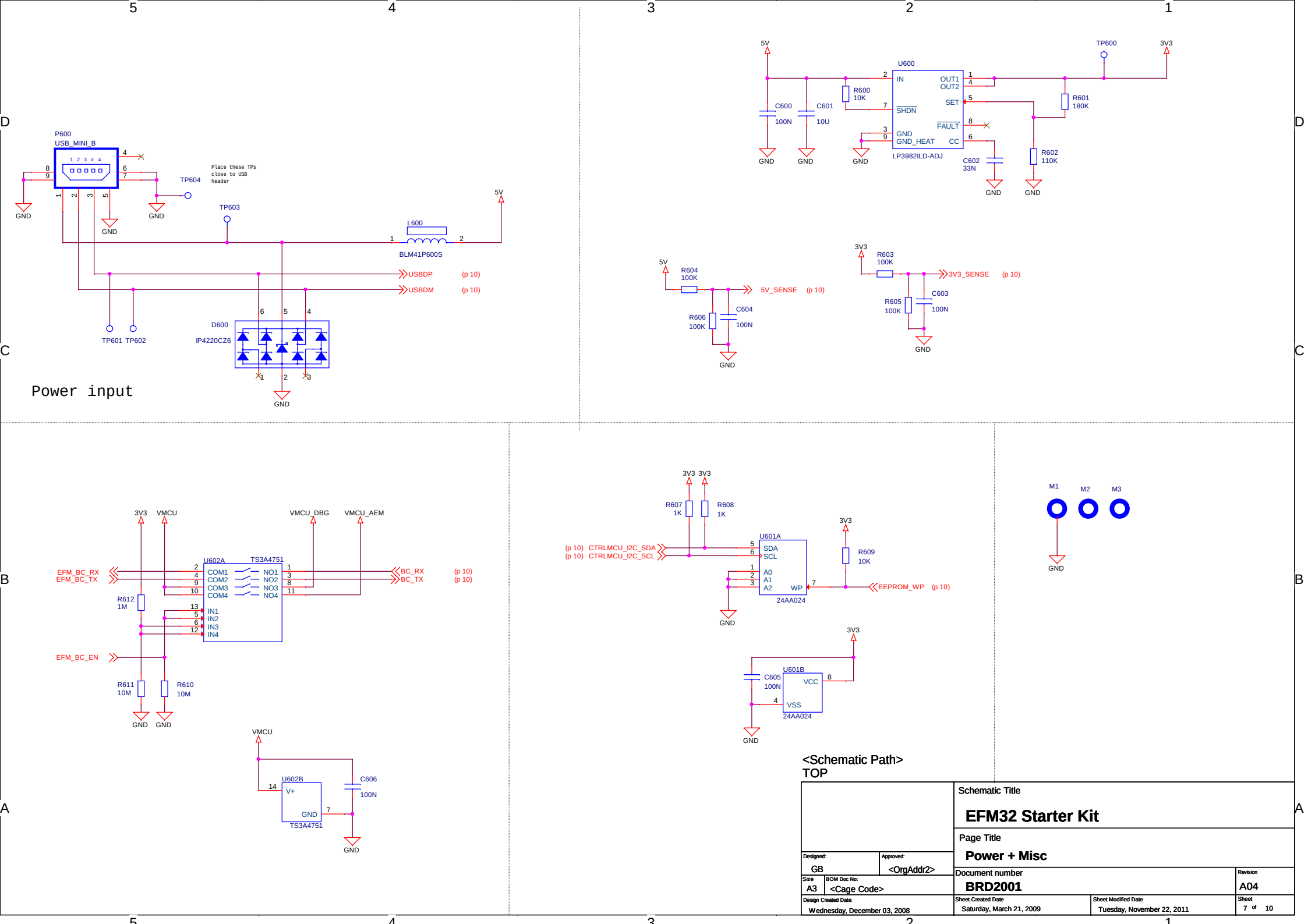
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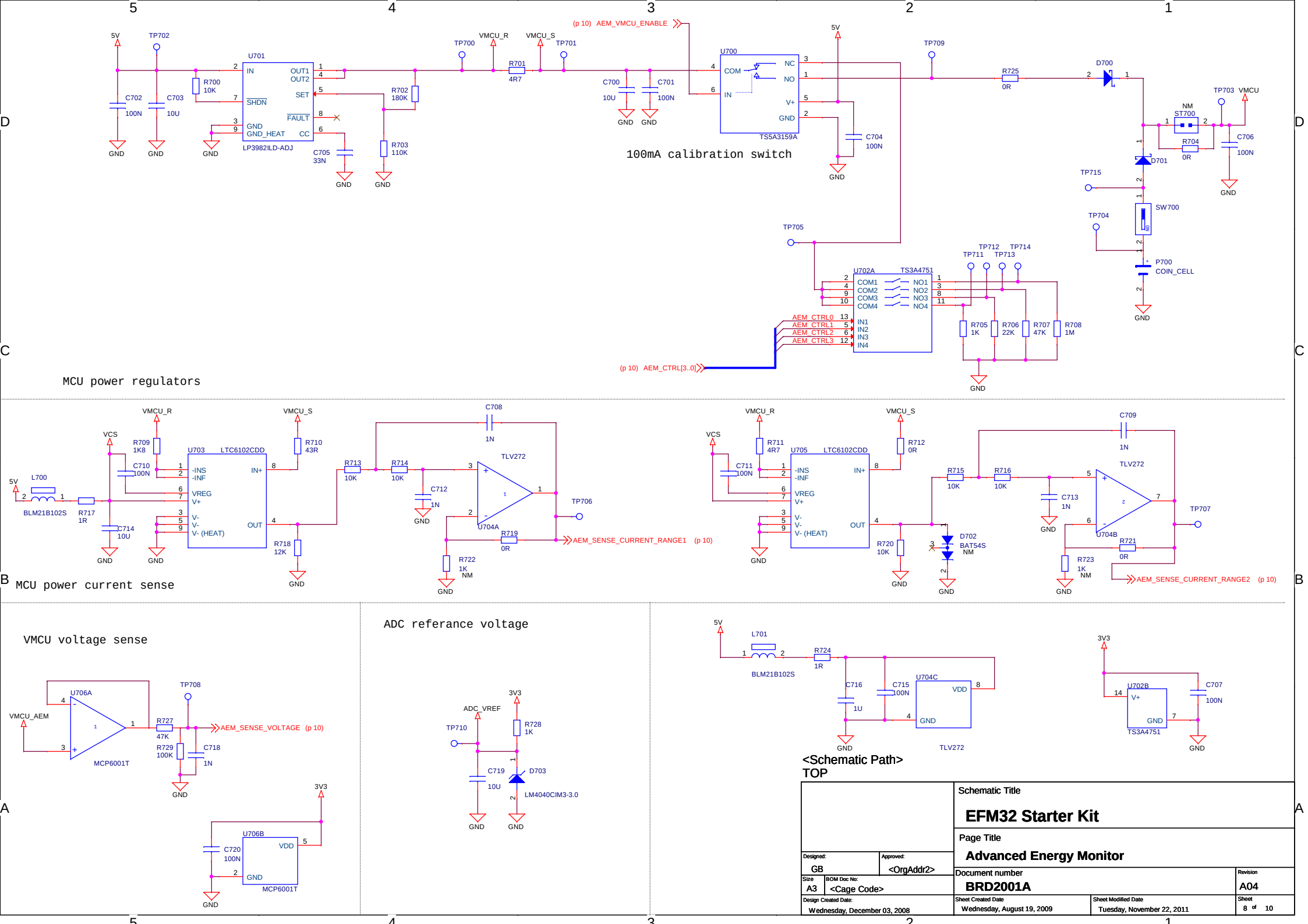


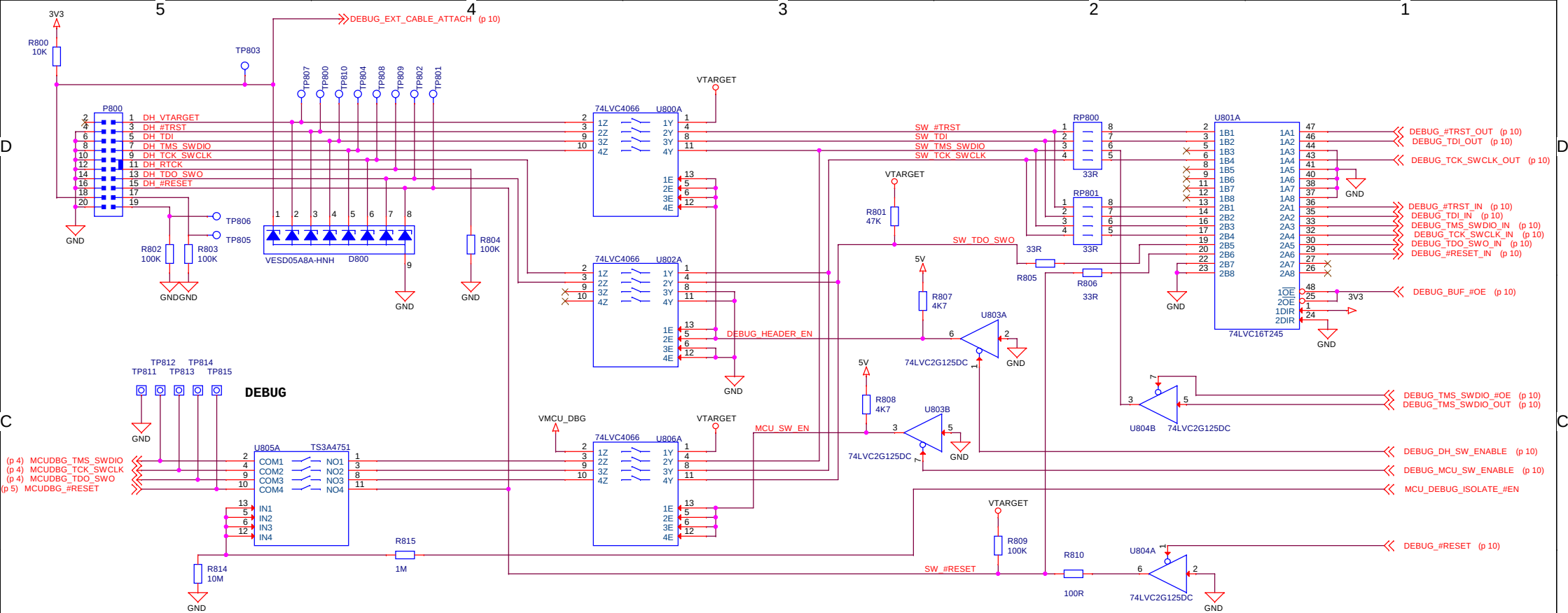
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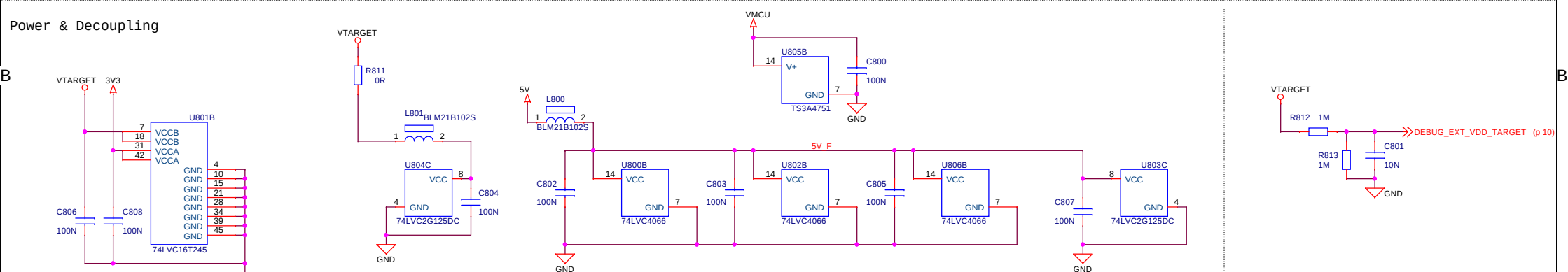
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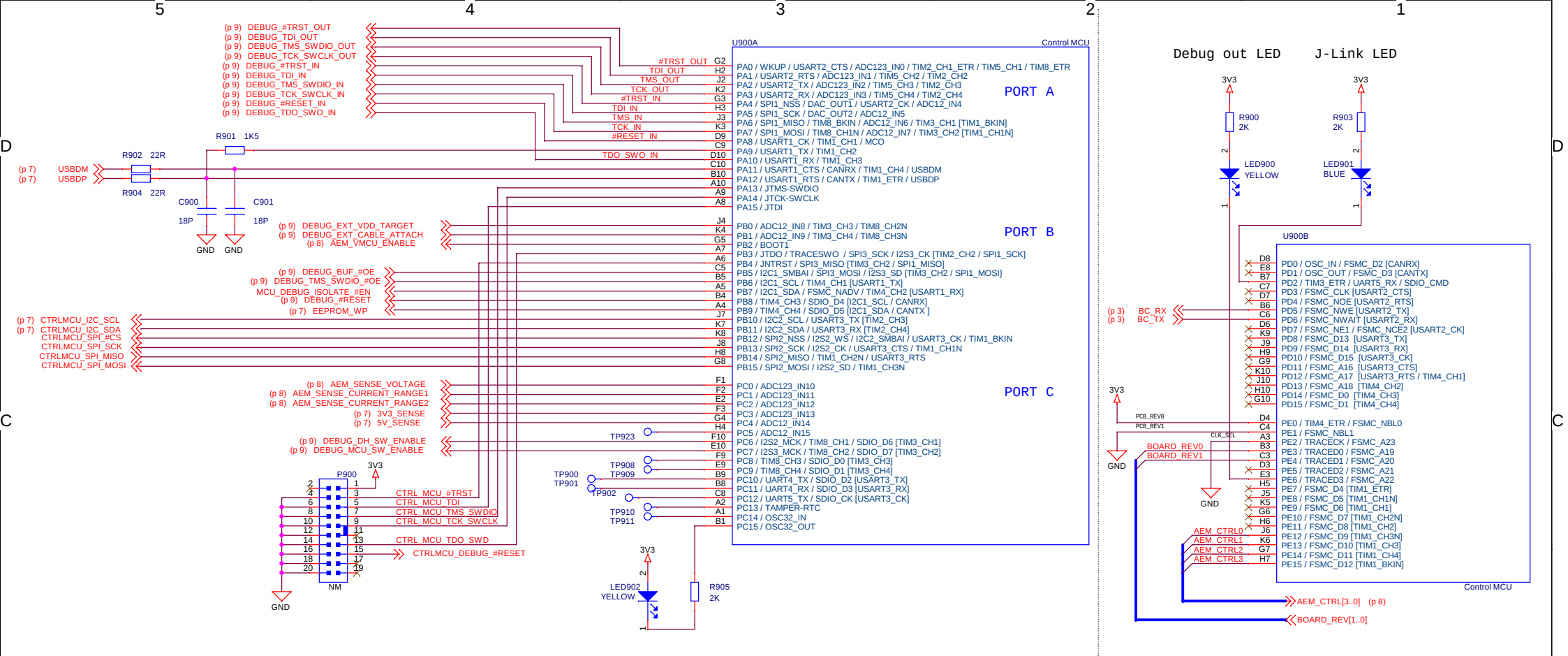


Power & Decoupling



Mode	DEBUG_MCU_SW_ENABLE	DEBUG_DH_SW_ENABLE	DEBUG_BUF_#OE	DH_VTARGET	VTARGET
Debug Out	0	1	0	External voltage	External voltage
MCU Debug	1	0	0	Disconnected	VMCU
Debug In	1	1	1	VMCU	VMCU

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Designed: GB Approved: <OrgAddr2>		Page Title Debug Interface	
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B Control MCU Power & Bypass

