



EFM32 Giant Gecko Starter Kit


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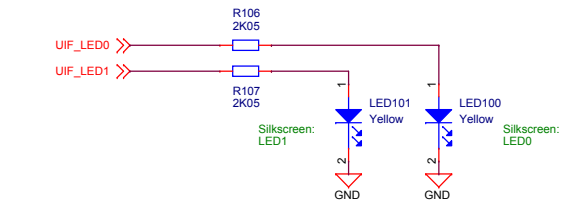
Revision History

Rev.	Description
A01	Initial release. Transition to new STK platform.

 SILICON LABS		Board Name EFM32 Giant Gecko Starter Kit	
		Page Title Title Page	
Designed MAH	Approved RGU	Board Number BRD2200C	Revision A01
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The image displays three circuit diagrams for the VMCU board, each with a title and a component count in the top right corner.

- Push Buttons** (5 components): This diagram shows two push buttons, SW100 and SW101, both labeled PTS810. Each button is connected to a 1MΩ resistor (R100, R101) and a 1N capacitor (C102, C103). The buttons are connected to the VMCU and GND. Silkscreen labels include UIF_BUTTON0, UIF_BUTTON1, BTN0, and BTN1.
- LEDs** (3 components): This diagram shows two yellow LEDs, LED100 and LED101. Each LED is connected to a 2K05 resistor (R106, R107) and a 100R resistor (R104, R105). The LEDs are connected to GND. Silkscreen labels include UIF_LED0, UIF_LED1, and LED0.
- LESENSE LC-Sensor** (2 components): This diagram shows the LC-Sensor circuit. It includes a DAC_LC_EXCITE input, a 0R resistor (R198), a 100N capacitor (C199), a 330P capacitor (C198), a 0R resistor (R195), a 1K5 resistor (R196), and a 390uH inductor (L100). The sensor is connected to GND. Silkscreen labels include LES_LC_SENSE and LC Sense.



OPAMP Connection Footprint

C

OPAMP Connection Footprint

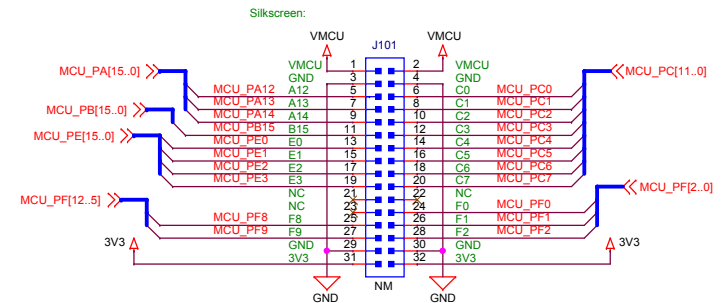
Layout Note:
Place on secondary side.

Silkscreen:
[Opamp symbol]

[illegible]

Touch Slider

The diagram illustrates a Touch Slider component, labeled 'T1 TOUCH SLIDER'. It is represented by a blue block with four output lines. These lines are connected to a red arrow pointing left, labeled 'UIF_SLIDER[3..0]'. The output lines are labeled 'UIF_SLIDER0', 'UIF_SLIDER1', 'UIF_SLIDER2', and 'UIF_SLIDER3'.



EXP Header

EXP_HEADER[16..3]

EXP_HEADER3
EXP_HEADER5
EXP_HEADER7
EXP_HEADER9
EXP_HEADER11
EXP_HEADER13
EXP_HEADER15

BC_I2C_EXP_SCL
BC_I2C_EXP_SDA

P101

1 2
3 4
5 6
7 8
9 10
11 12
13 14
15 16
17 18
19 20

VMCU 5V_EXP 3V3

EXP_HEADER4
EXP_HEADER6
EXP_HEADER8
EXP_HEADER10
EXP_HEADER12
EXP_HEADER14
EXP_HEADER16

GND

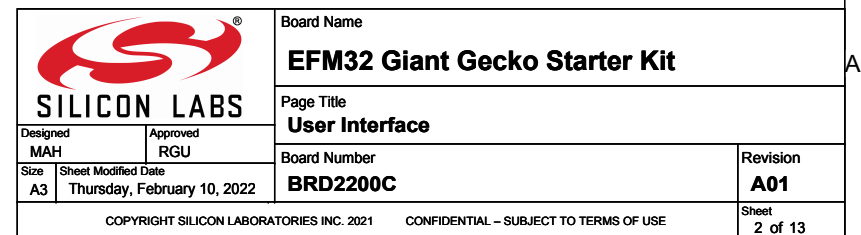
HEADER_2X10_2.54MM_TH

Top Row		
2	VMMC1	
4	PD0	US1_TX
6	PD1	US1_RX
8	PD2	US1_CLK
10	PD3	US1_CS
12	PD4	LEU0_TX
14	PD5	LEU0_RX
16	PD6	I2C0_SDA#1
18	SV	
20	3V3	

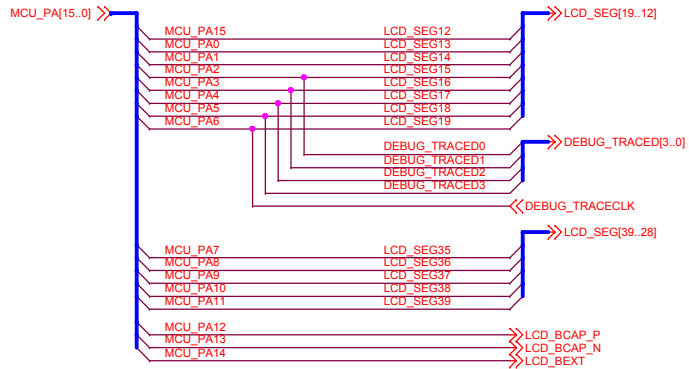
Bottom Row		
1	GND	
3	PC0	ACMP0_CH4
5	PC3	ACMP0_CH5
7	PC4	ACMP1_CH4
9	PC5	ACMP0_O
11	PB11	DA00_OUT0
13	PB12	DA00_OUT1
15	PD7	I2C0_SCL#1
17	Reserved for EXP Board Identification	
19	Reserved for EXP Board Identification	

Bottom Row

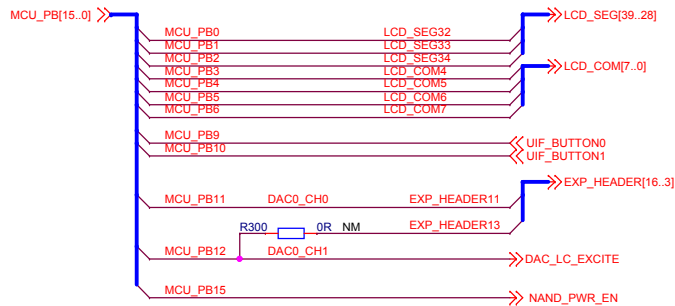
					Bottom Row
1	GND				
3	PC0				ACMP0_CH4
5	PC3				ACMP0_CH5
7	PC4				ACMP1_CH4
9	PC5				ACMP0_O
11	PB11				DAC0_OUT0
13	PB12				DAC0_OUT1
15	PD7	I2C0_SCL#1			
17	Reserved for EXP Board Identification				
19	Reserved for EXP Board Identification				



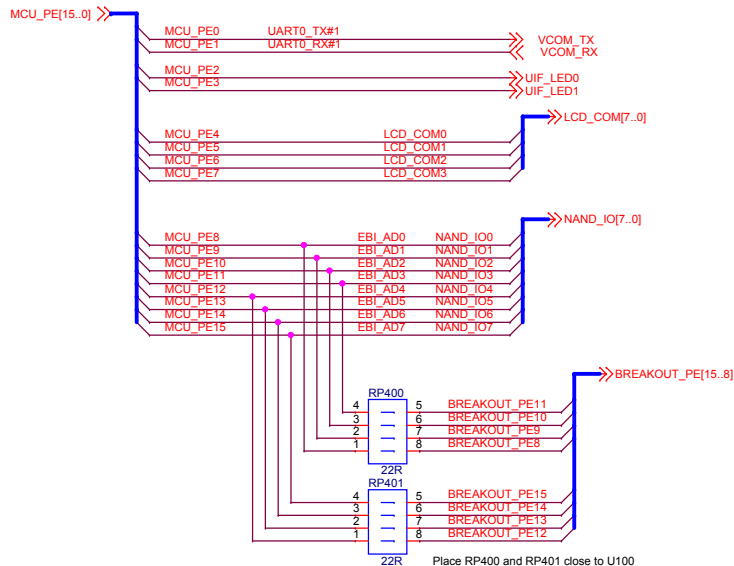
PA Connections



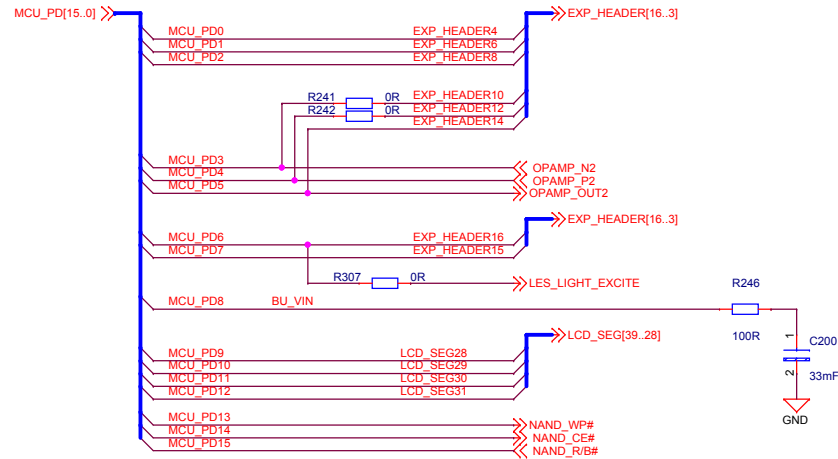
PB Connections



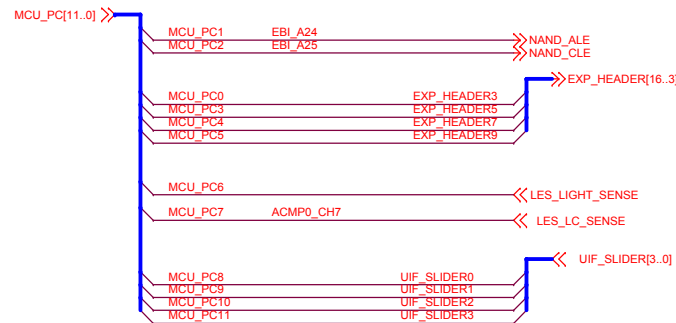
PE Connections



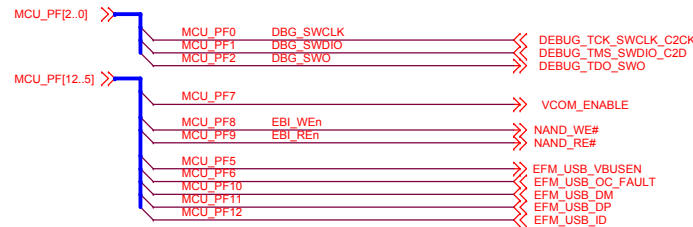
PD Connections




PC Connections

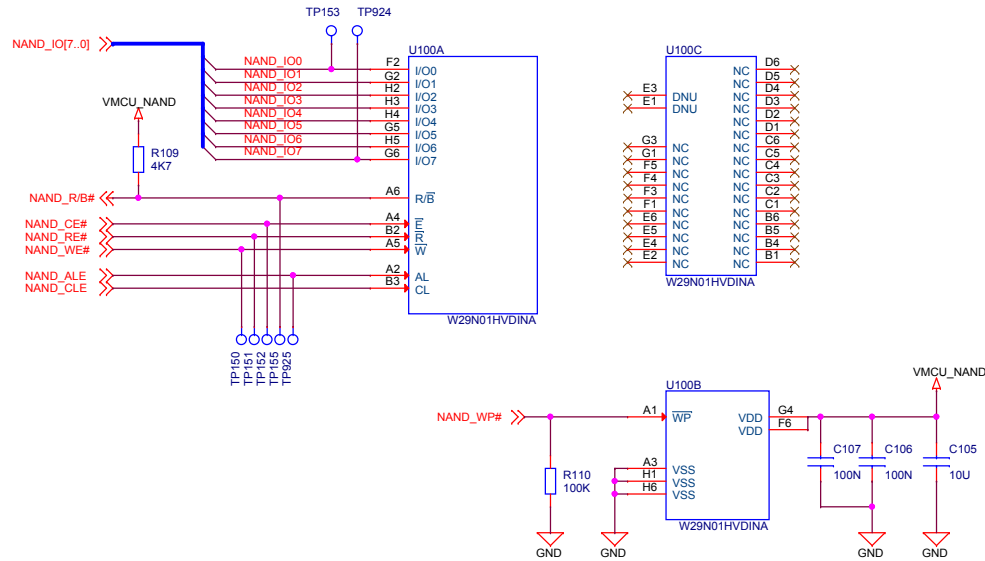


PF Connections

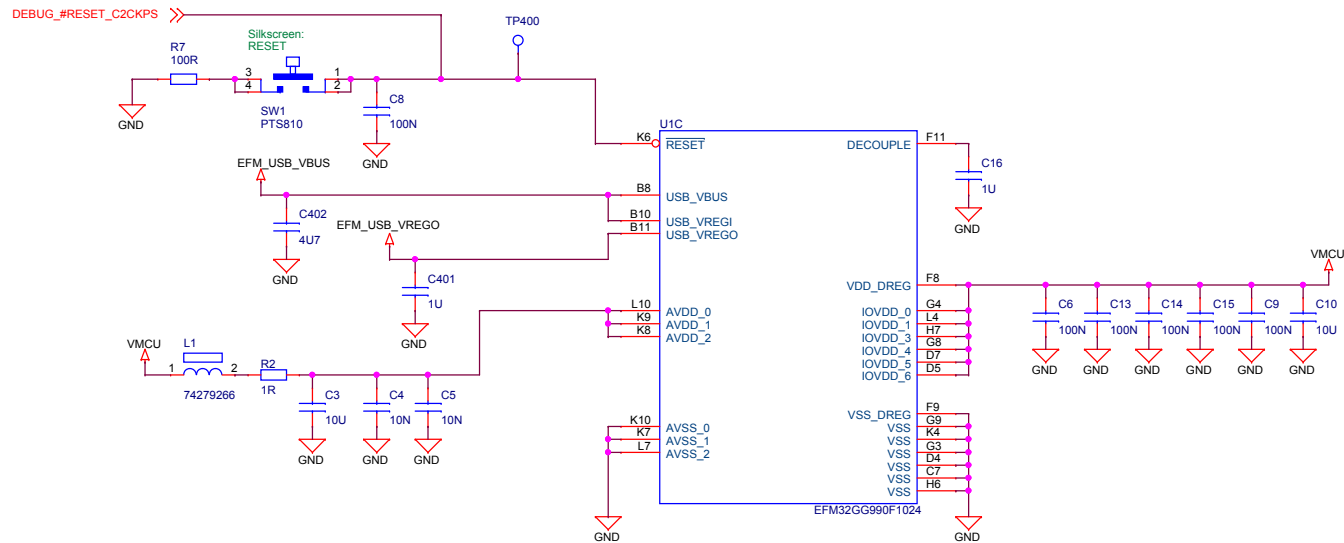


		Board Name	
		EFM32 Giant Gecko Starter Kit	
Designed MAH		Page Title	
Size A3		EFM32 Signal Assignments	
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		BRD2200C	A01
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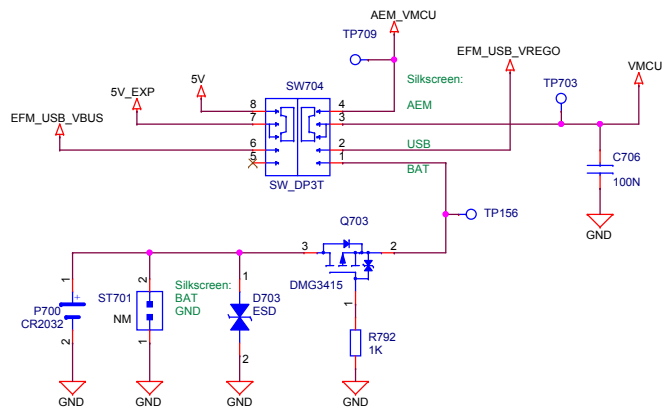
1 Gbit NAND Flash




EFM32 Power and Decoupling



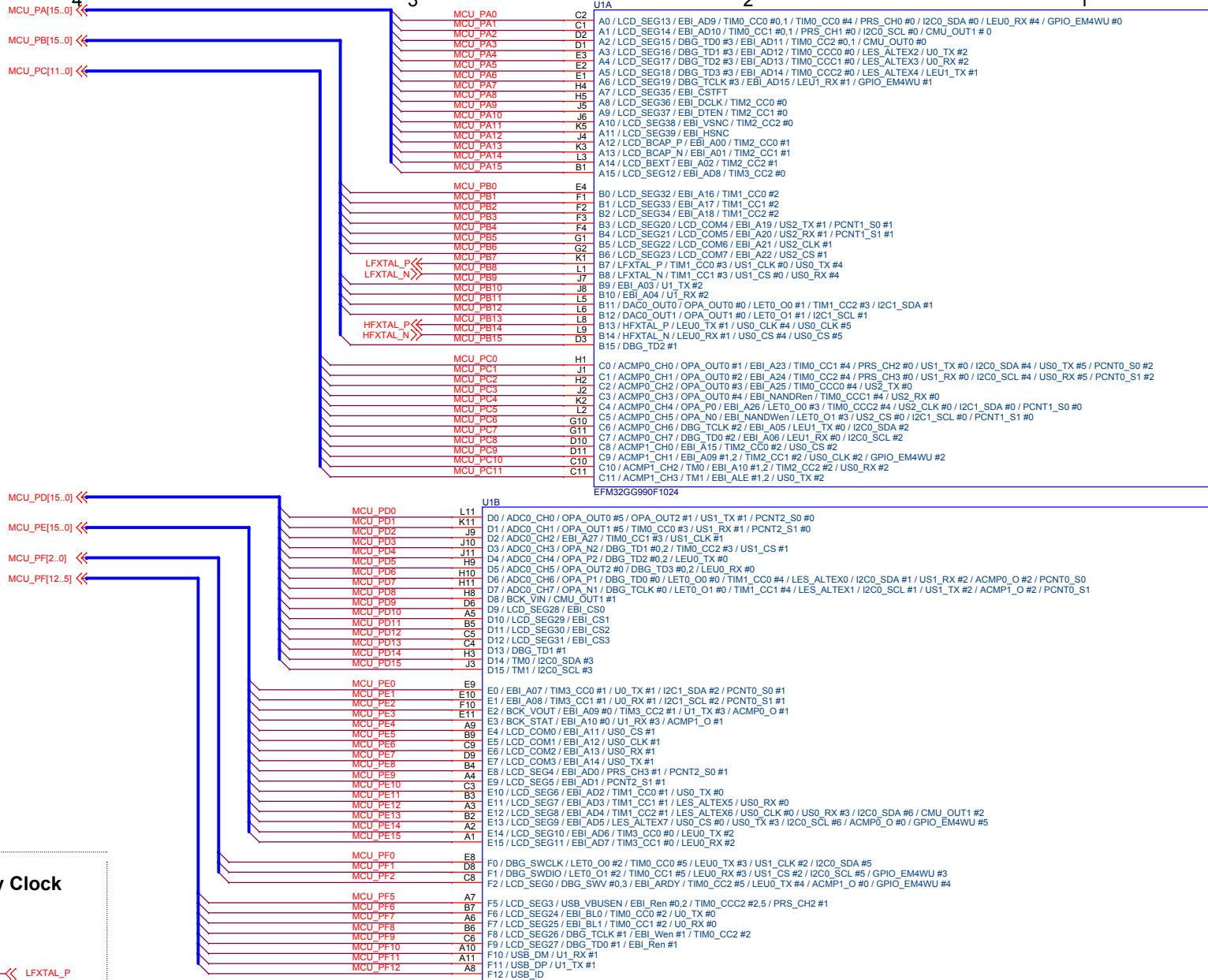
Power Selection Switch: AEM/USB/BAT



SWITCH POS	MODE DESCRIPTION
AEM	AEM Enabled, VMCU sourced from external 3.3V LDO powered by BC USB 5V supply
USB	AEM Disabled, VMCU sourced from internal 3.3V LDO powered by MCU USB 5V supply, EXP header and breakout 5V sourced from MCU USB 5V supply
BAT	AEM Disabled, VMCU sourced from coin-cell battery or external power supply

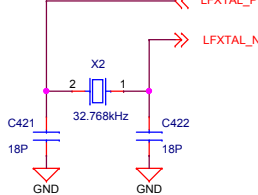
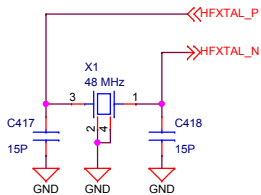
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		Page Title EFM32 Power	
Designed HEL	Approved RGU	Board Number BRD2200C	Revision A01
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EFM32 MCU



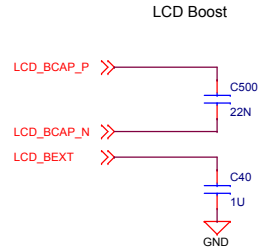
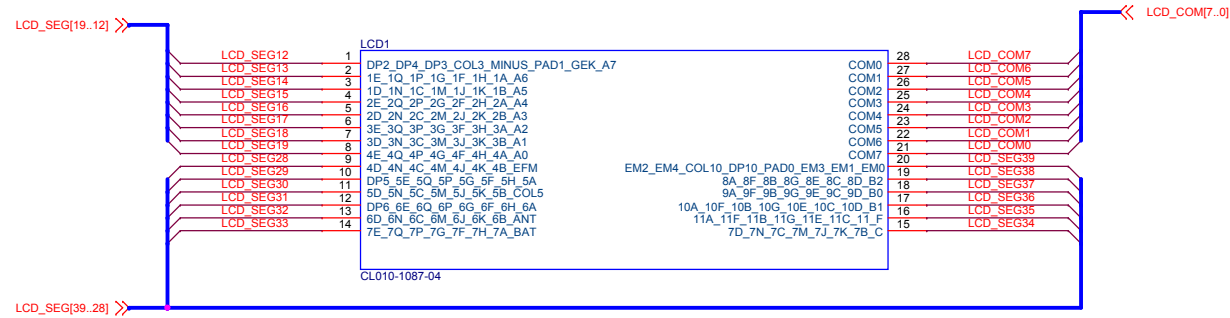
High Frequency Clock

Low Frequency Clock



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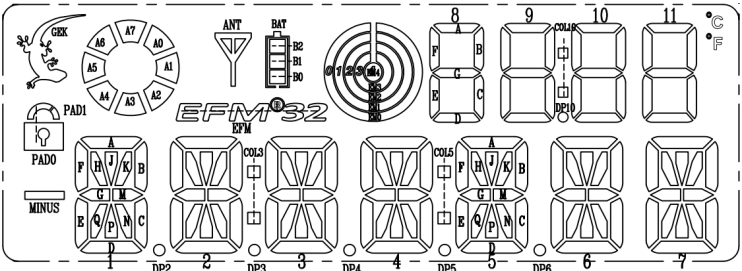
Segment LCD Signal Connections




Segment Names

PIN	1	2	3	4	5	6	7	8	9	10	11	12	13	14
---	S0	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13
COM0	DP2	1 E	1 D	2 E	2 D	3 E	3 D	4 E	4 D	DP5	5 D	DP6	6 D	7 E
COM1	DP4	1 Q	1 N	2 Q	2 N	3 Q	3 N	4 Q	4 N	5 E	5 N	6 E	6 N	7 Q
COM2	DP3	1 P	1 C	2 P	2 C	3 P	3 C	4 P	4 C	5 Q	5 C	6 Q	6 C	7 P
COM3	COL3	1 G	1 M	2 G	2 M	3 G	3 M	4 G	4 M	5 P	5 M	6 P	6 M	7 G
COM4	MINUS	1 F	1 J	2 F	2 J	3 F	3 J	4 F	4 J	5 G	5 J	6 G	6 J	7 F
COM5	PAD1	1 H	1 K	2 H	2 K	3 H	3 K	4 H	4 K	5 F	5 K	6 F	6 K	7 H
COM6	GEK	1 A	1 B	2 A	2 B	3 A	3 B	4 A	4 B	5 H	5 B	6 H	6 B	7 A
COM7	A7	A6	A5	A4	A3	A2	A1	A0	EFM	5 A	COL5	6 A	ANT	BAT

PIN	15	16	17	18	19	20	21	22	23	24	25	26	27	28
---	S14	S15	S16	S17	S18	S19	COM7	COM6	COM5	COM4	COM3	COM2	COM1	COM0
COM0	7 D	11 A	10 A	9 A	8 A	EM2								COM0
COM1	7 N	11 F	10 F	9 F	8 F	EM4							COM1	
COM2	7 C	11 B	10 B	9 B	8 B	COL10						COM2		
COM3	7 M	11 G	10 G	9 G	8 G	DP10					COM3			
COM4	7 J	11 E	10 E	9 E	8 E	PAD0				COM4				
COM5	7 K	11 C	10 C	9 C	8 C	EM3			COM5					
COM6	7 B	11 D	10 D	9 D	8 D	EM1		COM6						
COM7	°C	°F	B1	B0	B2	EM0	COM7							





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Designed
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Approved
RGU

Board Name
EFM32 Giant Gecko Starter Kit

Page Title
Segment LCD

Board Number
BRD2200C

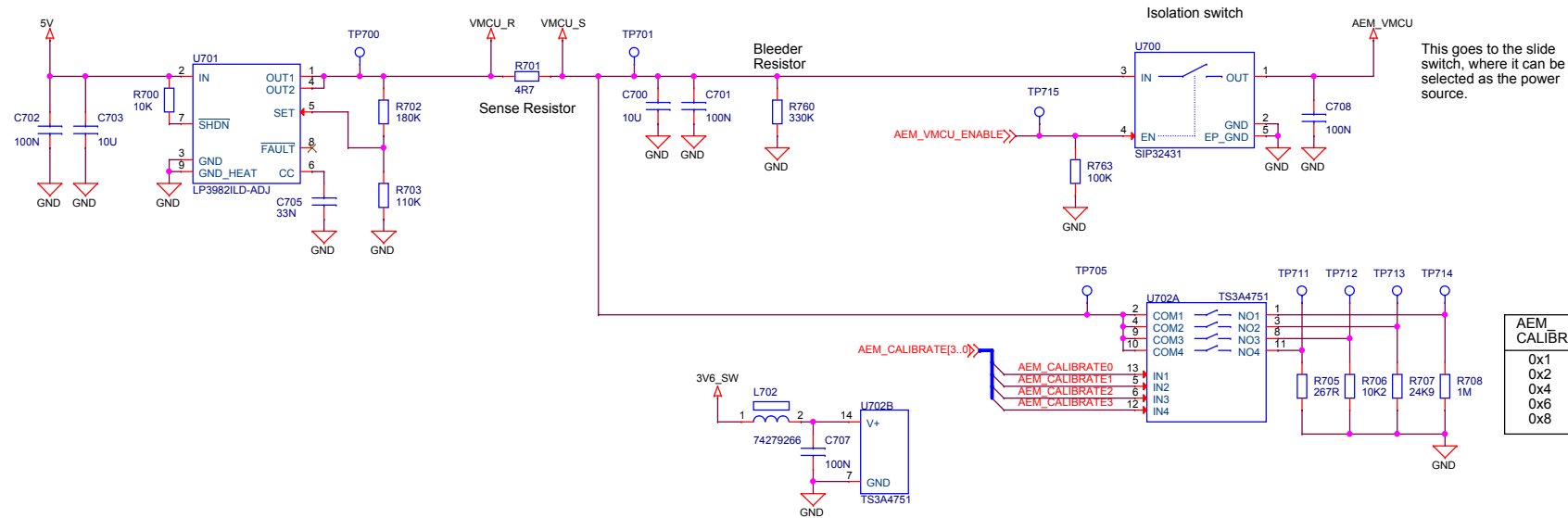
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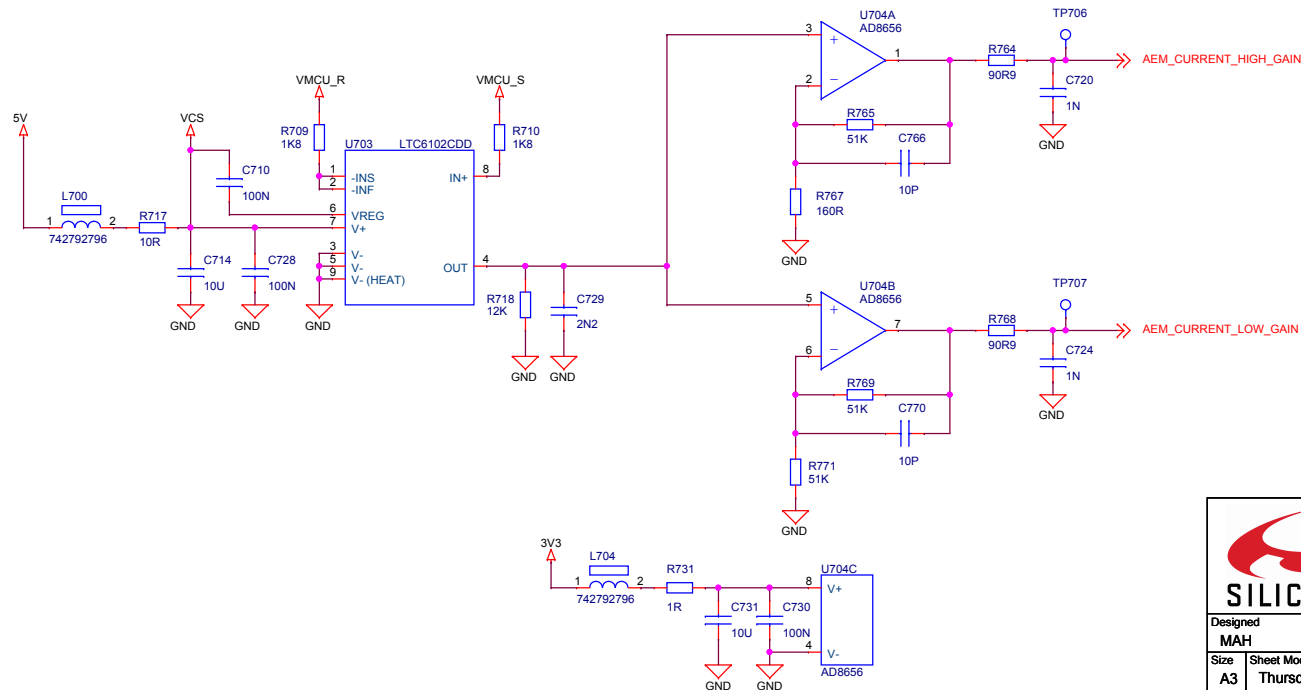
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MCU Power Regulator



AEM CALIBRATE	Current
0x1	3.30 uA
0x2	132.5 uA
0x4	323.5 uA
0x6	456.1 uA
0x8	12.36 mA

Advanced Energy Monitoring

Designed
MAH

Size	SI
A3	

Approved	RGU
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February

	Board Name
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EFM32 Giant Gecko Starter Kit

Page Title

Target Voltage Supply & AEM

Board Number	
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BRD2200C

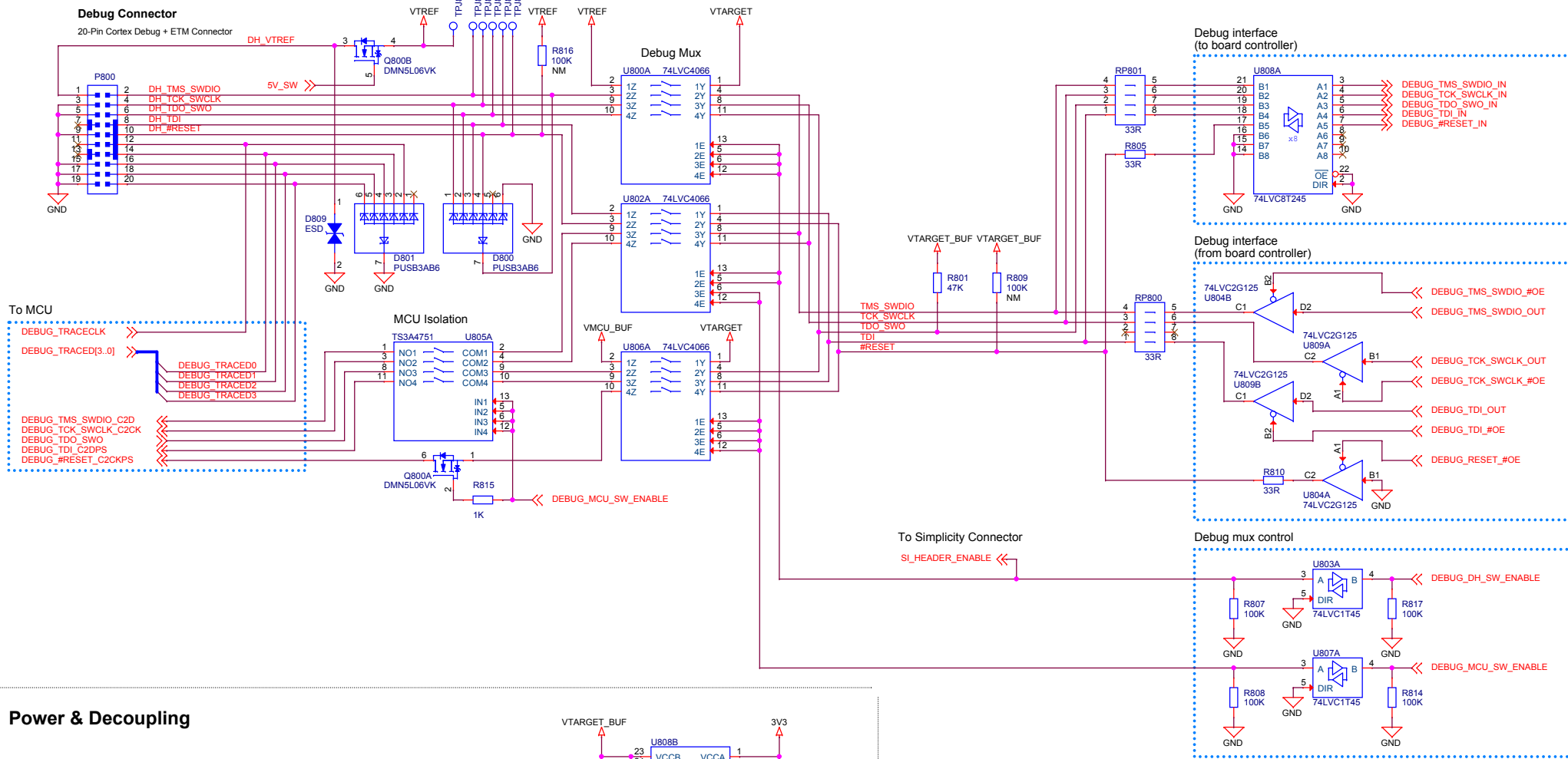
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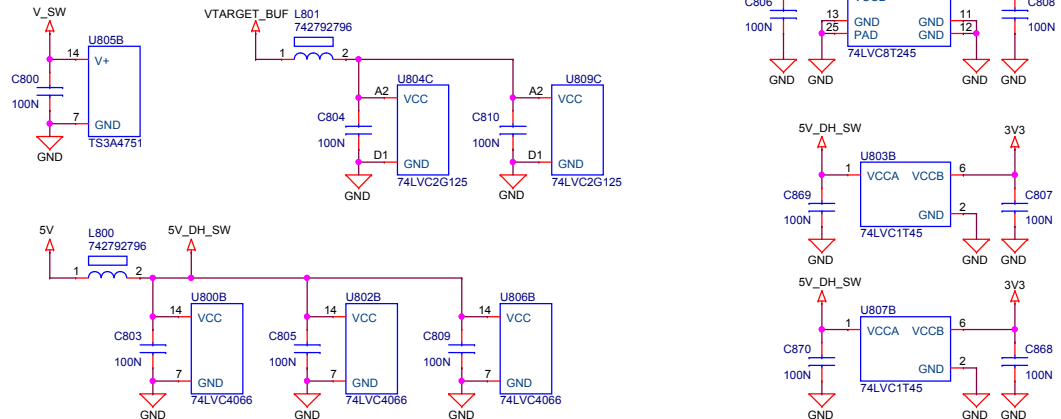
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Debug Mux



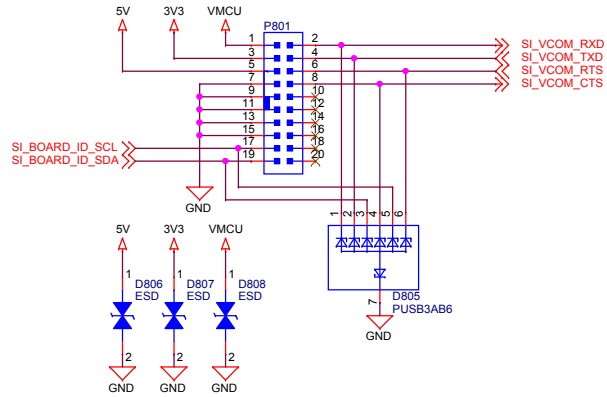
Power & Decoupling



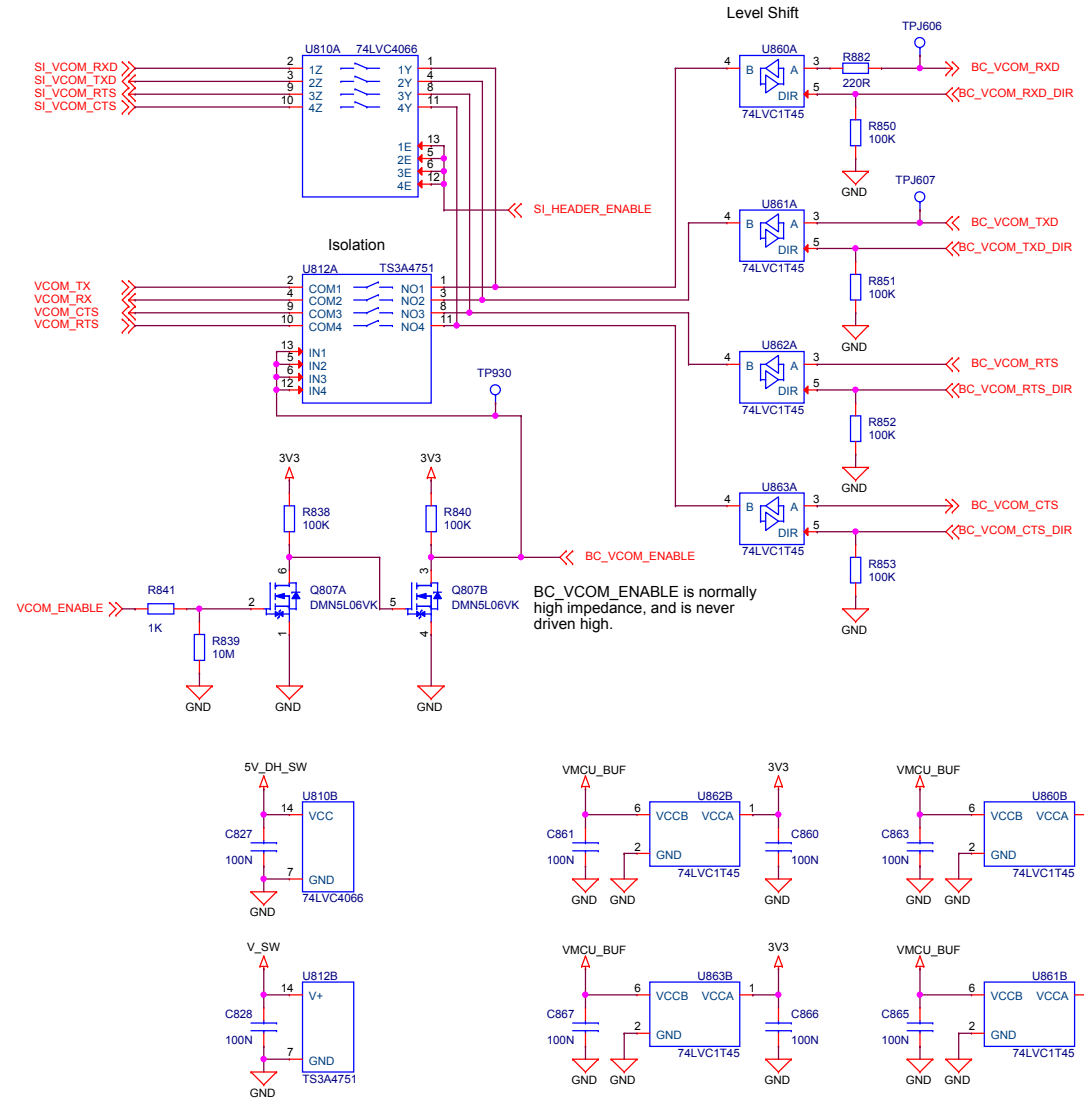
Debug Mode	DH_SW_ENABLE	MCU_SW_ENABLE	Debug Mode	VTARGET Source	VTREF Source
MCU	0	1	MCU	VMCU	None
Debug Out	1	0	Debug Out	VTREF (EXT)	External
Debug In	1	1	Debug In	VMCU	VMCU_BUF
Debug Off	0	0	Debug Off	None	None


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Simplicity Connector

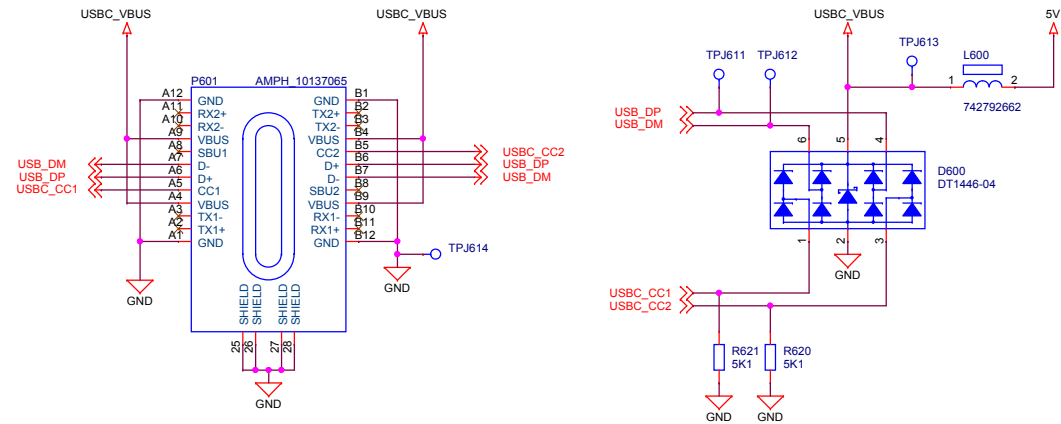


VCOM Interface

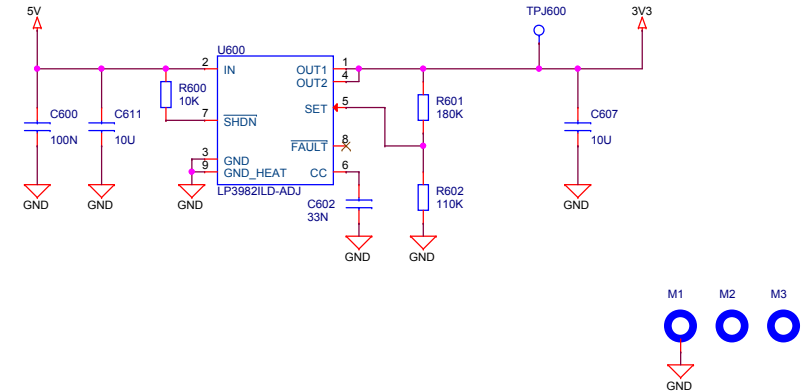


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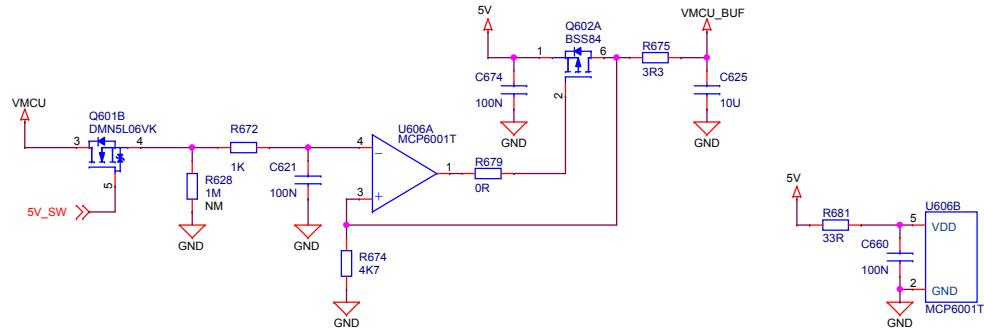
J-Link USB Port



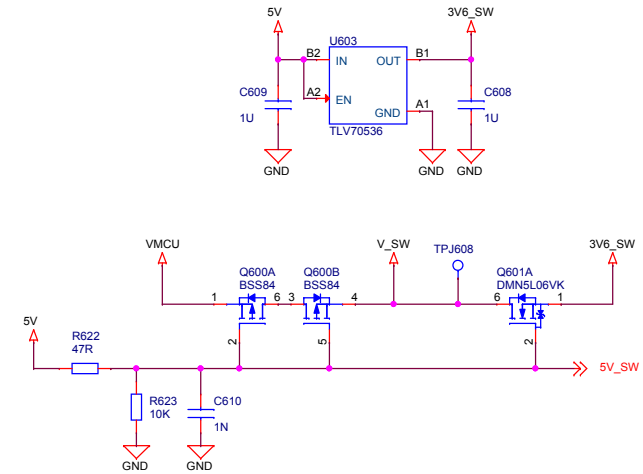
3V3 Regulator



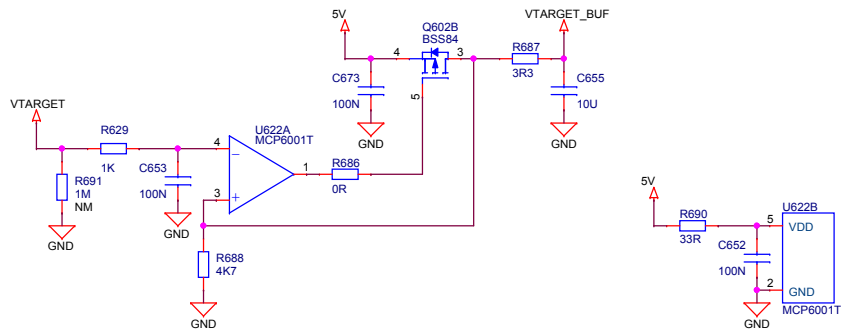
VMCU Voltage Mirror



Power Supply for Analog Switches



VTarget Voltage Mirror



J-Link USB Cable	Q600 State	Q601A State	V_SW	VMCU_SENSE
Connected	OFF	ON	3.6V VMCU	VMCU Isolated
Disconnected	ON	OFF	3.6V VMCU	VMCU Isolated

Isolation switches are powered by 3V6_SW when the USB cable is connected, otherwise by VMCU.

		Board Name	
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The schematic diagram illustrates the Board Controller, featuring three U900A, U900B, and U900C chips. The connections are as follows:

- U900A:**
 - PA0-PA15:** PA0, PA1, PA2, PA3, PA4 / UART0_RX #2, PA5, PA6, PA7, PA8, PA9, PA10, PA11, PA12, PA13, PA14, PA15.
 - PB0-PB15:** PB0, PB1, PB2, PB3, PB4, PB5, PB6, PB7, PB8, PB9, PB10 / ADC1_CH26, PB11 / VDAG0_OUT0, PB12, PB13 / HFXTAL_P, PB14 / HFXTAL_N, PB15 / ETM_TD2 #1.
 - Other pins:** C1, C2, D1, D2, E1, E2, E3, E4, E5, E6, E7, E8, E9, E10, E11, E12, E13, E14, E15, F1, F2, F3, F4, F5, F6, F7, F8, F9, F10, F11, F12, F13, F14, F15, G1, G2, G3, G4, G5, G6, G7, G8, G9, G10, G11, G12, G13, G14, G15, H1, H2, H3, H4, H5, H6, H7, H8, H9, H10, H11, H12, H13, H14, H15, I1, I2, I3, I4, I5, I6, I7, I8, I9, I10, I11, I12, I13, I14, I15, J1, J2, J3, J4, J5, J6, J7, J8, J9, J10, J11, J12, J13, J14, J15, K1, K2, K3, K4, K5, K6, K7, K8, K9, K10, K11, K12, K13, K14, K15, L1, L2, L3, L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, M1, M2, M3, M4, M5, M6, M7, M8, M9, M10, M11, M12, M13, M14, M15, N1, N2, N3, N4, N5, N6, N7, N8, N9, N10, N11, N12, N13, N14, N15, O1, O2, O3, O4, O5, O6, O7, O8, O9, O10, O11, O12, O13, O14, O15, P1, P2, P3, P4, P5, P6, P7, P8, P9, P10, P11, P12, P13, P14, P15, Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11, Q12, Q13, Q14, Q15, R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S12, S13, S14, S15, T1, T2, T3, T4, T5, T6, T7, T8, T9, T10, T11, T12, T13, T14, T15, U1, U2, U3, U4, U5, U6, U7, U8, U9, U10, U11, U12, U13, U14, U15, V1, V2, V3, V4, V5, V6, V7, V8, V9, V10, V11, V12, V13, V14, V15, W1, W2, W3, W4, W5, W6, W7, W8, W9, W10, W11, W12, W13, W14, W15, X1, X2, X3, X4, X5, X6, X7, X8, X9, X10, X11, X12, X13, X14, X15, Y1, Y2, Y3, Y4, Y5, Y6, Y7, Y8, Y9, Y10, Y11, Y12, Y13, Y14, Y15, Z1, Z2, Z3, Z4, Z5, Z6, Z7, Z8, Z9, Z10, Z11, Z12, Z13, Z14, Z15.
- U900B:**
 - PC0-PC15:** PC0 / USART2_RTS #0, PC1 / USART2_CTS #0, PC2 / USART2_TX #0, PC3 / USART2_RX #0, PC4, PC5, PC6 / I2C0_SDA #2, PC7 / I2C0_SCL #2, PC8, PC9, PC10, PC11, PC12, PC13, PC14 / USART1_CS #3, PC15 / USART1_CLK #3.
 - PD0-PD15:** PD0, PD1, PD2 / ADC0_CH2, PD3 / ADC0_CH3, PD4, PD5 / ADC0_CH5, PD6 / ADC_EXT, PD7, PD8, PD9 / USART4_TX #1, PD10 / USART4_RX #1, PD11 / USART4_CLK #1, PD12 / USART4_CS #1, PD13 / ETM_TD1 #1, PD14, PD15.
 - Other pins:** K1, K2, K3, K4, K5, K6, K7, K8, K9, K10, K11, K12, K13, K14, K15, L1, L2, L3, L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, M1, M2, M3, M4, M5, M6, M7, M8, M9, M10, M11, M12, M13, M14, M15, N1, N2, N3, N4, N5, N6, N7, N8, N9, N10, N11, N12, N13, N14, N15, O1, O2, O3, O4, O5, O6, O7, O8, O9, O10, O11, O12, O13, O14, O15, P1, P2, P3, P4, P5, P6, P7, P8, P9, P10, P11, P12, P13, P14, P15, Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11, Q12, Q13, Q14, Q15, R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, S1, S2, S3, S4, S5, S6, S7, S8, S9, S10, S11, S12, S13, S14, S15, T1, T2, T3, T4, T5, T6, T7, T8, T9, T10, T11, T12, T13, T14, T15, U1, U2, U3, U4, U5, U6, U7, U8, U9, U10, U11, U12, U13, U14, U15, V1, V2, V3, V4, V5, V6, V7, V8, V9, V10, V11, V12, V13, V14, V15, W1, W2, W3, W4, W5, W6, W7, W8, W9, W10, W11, W12, W13, W14, W15, X1, X2, X3, X4, X5, X6, X7, X8, X9, X10, X11, X12, X13, X14, X15, Y1, Y2, Y3, Y4, Y5, Y6, Y7, Y8, Y9, Y10, Y11, Y12, Y13, Y14, Y15, Z1, Z2, Z3, Z4, Z5, Z6, Z7, Z8, Z9, Z10, Z11, Z12, Z13, Z14, Z15.
- U900C:**
 - PE0-PE15:** PE0, PE1, PE2, PE3, PE4, PE5, PE6 / TIM3_CC1 #3, PE7, PE8, PE9, PE10 / USART0_TX #0, PE11 / USART0_RX #0, PE12 / USART0_CLK #0, PE13 / USART0_CS #0, PE14 / LEUART0_TX #2, PE15 / LEUART0_RX #2.
 - PF0-PF15:** PF0 / DBG_SWCLK, PF1 / DBG_SWIO, PF2 / DBG_SWO #0, PF3 / ETM_TD3 #1, PF4, PF5, PF6 / USART1_TX #3, PF7 / TIM0_CC1 #1, PF8 / ETM_TCLK #1, PF9 / ETM_TD0 #1, PF10 / USB_DM, PF11 / USB_DP, PF12.
 - Other pins:** G11, G12, G13, G14, G15, H11, H12, H13, H14, H15, I11, I12, I13, I14, I15, J11, J12, J13, J14, J15, K11, K12, K13, K14, K15, L11, L12, L13, L14, L15, M11, M12, M13, M14, M15, N11, N12, N13, N14, N15, O11, O12, O13, O14, O15, P11, P12, P13, P14, P15, Q11, Q12, Q13, Q14, Q15, R11, R12, R13, R14, R15, S11, S12, S13, S14, S15, T11, T12, T13, T14, T15, U11, U12, U13, U14, U15, V11, V12, V13, V14, V15, W11, W12, W13, W14, W15, X11, X12, X13, X14, X15, Y11, Y12, Y13, Y14, Y15, Z11, Z12, Z13, Z14, Z15.

Additional components and connections include:

- TPJ900-TPJ958:** Test points for various signals like TEST_USB_ADDR0, TEST_USB_ADDR1, TEST_USB_ADDR2, BC_DISP_COM, BC_UIF_BUTTON0, BC_UIF_BUTTON1, BC_SPI_COPI, BC_SPI_CPO, BC_SPI_SCLK, BC_SPI_CS, TEST_BC_TXD, TEST_BC_RXD, BC_DBG_TCK_SWCLK, BC_DBG_TMS_SWIO, BC_DBG_TDO_SWIO, BC_TRACED3, BC_TRACECLK, BC_TRACED0, R902 33R, R904 33R, TPJ950, 3V3, GND.
- X900:** 48 MHz crystal.
- EFM32GG12B410F1024GL120-A:** Two identical chips connected to the U900A and U900B chips.

Power & Decoupling

This schematic shows the power and decoupling circuitry for the EFM32GG12B410F1024GL120-A microcontroller. It includes a USB_VBUS input, a USB_VREGI regulator, and various decoupling capacitors (C901-C927) connected to the microcontroller's power pins. A 3V3 supply is also shown with a 74279266 component and a 1R resistor.

Board ID & Button Isolation

This schematic shows the board ID and button isolation circuitry. It includes a BOARD_ID_SDA, BOARD_ID_SCL, and BOARD_ID_WP input. The circuit also shows button inputs (BC_UIF_BUTTON0, BC_UIF_BUTTON1, BC_I2C_EXP_ENABLE, BC_BUTTON_ENABLE) connected to U901A and U950A. A 3V3 supply is shown with a 10N capacitor and a 100K resistor.

BC Serial Flash

This schematic shows the BC Serial Flash circuitry. It includes a BC_SPI_COP1, BC_SPI_SCLK, BC_SPI_CS, and BC_SPI_CPO input. The circuit also shows BC_I2C_EXP_SDA and BC_I2C_EXP_SCL inputs connected to U902A and U902B. A 3V3 supply is shown with a 100N capacitor and a 10K resistor.

Board Version

This schematic shows the board version circuitry. It includes a BOARD_VER0 and BOARD_VER1 input connected to R931 and R930. A 3V3 supply is shown with a 1K resistor.

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Size A3	Sheet Modified Date Thursday, February 10, 2022		Revision A01
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Board Name

EFM32 Giant Gecko Starter Kit

Page Title

Board Controller

Board Number

BRD2200C

Revision

A01

Board Name

EFM32 Giant Gecko Starter Kit

Page Title

Board Controller

Board Number

BRD2200C

Revision

A01

[illegible]

BC Serial Flash

U902A: MX25R8035F

U902B: MX25R8035F

R906: 10K

R931: 1K

R930: 1K

C914: 100N

3V3

GND

BC_SPL_COP1

BC_SPL_SCLK

BC_SPL_CS

BC_SPL_CIPO

BOARD_VER0

BOARD_VER1

2

1

BC Serial Flash

U902A: MX25R8035F

U902B: MX25R8035F

3V3

BC_SPI_COP1

BC_SPI_SCLK

R906 10K

CS#

SCLK

SI / SIO0

SO / SIO1

WP# / SIO2

RESET# / SIO3

C3

C914 100N

A1

B2

GND

BOARD_VER0

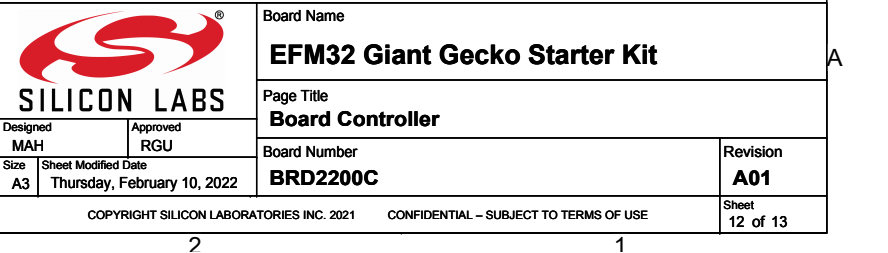
BOARD_VER1

R931 1K

R930 1K

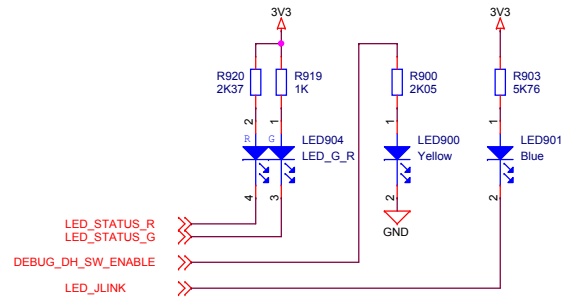
GND

GND

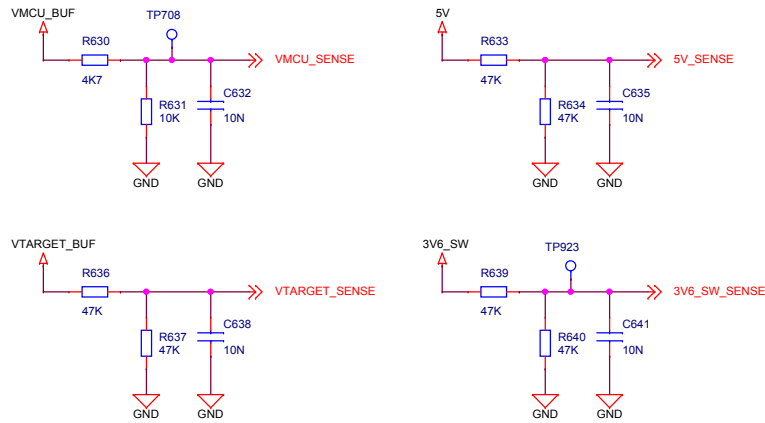


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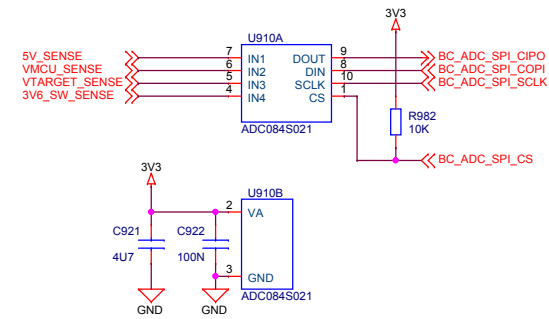
Indicator LEDs



BC Voltage Sense



BC Voltage Sense ADC



		Board Name	
		EFM32 Giant Gecko Starter Kit	
Designed MAH		Page Title	
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