



EFM32PG22 Dev Kit	
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Revision History	
Rev.	Description
A00	Initial version
A01	Changed LF crystal (X2), ESD diodes (D902), and updated PCB revision
A02	Updated EFM32PG22 with new production revision



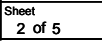
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C



A



Sensor Power/Isolation

There are no pull-down resistors on the enable pins, and software should therefore immediately drive the lines to prevent the pins from floating.

SPI Flash

The SPI bus is shared with the IMU.

6-axis Inertial Sensor (IMU)

The SPI bus is shared with the flash memory.

Sensor Power/Isolation

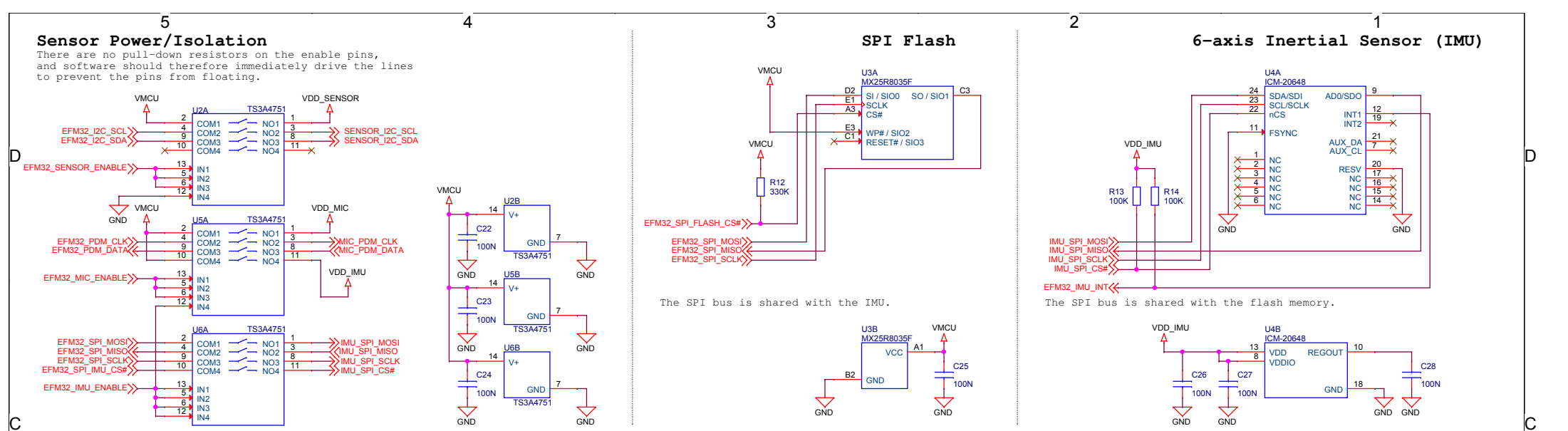
There are no pull-down resistors on the enable pins, and software should therefore immediately drive the lines to prevent the pins from floating.

SPI Flash

The SPI bus is shared with the IMU.

6-axis Inertial Sensor (IMU)

The SPI bus is shared with the flash memory.

[illegible]

Left mic
(data is sampled on the rising clock edge)

Right mic
(data is sampled on the falling clock edge)

Push Buttons & LEDs

Breakout Pads - EXP Header

All signals routed to the EXP header are used on the board as well.
Take care to avoid signal conflicts (see page 2 for signal assignments).

5
4
3
2
1

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Designed FRM Approved JSH

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5
4
3
2
1

Push Buttons & LEDs

Breakout Pads - EXP Header

All signals routed to the EXP header are used on the board as well.
Take care to avoid signal conflicts (see page 2 for signal assignments).

5

4

3

2

1

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Push Buttons & LEDs

VMCU

R25 1M

SW2 PTS810

C38 1N

R26 100R

GND

EXP_HEADER[16..3]

EXP_HEADER3 PA08

EXP_HEADERS PA07

EXP_HEADER7 PB00

EXP_HEADERS PB01

EXP_HEADER11 PA04

EXP_HEADERS PB03

EXP_HEADER13 PB03

EXP_HEADER15 PB03

BOARD_ID_SCL

BOARD_ID_SDA

GND

EXP1A

EXP1B

VMCU 5V 3V0

PC00 EXP_HEADER4

PC01 EXP_HEADER6

PC02 EXP_HEADER8

PB02 EXP_HEADER10

PA05 EXP_HEADER12

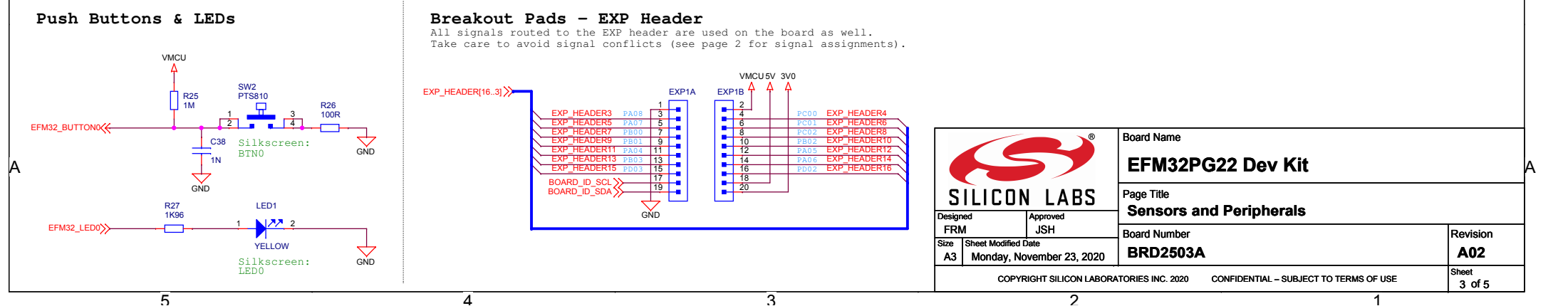
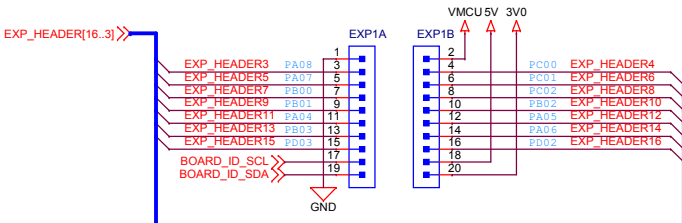
PA06 EXP_HEADER14

PB02 EXP_HEADER16

Breakout Pads - EXP Header

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Push Buttons & LEDs

VMCU

R25 1M

SW2 PTS810

C38 1N

R26 100R

R27 1K96

LED1

EFM32_BUTTON0

EFM32_LED0

Silkscreen: BTN0

Silkscreen: LED0

5

Breakout Pads - EXP Header

All signals routed to the EXP header are used on the board as well.
Take care to avoid signal conflicts (see page 2 for signal assignments).

EXP_HEADER[16..3]

EXP1A

EXP1B

VMCU 5V 3V0

EXP_HEADER3 PA08

EXP_HEADERS PA07

EXP_HEADER7 PB00

EXP_HEADERS PB01

EXP_HEADER11 PA04

EXP_HEADERS PB03

EXP_HEADER13 PB03

EXP_HEADER15 PB03

BOARD_ID_SCL

BOARD_ID_SDA

EXP1B

PC00 EXP_HEADER4

PC01 EXP_HEADER6

PC02 EXP_HEADER8

PB02 EXP_HEADER10

PA05 EXP_HEADER12

PA06 EXP_HEADER14

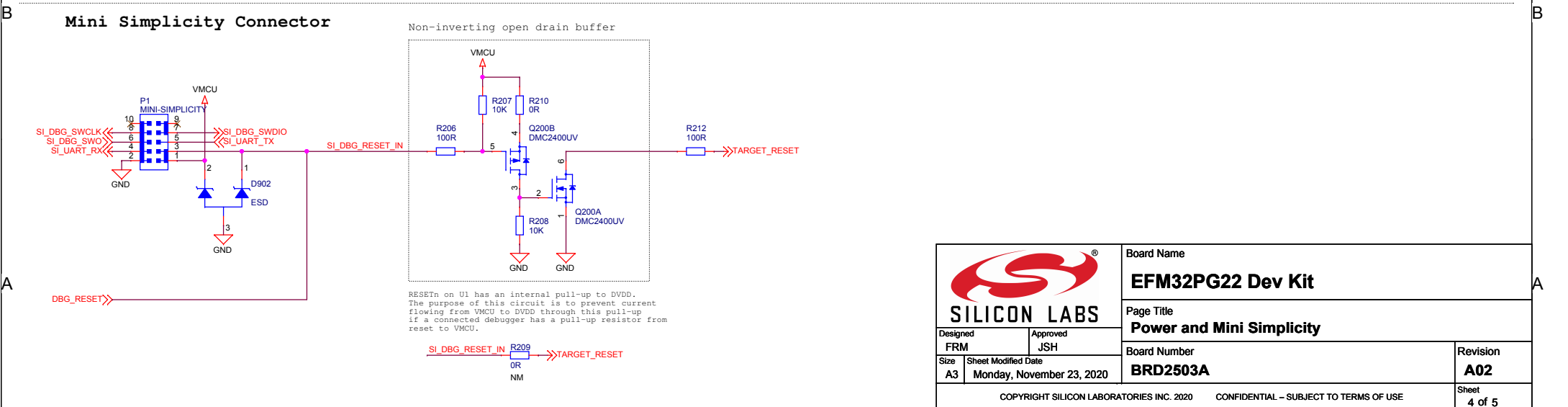
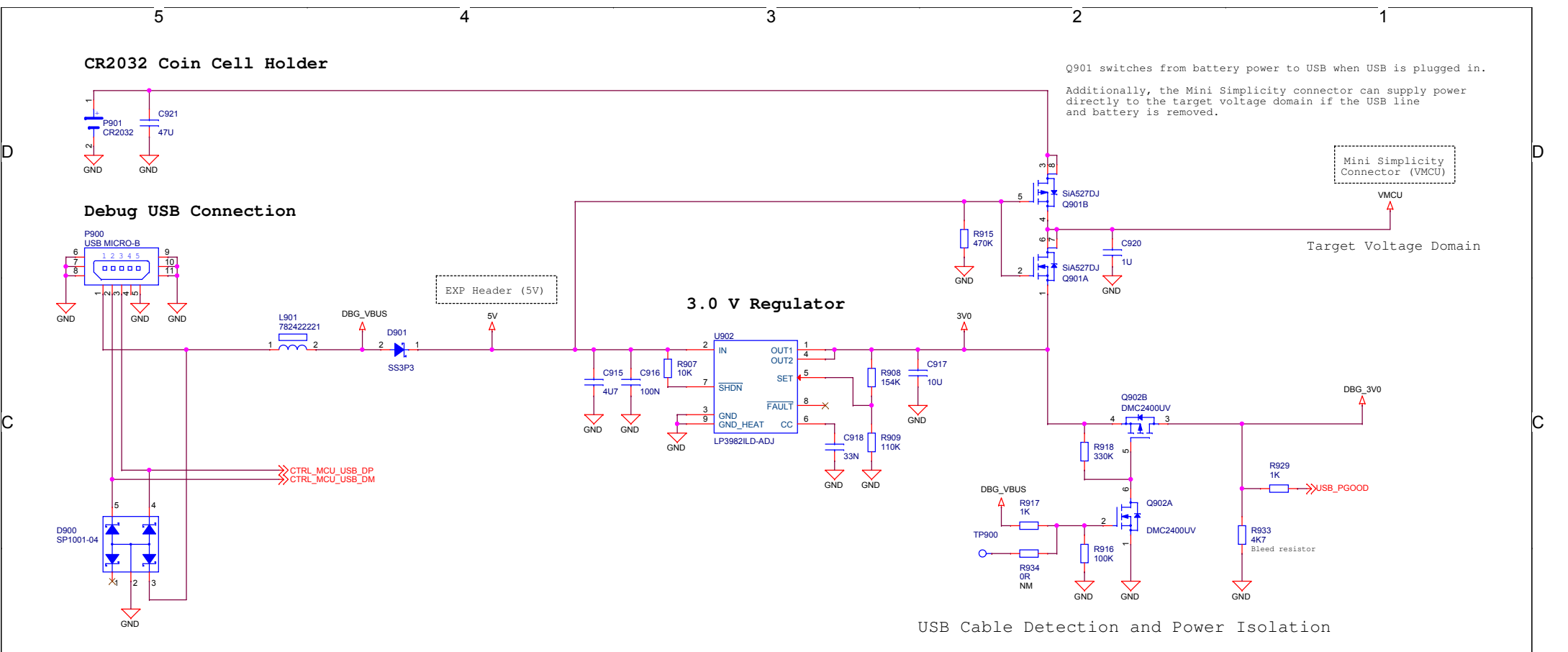
PB02 EXP_HEADER16

3

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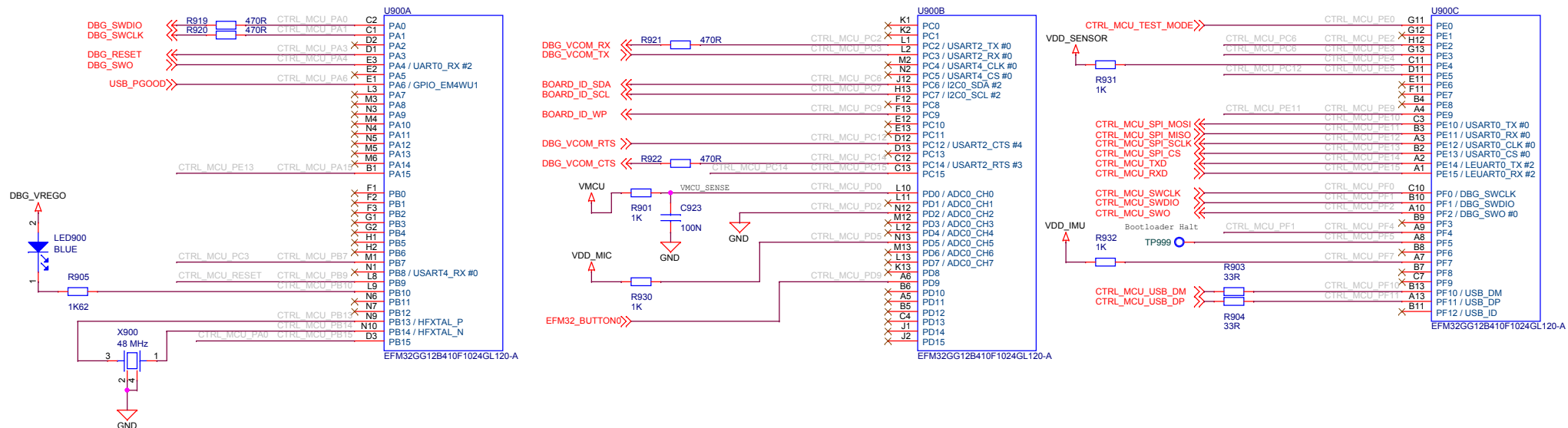
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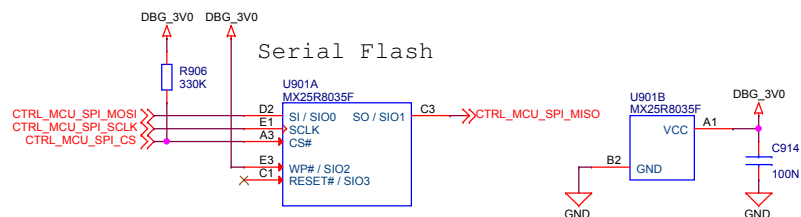


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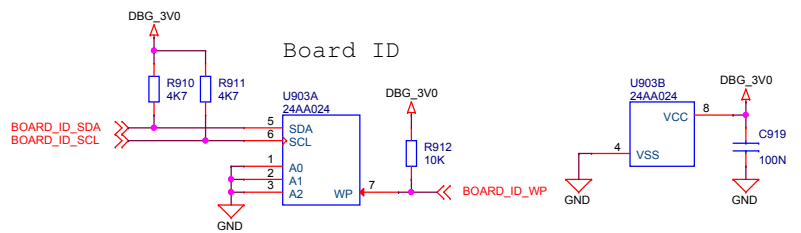
On-board Debugger



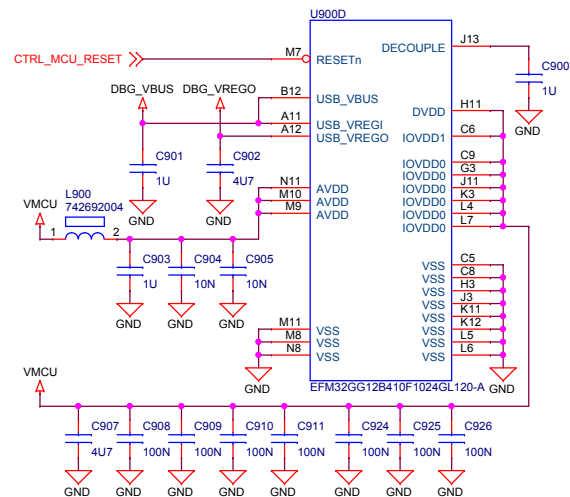
Serial Flash



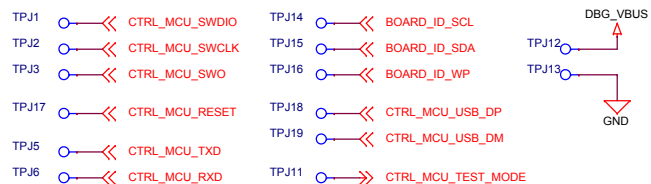
Board ID



On-board Debugger Power



Test Points



Mech



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