EFR32BG27 +8 dBm DK

Revision History

Rev.	Description
A00	Initial Release
A01	Updated matching network

PCB1

PCB2602A

SILICON LABS

Designed: PEP	Approved: RGU
Size: A3	Sheet Modified Date: Thursday, November 24, 2022

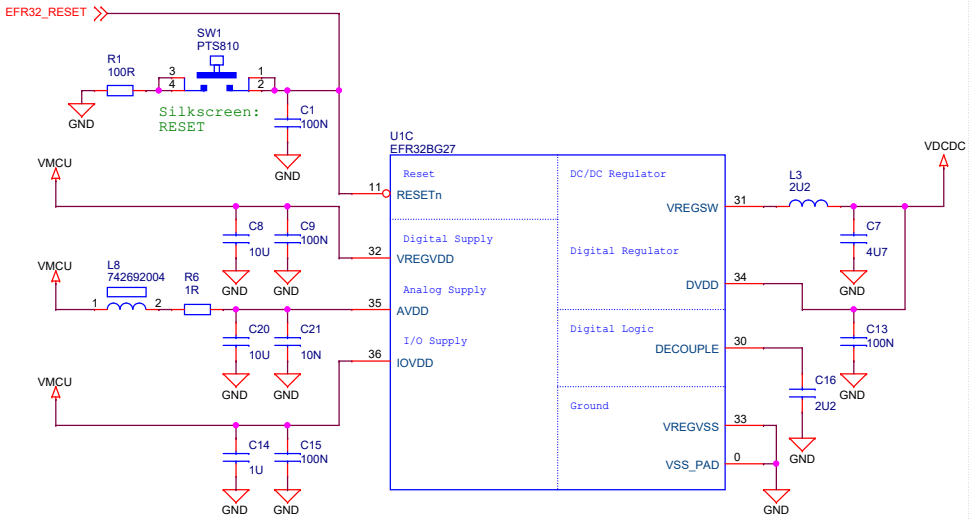
Board Name: **EFR32BG27 +8 dBm DK**

Page Title: **Title Page**

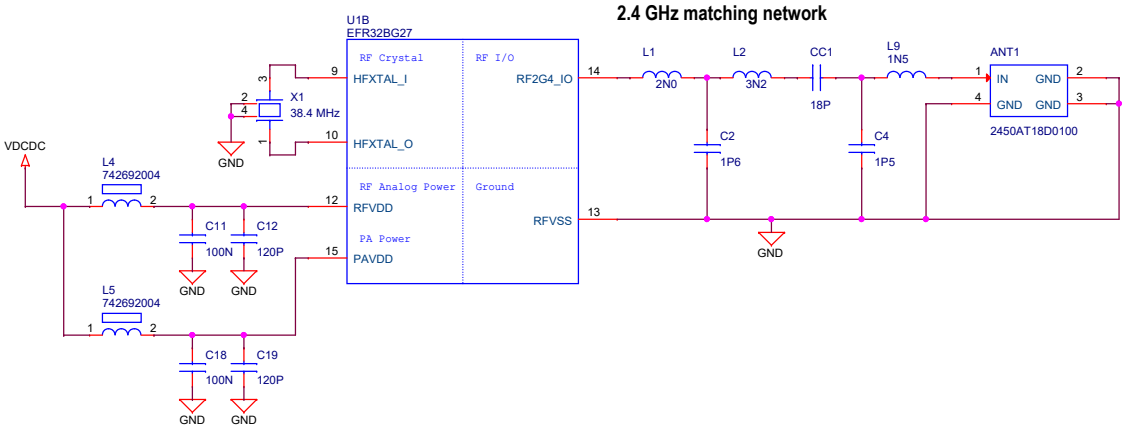
Board Number: **BRD2602A**

Revision: **A01**

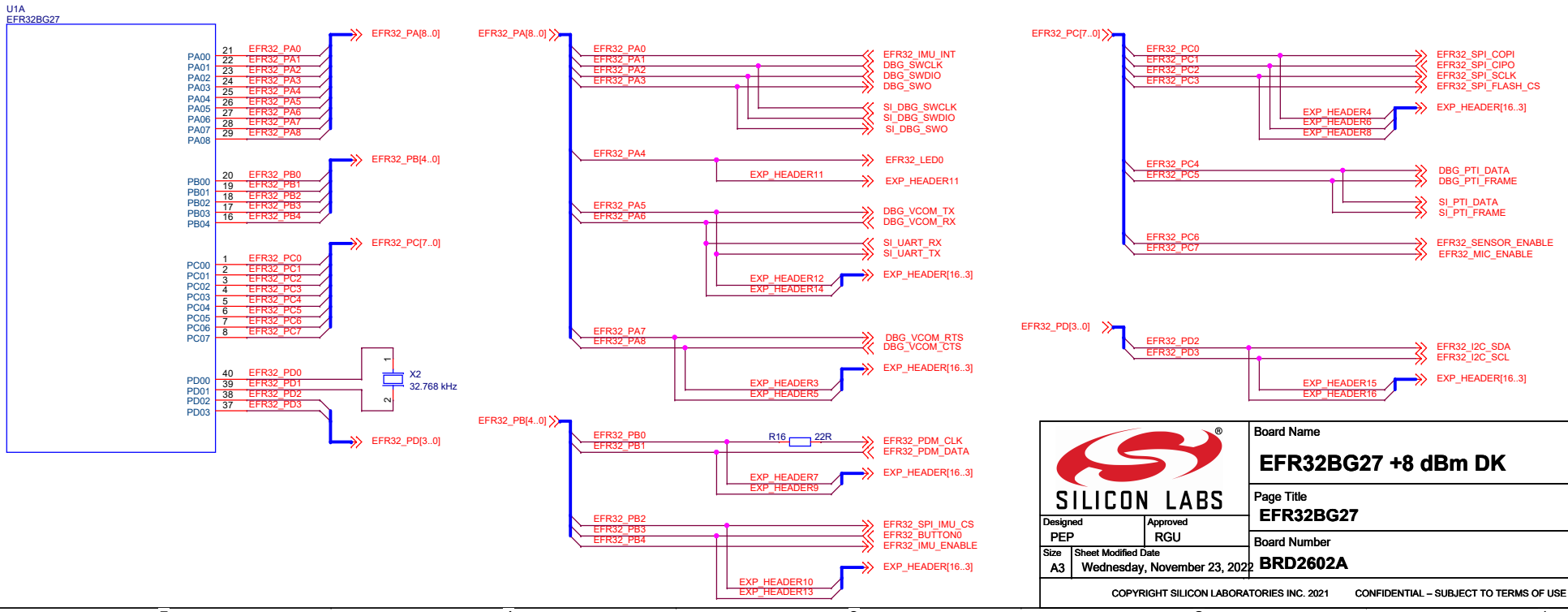
EFR32BG27 Power Section



EFR32BG27 RF Section



EFR32BG27 IOs & Signal Assignments



SILICON LABS

Designed	PEP
Approved	RGU
Size	A3
Sheet Modified Date	Wednesday, November 23, 2022

Board Name
EFR32BG27 +8 dBm DK

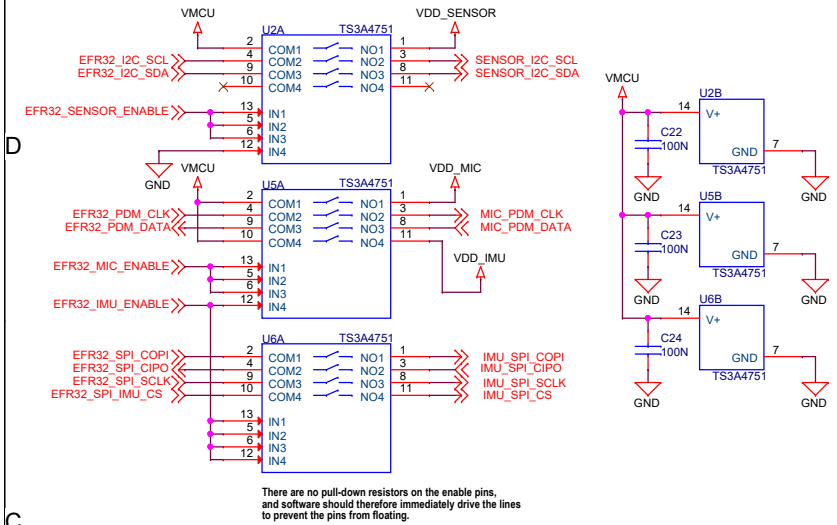
Page Title
EFR32BG27

Board Number
BRD2602A

Revision
A01

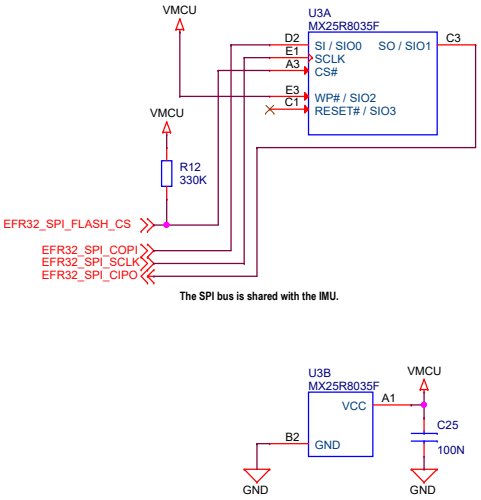
Sheet
2 of 5

Sensor Power/Isolation



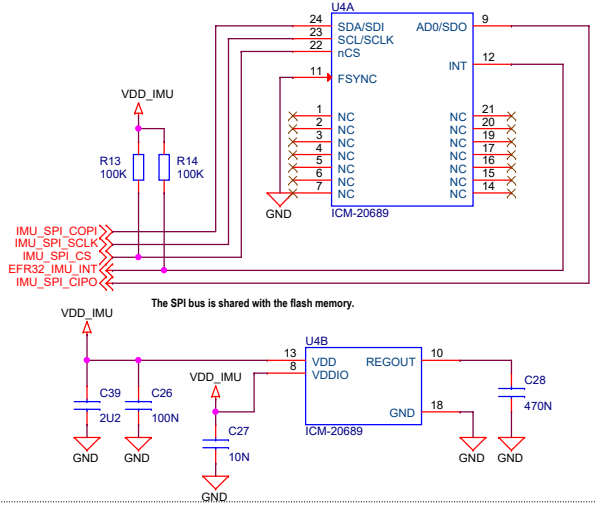
There are no pull-down resistors on the enable pins, and software should therefore immediately drive the lines to prevent the pins from floating.

SPI Flash



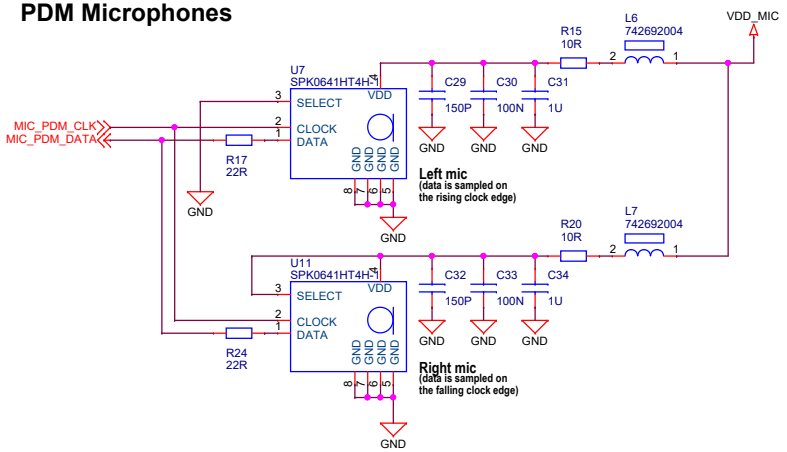
The SPI bus is shared with the IMU.

6-axis Inertial Sensor (IMU)



The SPI bus is shared with the flash memory.

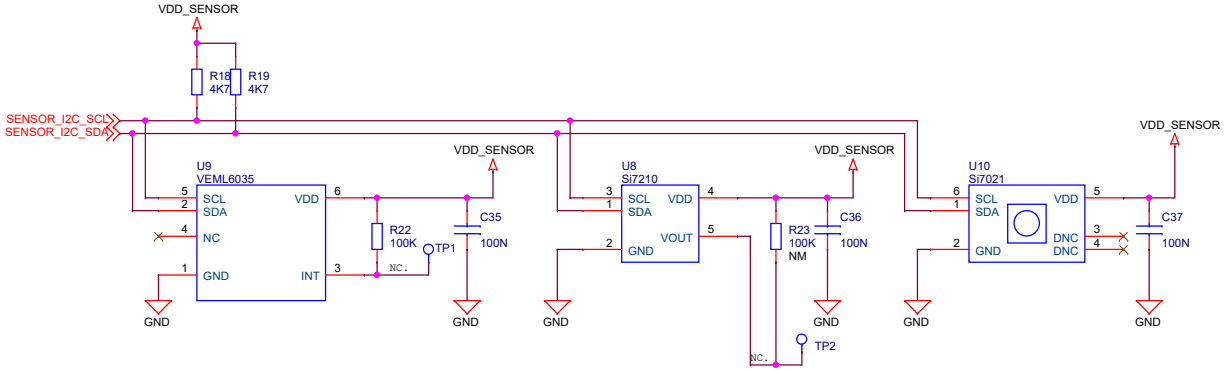
PDM Microphones



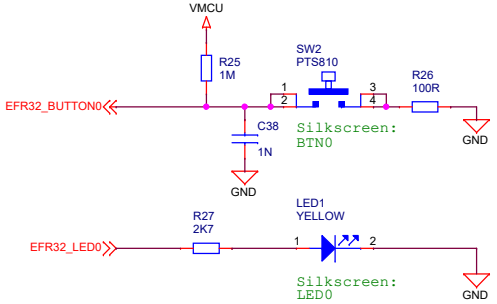
Ambient Light Sensor

Hall-effect Sensor

RH/Temp Sensor

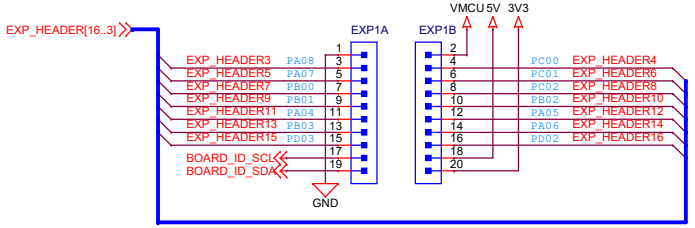


Push Button & LED



Breakout Pads - EXP Header

All signals routed to the EXP header are used on the board as well.
Take care to avoid signal conflicts (see page 2 for signal assignments).



SILICON LABS

Designed	PEP
Approved	RGU
Size	A3
Sheet Modified Date	Wednesday, November 23, 2022

Board Name
EFR32BG27 +8 dBm DK

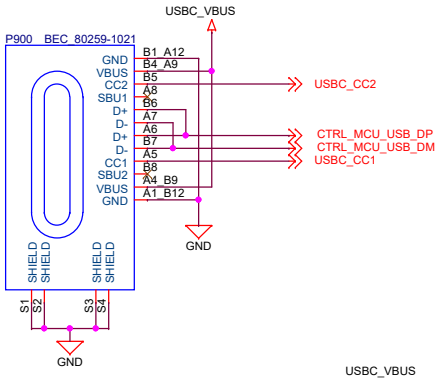
Page Title
Sensors and Peripherals

Board Number
BRD2602A

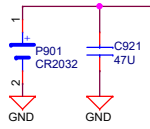
Revision
A01

Sheet
3 of 5

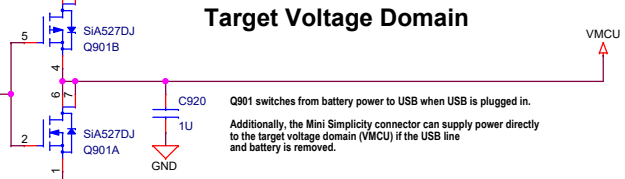
Debug USB Connection



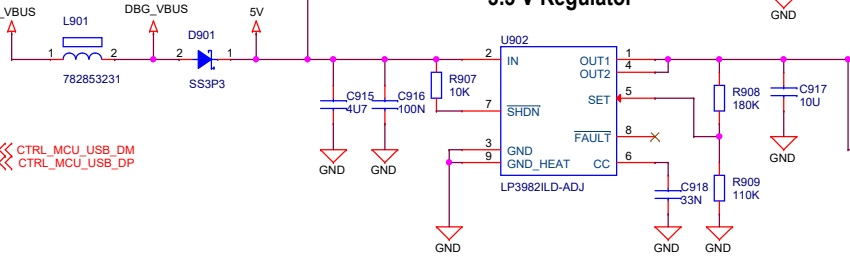
CR2032 Coin Cell Battery Holder



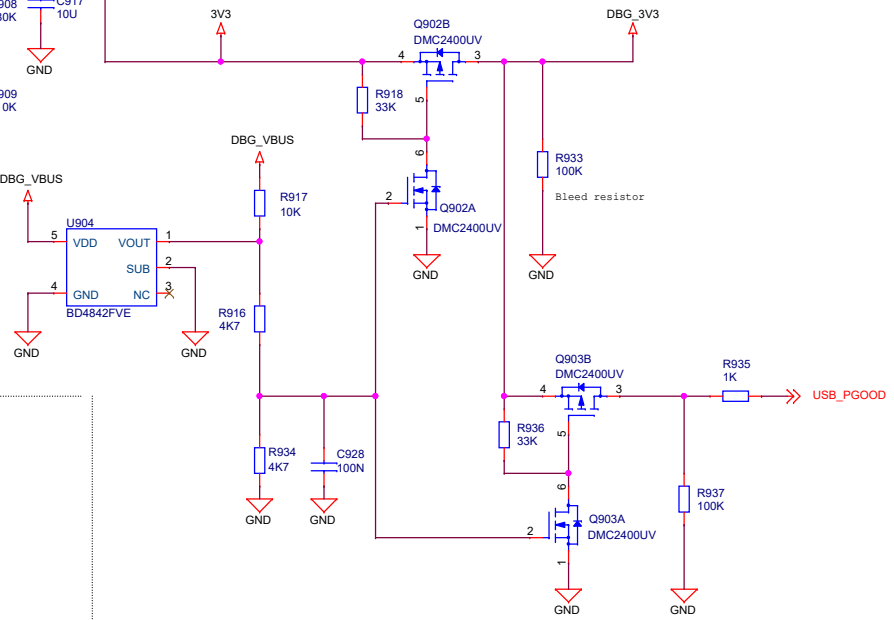
Target Voltage Domain

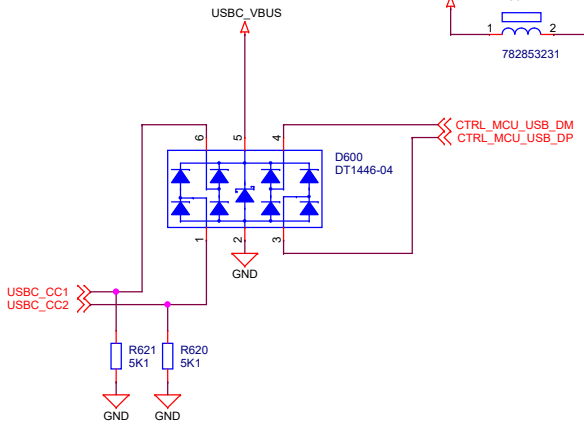


3.3 V Regulator

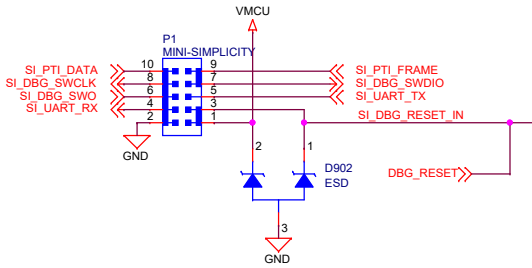


Debugger Power Isolation

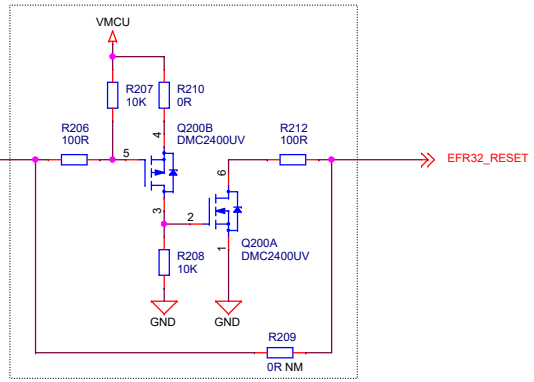




Mini Simplicity Connector



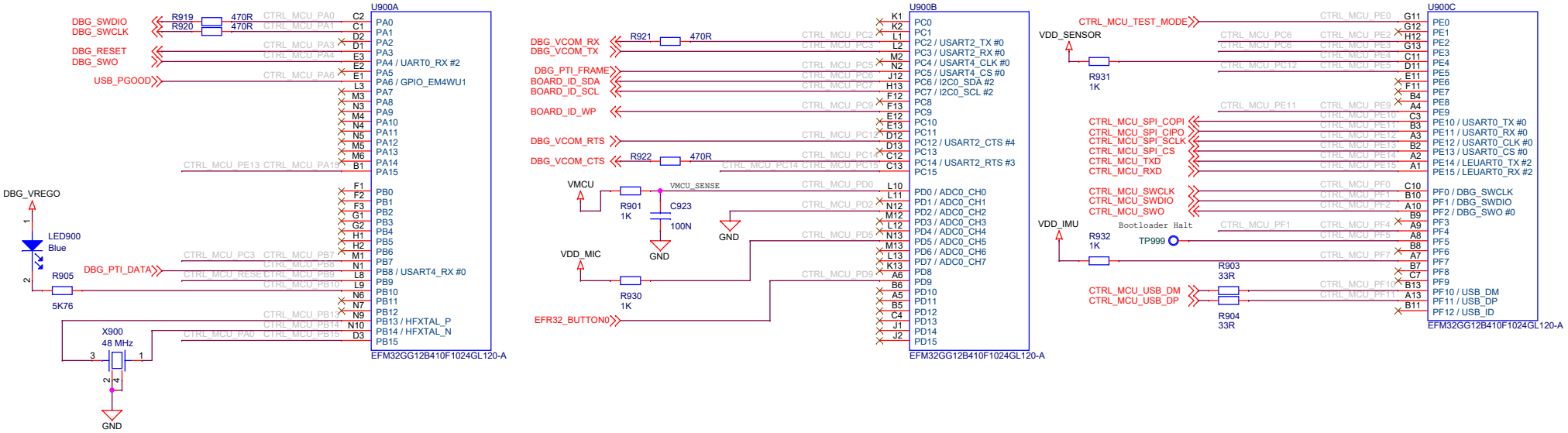
Non-inverting open drain buffer



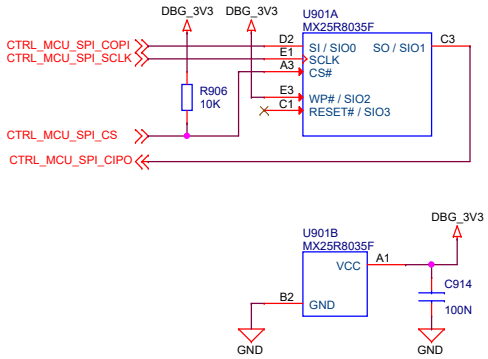
RESETn on U1 has an internal pull-up to DVDD. The purpose of this circuit is to prevent current flowing from VMCU to DVDD through this pull-upif a connected debugger has a pull-up resistor from reset to VMCU.

Board Name	EFR32BG27 +8 dBm DK
Page Title	Power and Mini Simplicity
Designed	PEP
Approved	RGU
Size	A3
Sheet Modified Date	Wednesday, November 23, 2022
Board Number	BRD2602A
Revision	A01
Sheet	4 of 5

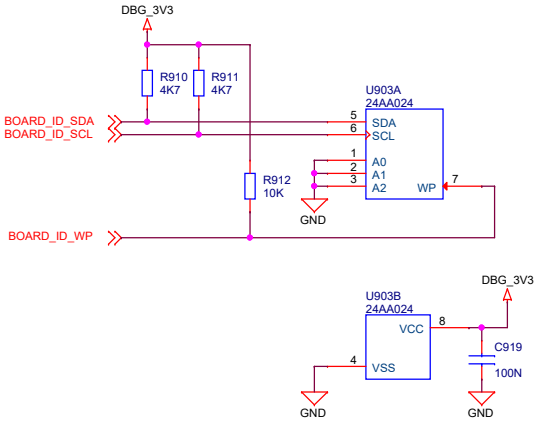
On-board Debugger



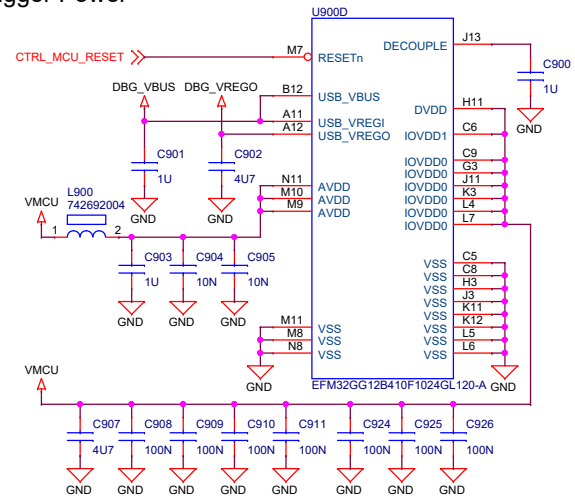
Serial Flash



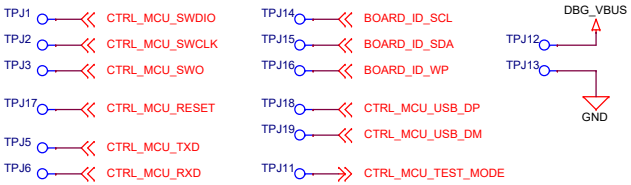
Board ID



On-board Debugger Power



Test Points



Mech

Board Name	**EFR32BG27 +8 dBm DK**
Page Title	**On-board Debugger**
Designed: PEP / Approved: RGU	
Size: A3 / Sheet Modified Date: Wednesday, November 23, 2022	Board Number: **BRD2602A** / Revision: **A01**
	Sheet: 5 of 5