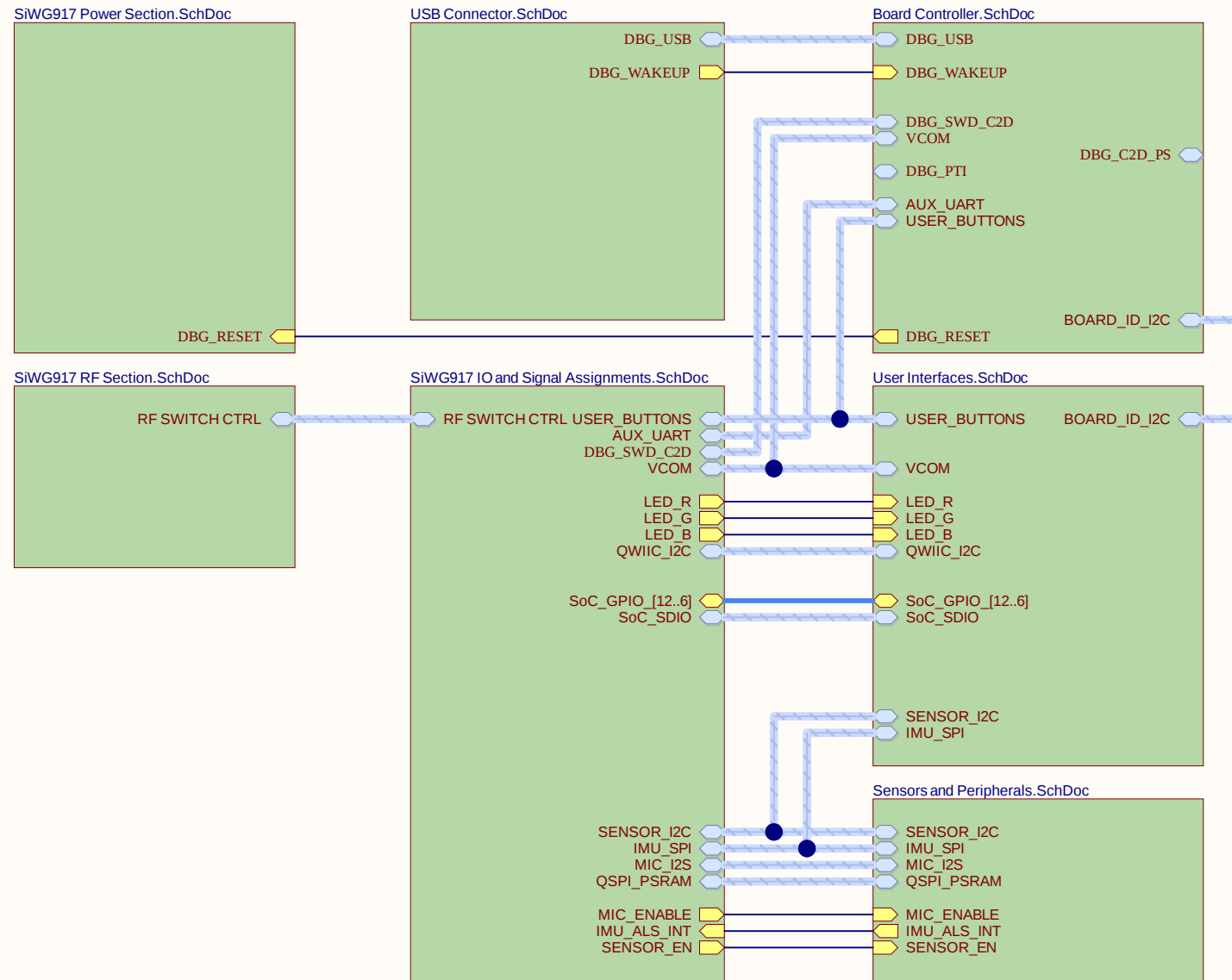


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Page 6	User Interfaces
Page 7	USB Connector and Power
Page 8	Board Controller

REVISION HISTORY:

A02	Initial release
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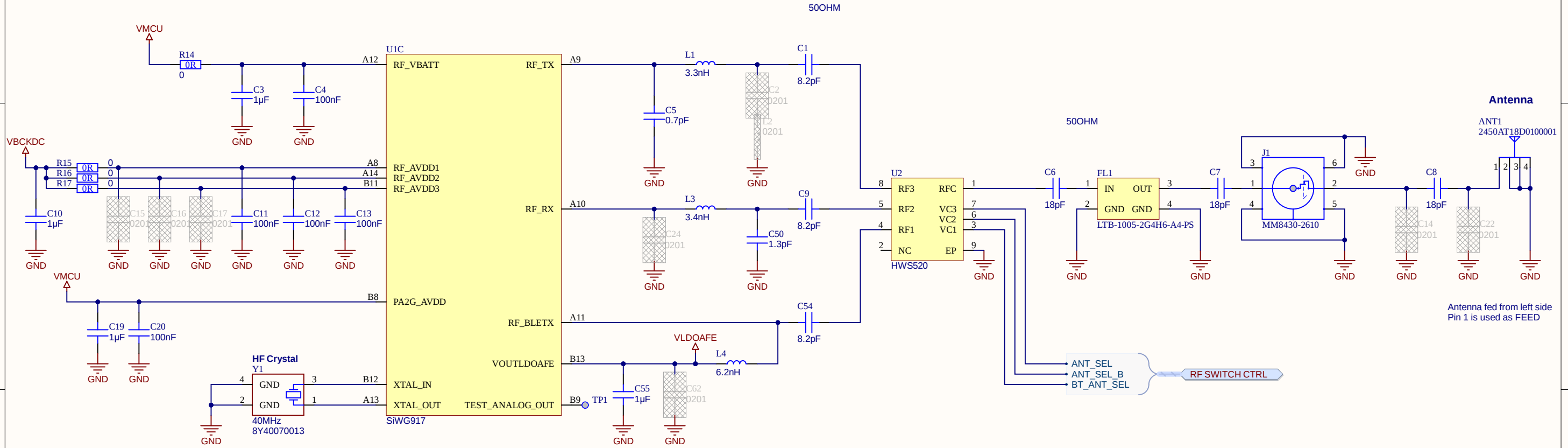
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 SILICON LABS		Board name SiWG917 Dev Kit	
Designed MSV	Approved FRM	Page Title Block Diagram	
Size A3	Sheet Modified Date 2/8/2024	Board number BRD2605A	Revision A02 --
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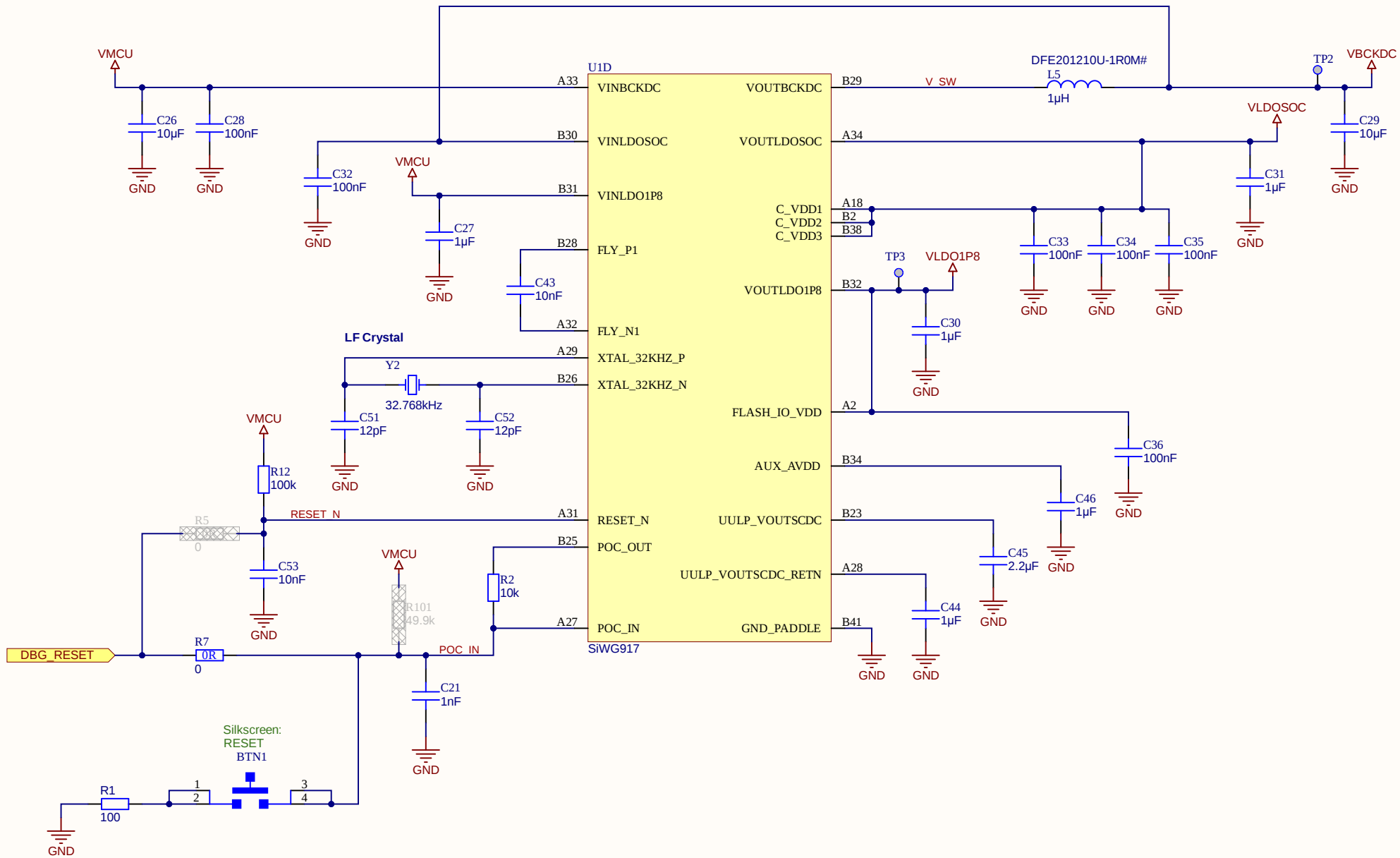
SiWG917 RF Frontend (External RF Switch + Band Pass Filter)



Antenna fed from left side
Pin 1 is used as FEED

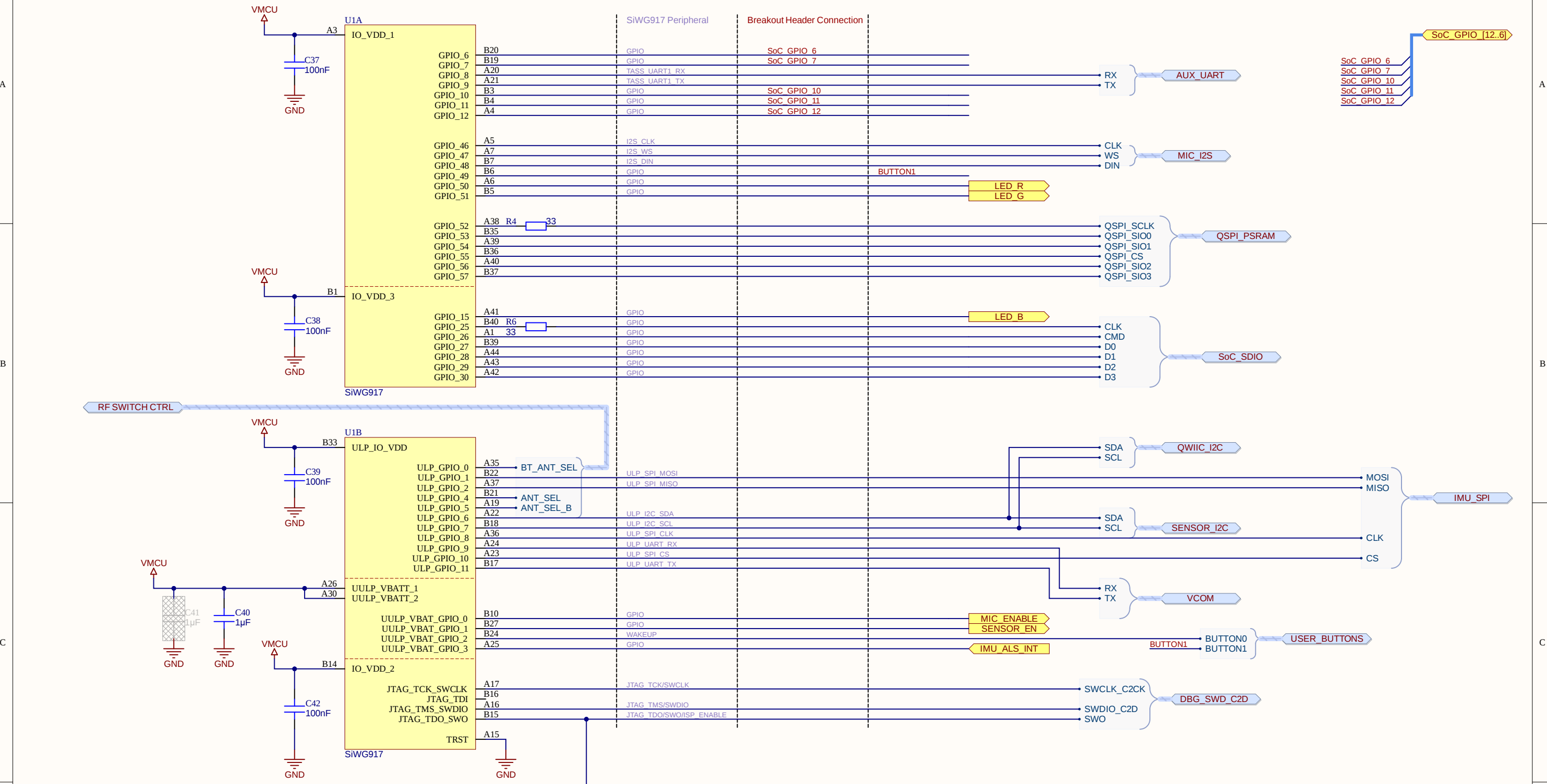
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Designed MSV	Approved FRM	Page Title SiWG917 RF Section	
Size A3	Sheet Modified Date 2/8/2024	Board number BRD2605A	Revision A02 --
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SiWG917 Power Section

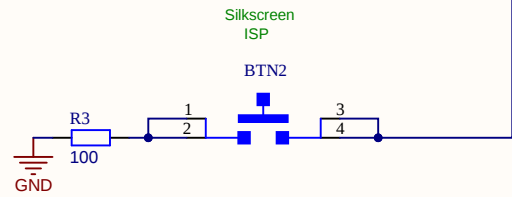


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Designed MSV	Approved FRM	Page Title SiWG917 Power Section	
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SiWG917 I/O and Signal Assignments

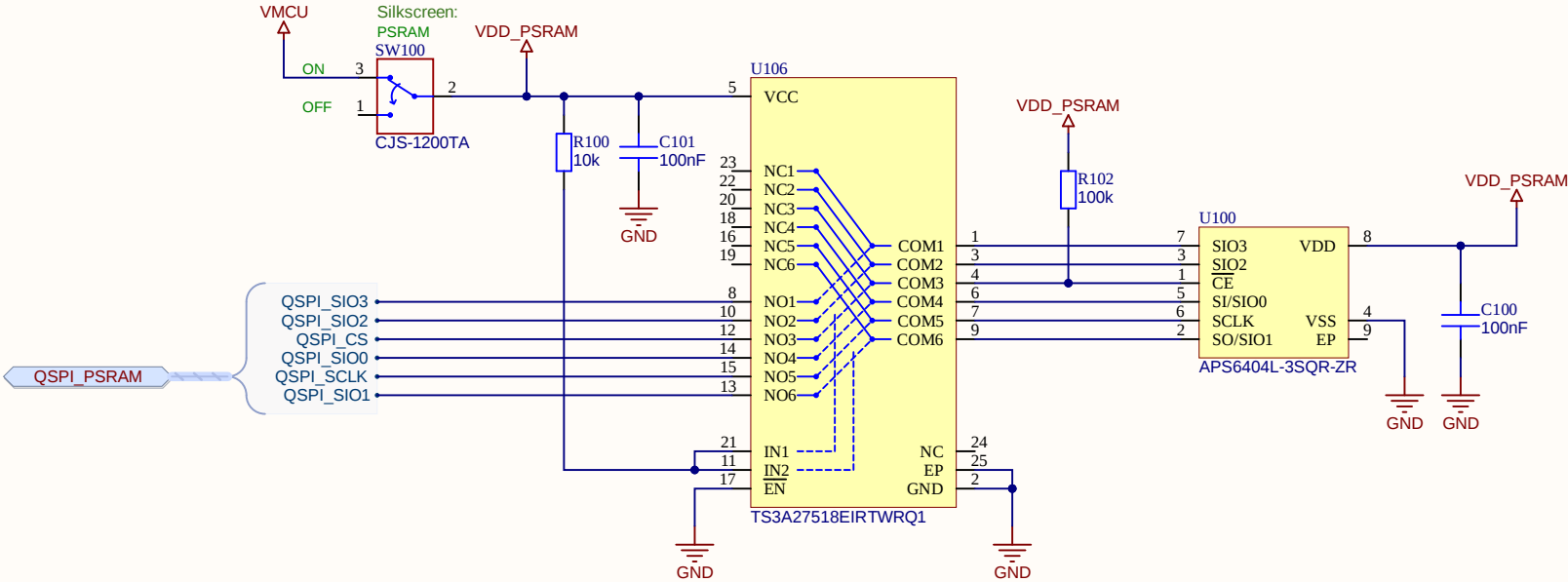


ISP Mode Button

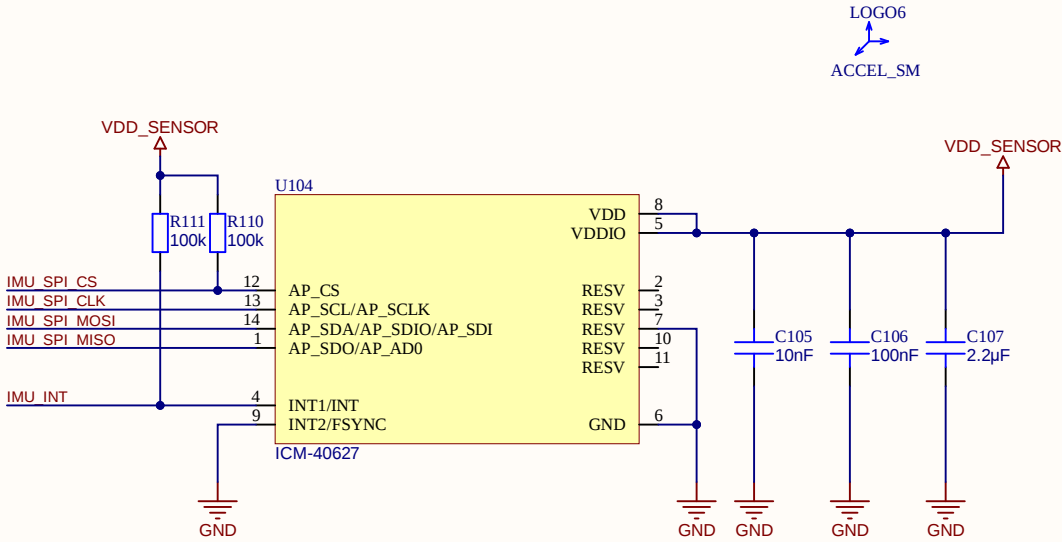


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		Page Title SiWG917 I/O and Signal Assignments	
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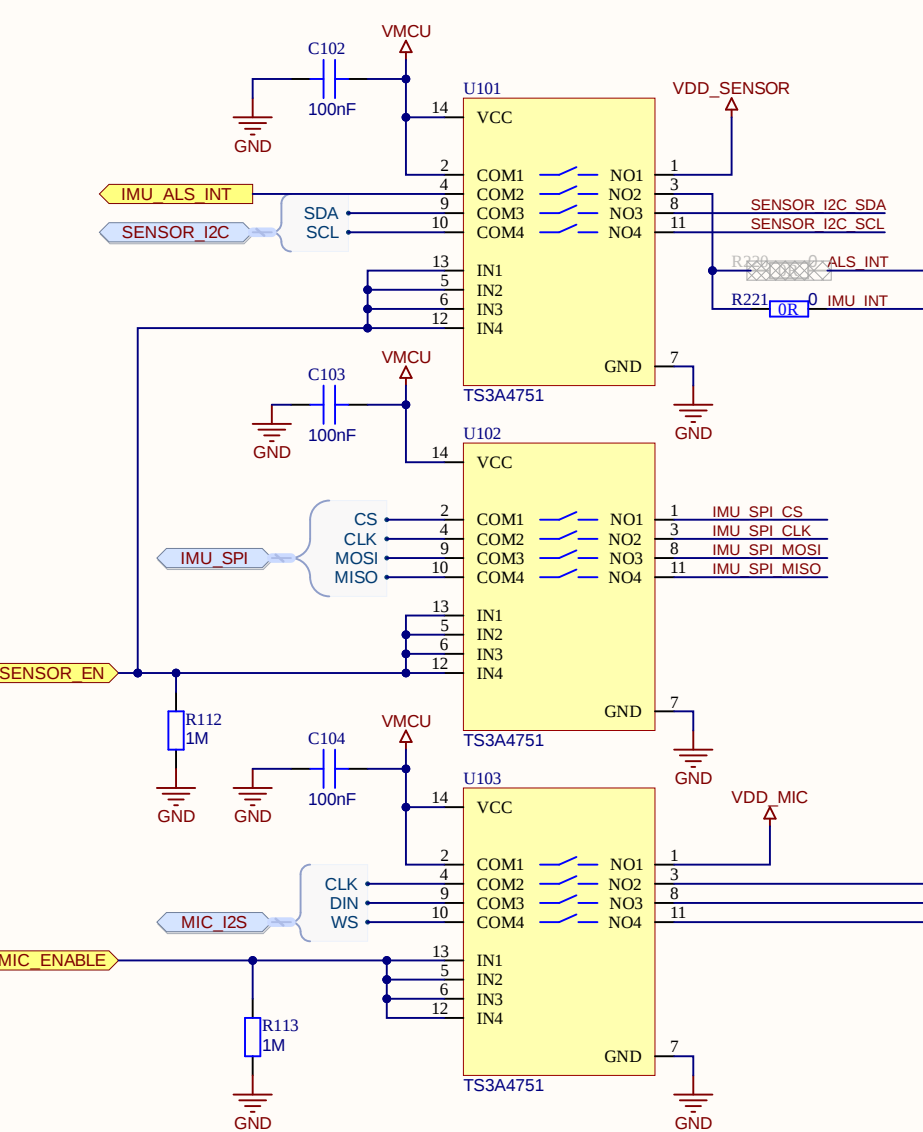
QSPI PSRAM



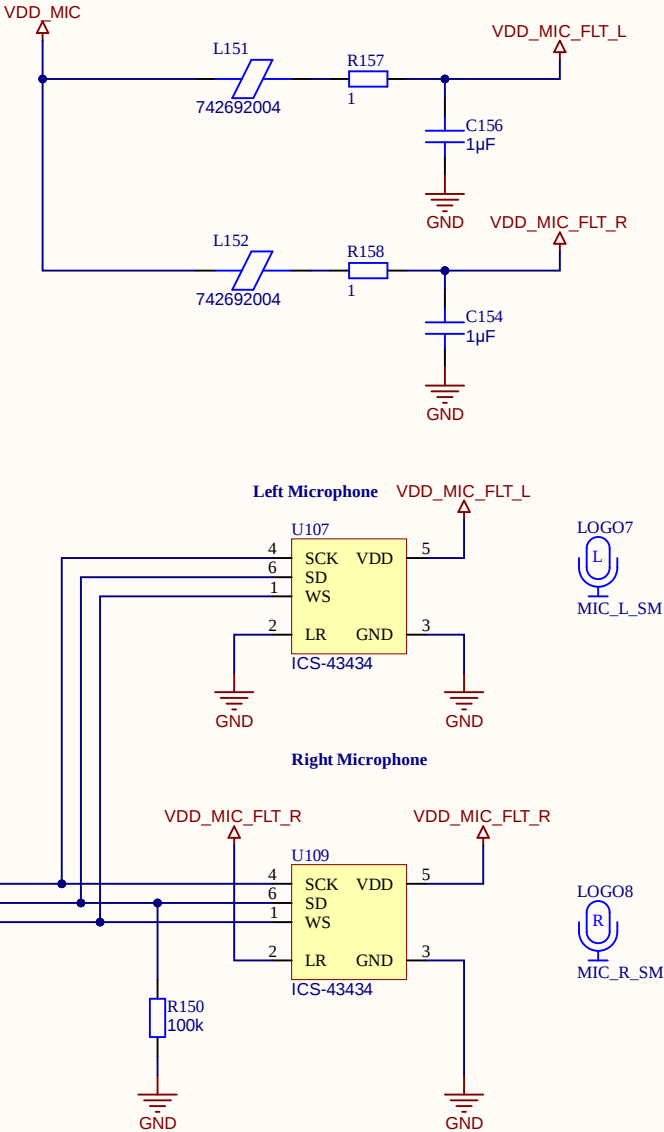
6-axis Inertial Sensor (IMU)



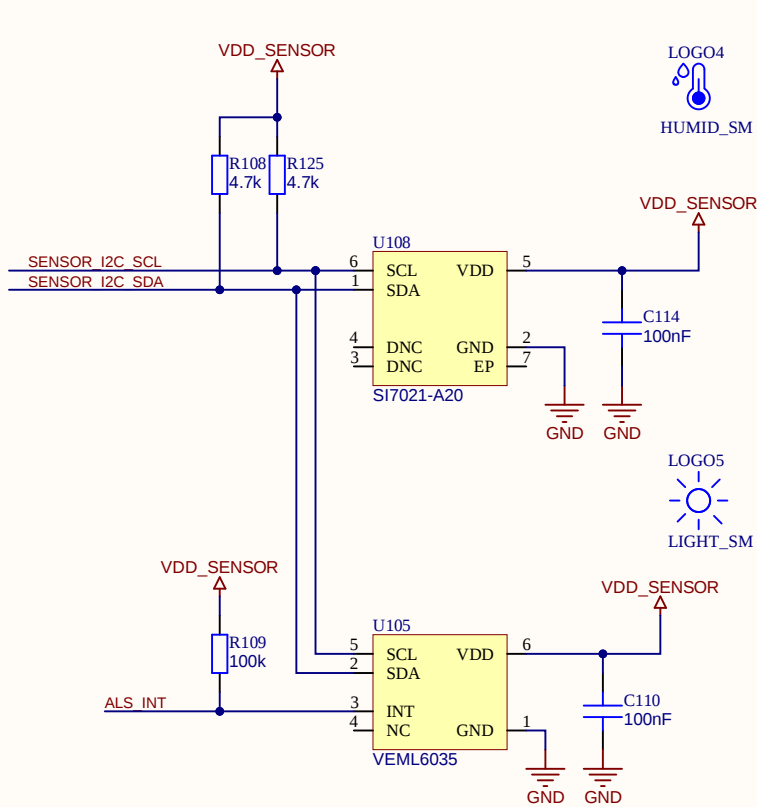
Sensor Power/Isolation



Digital Microphone

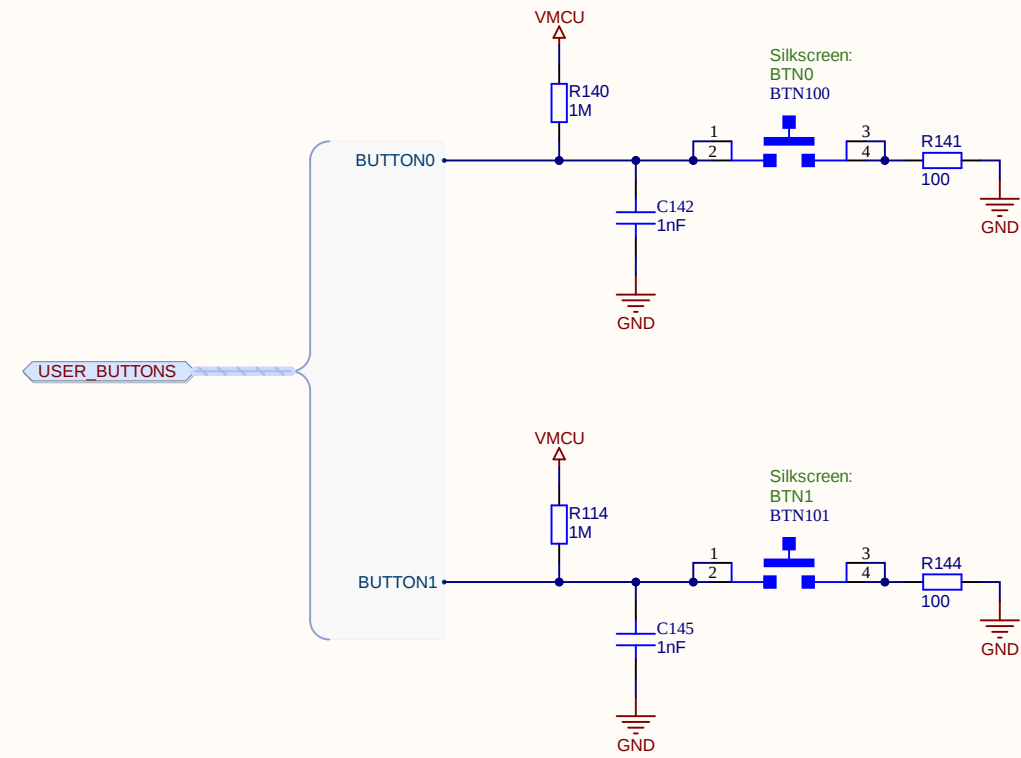


Ambient Light Sensor & RH/Temp Sensor

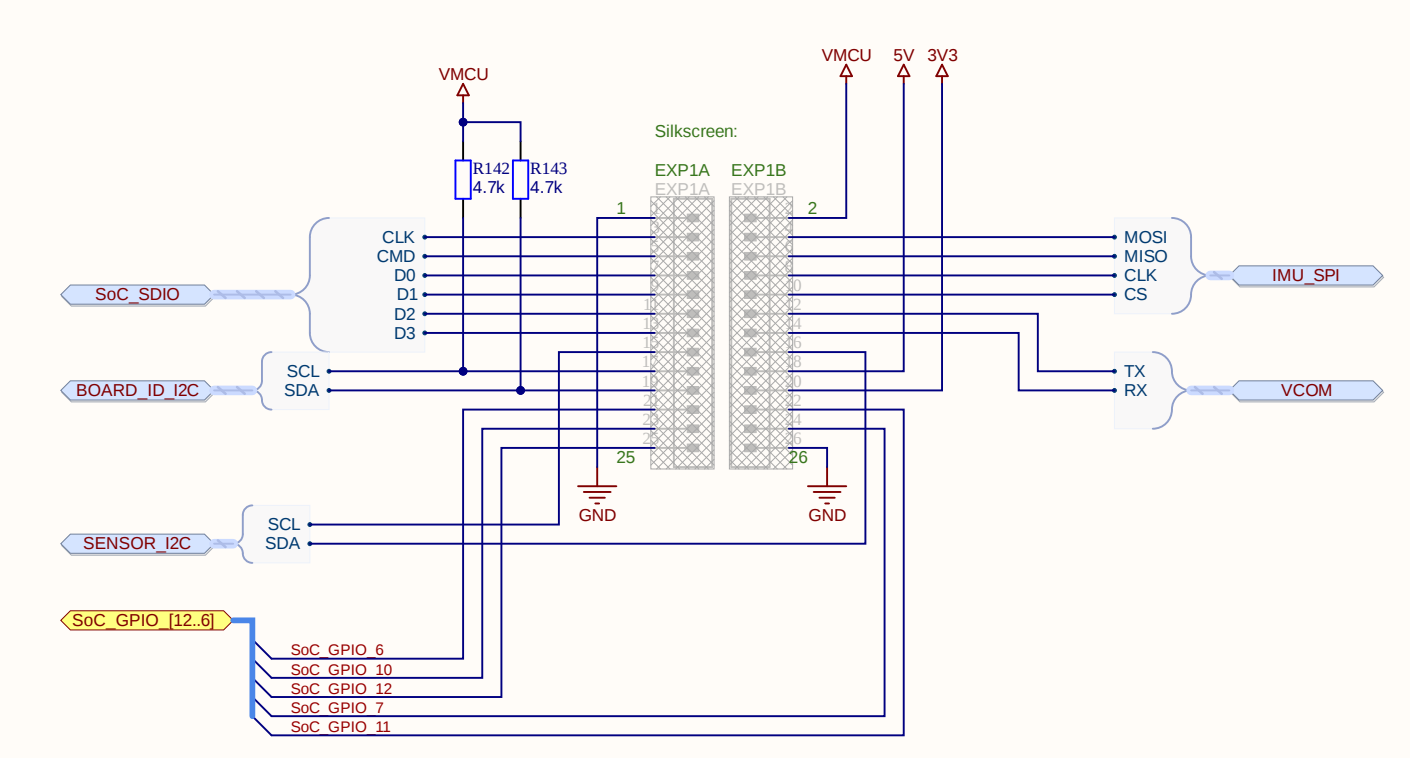


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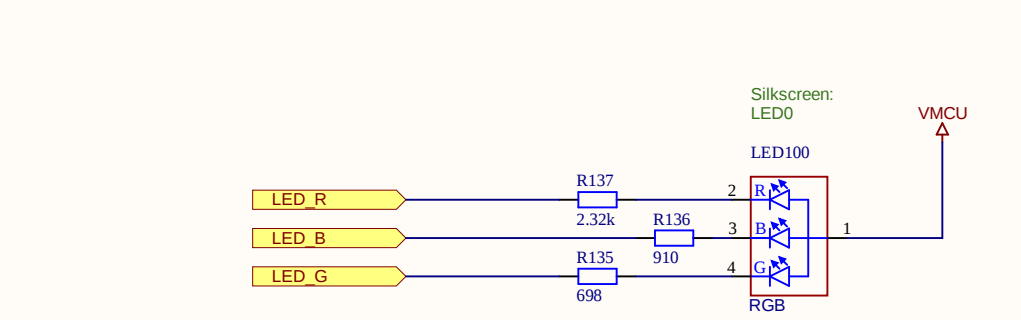
Push Buttons



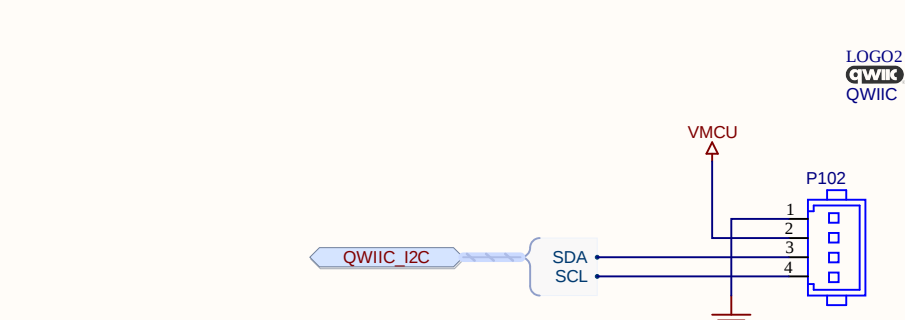
Breakout Pads



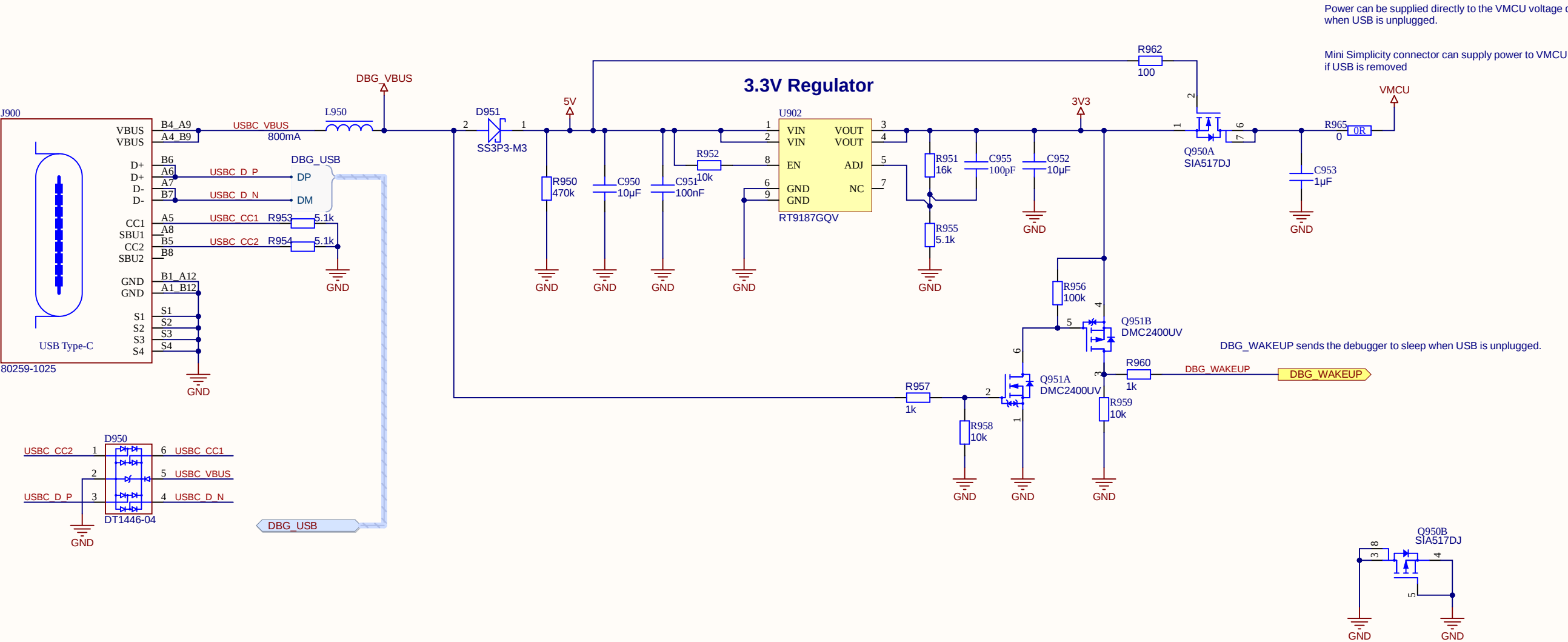
User LEDs



Qwiic Header

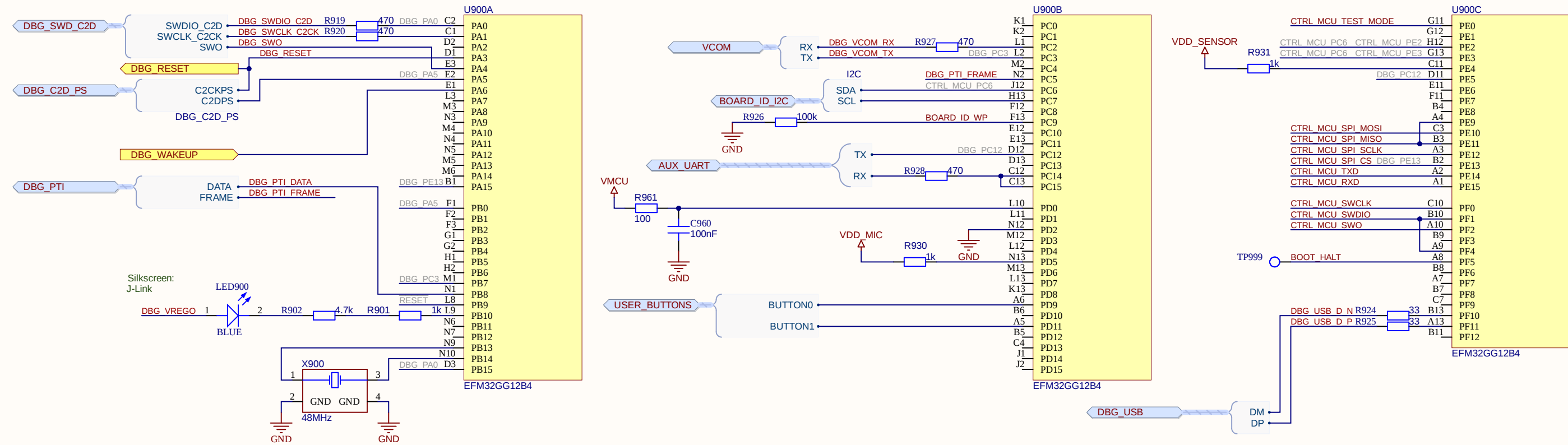


USB Type-C Connector

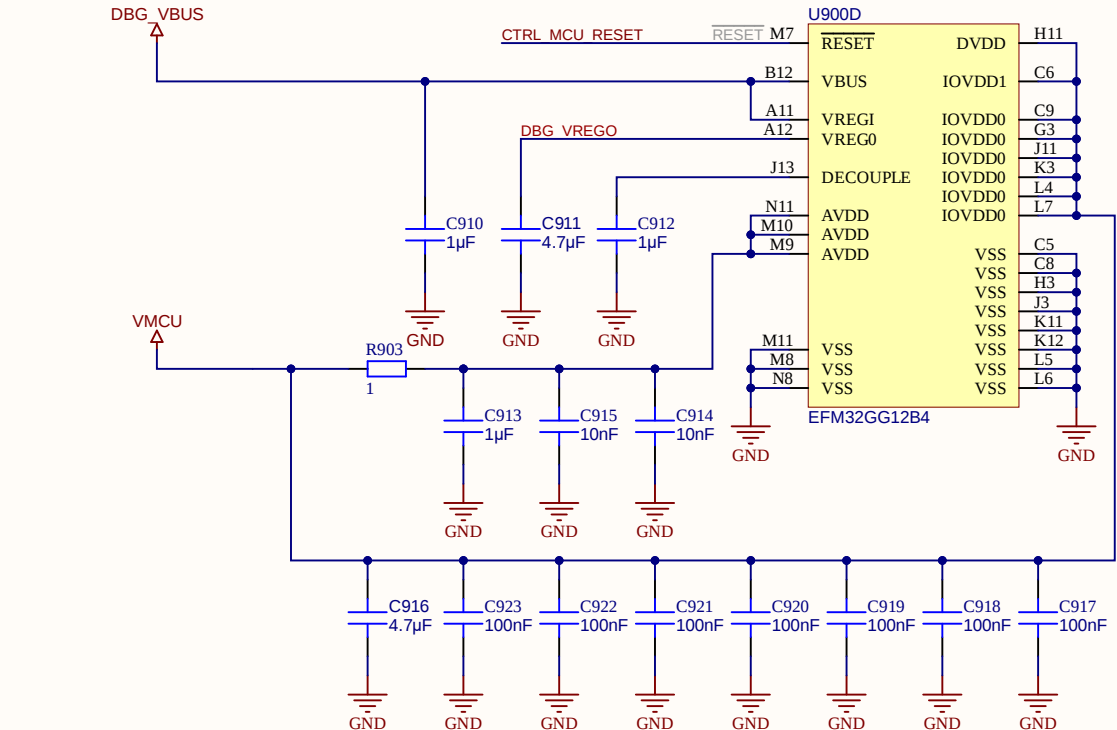


 SILICON LABS		Board name SiWG917 Dev Kit	
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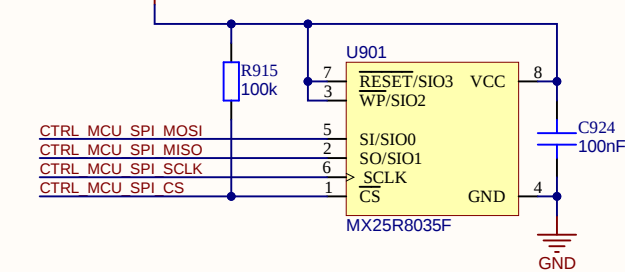
Board Controller



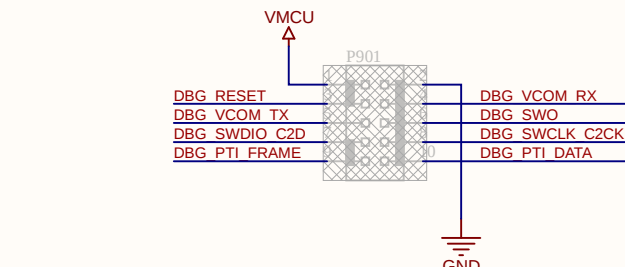
Board Controller Power



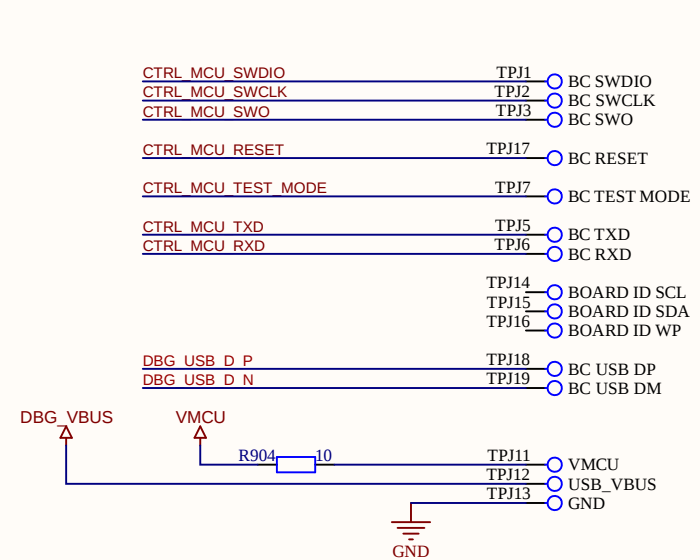
Board Controller Flash



Mini Simplicity Connector



Test Points



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