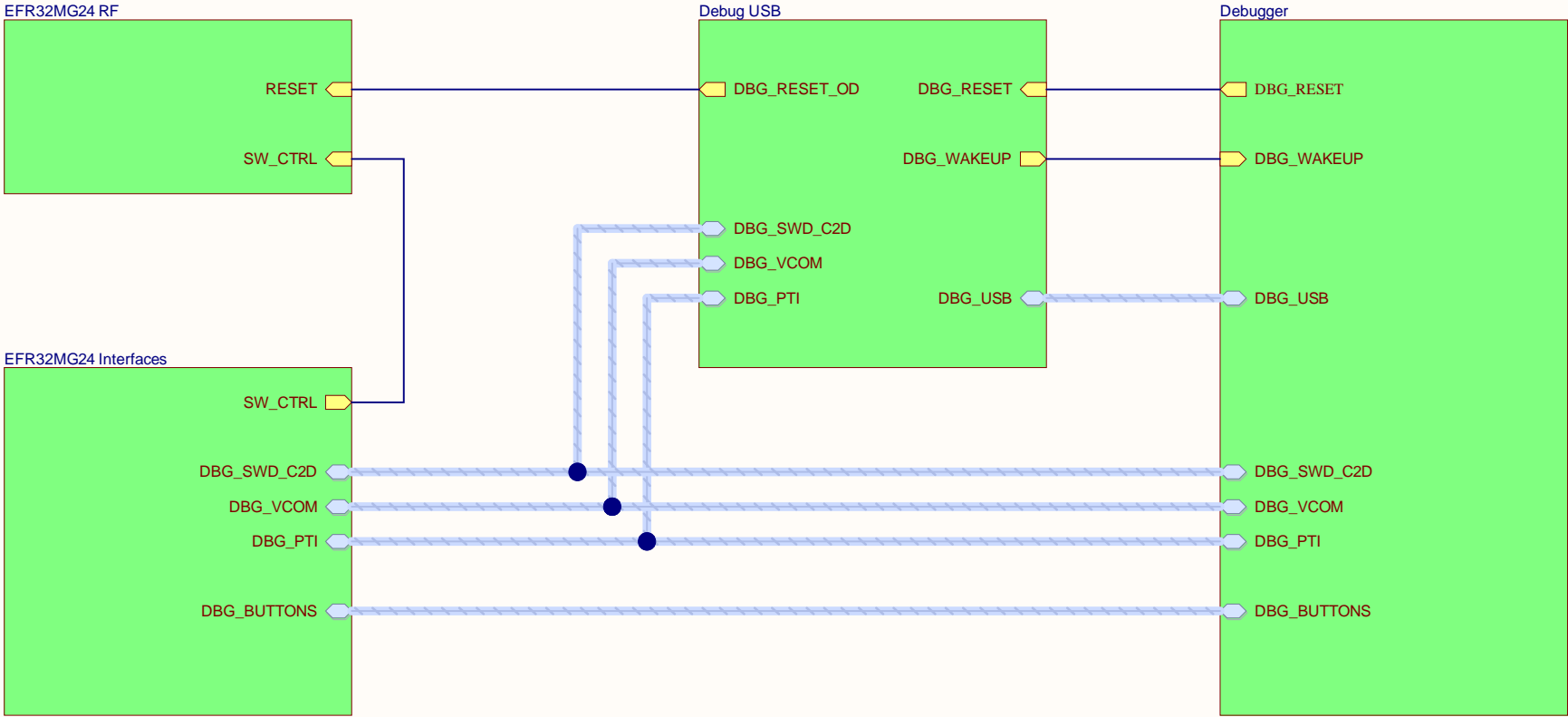


TABLE OF CONTENTS:

- Table of Contents and Revision History
- Antenna, RF & Power Sections
- Pin Assignment
- On-board Debugger
- Debugger USB and Power

REVISION HISTORY:

- | | |
|-----|-----------------------|
| A01 | Initial Release |
| A02 | Increased via size |
| A03 | Tuned for new stackup |



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LOGO1

SILICON LABS

SILABS 14MM

MARK1

RoHS

MARK2

ESD


MARK3

Pb-free

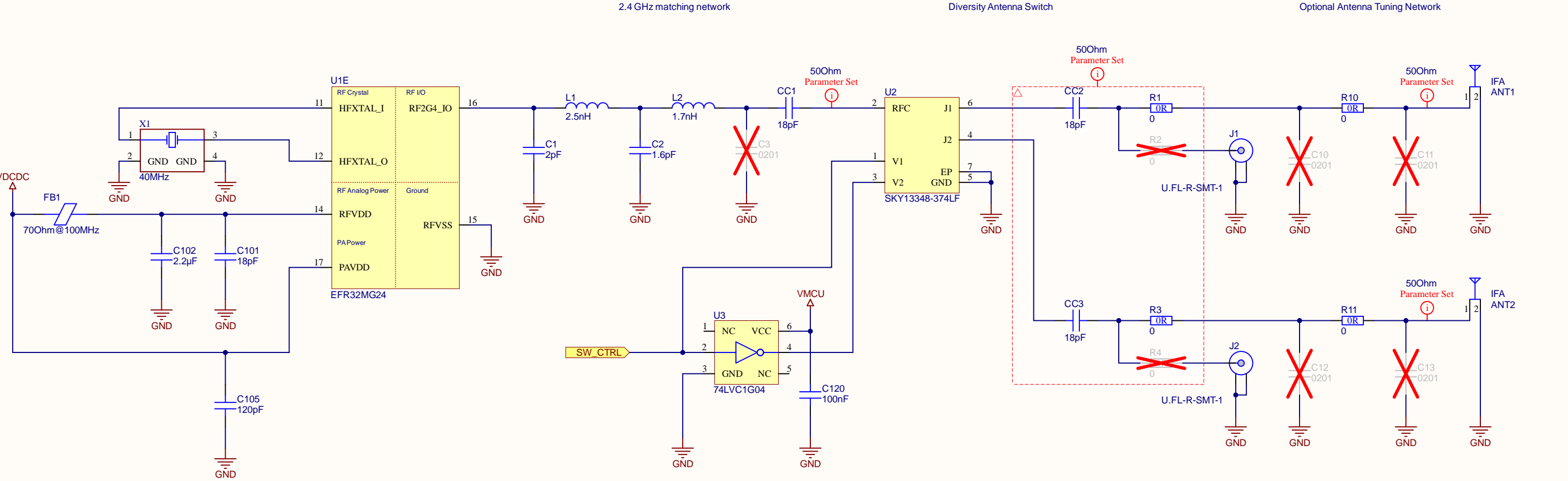
MARK4

BOARD INFO FRAME 14MM

PCB2606A A03

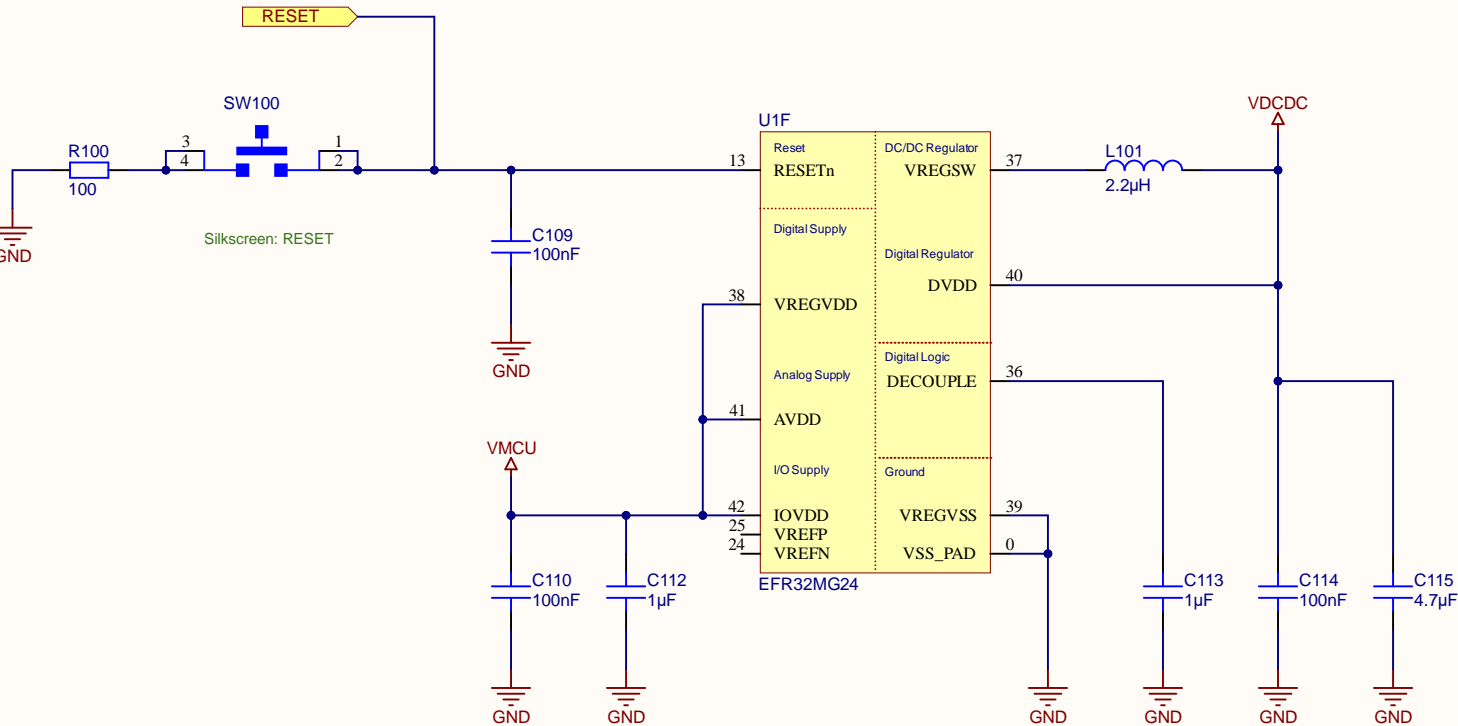
 SILICON LABS		Board name EFR32MG24 HADM Dev Kit	
Designed SZL	Approved RGU	Page Title Table of Contents and Revision History	
Size A3	Sheet Modified Date 2024. 05. 29.	Board number BRD2606A	Revision A03--
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Radio Interface



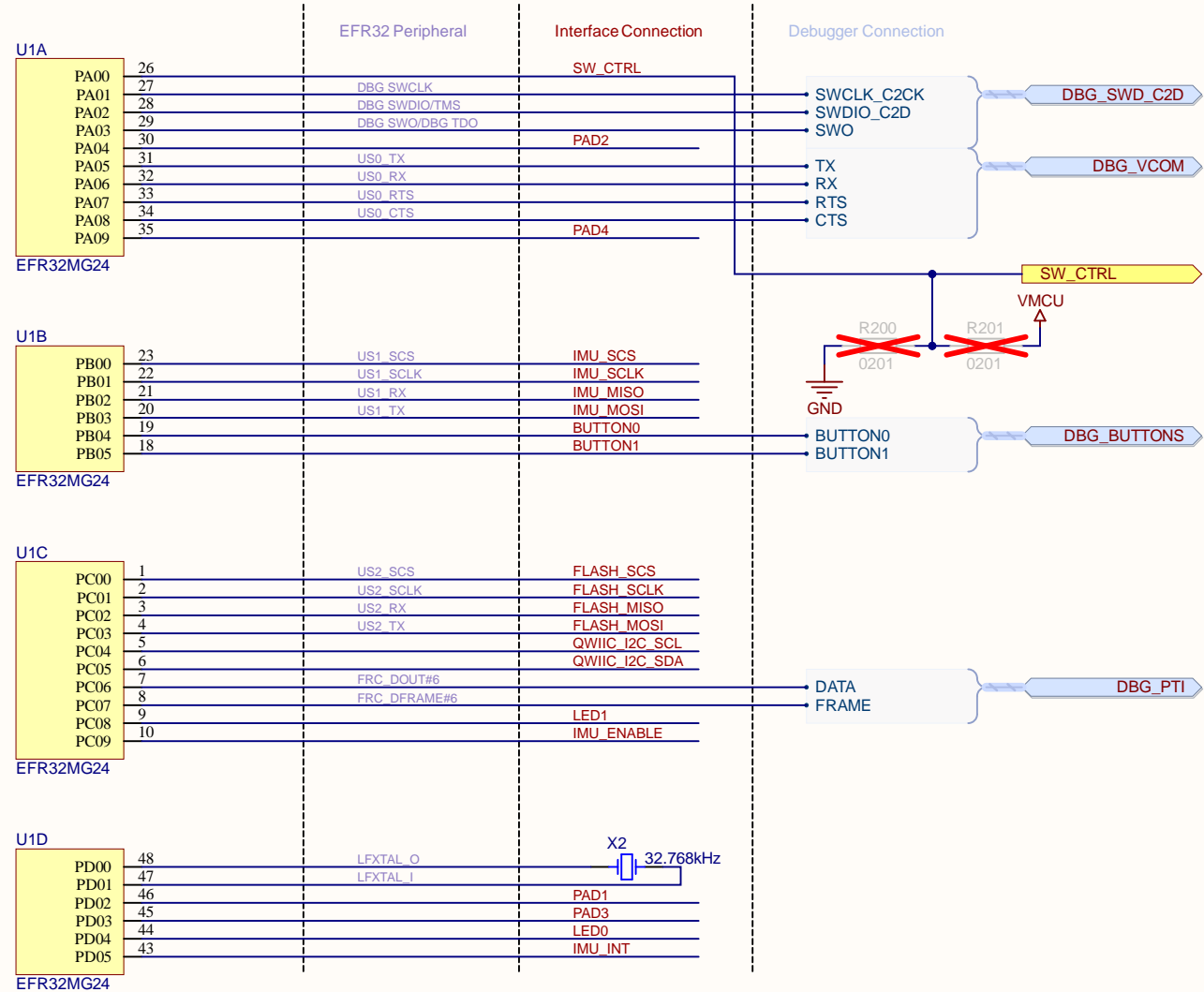
Minimal BOM supply filtering based on AN933.2

Power & Decoupling

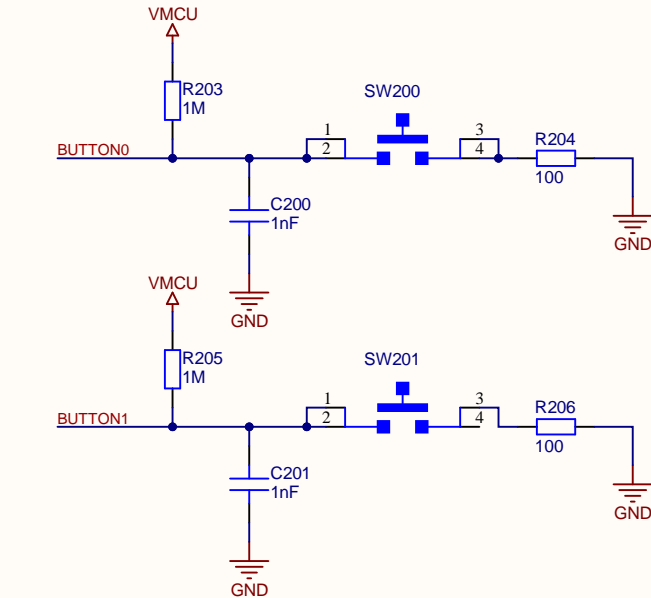


		Board name EFR32MG24 HADM Dev Kit	
Designed SZL	Approved RGU	Page Title Antenna, RF & Power Sections	
Size A3	Sheet Modified Date 2024. 05. 24.	Board number BRD2606A	Revision A03--
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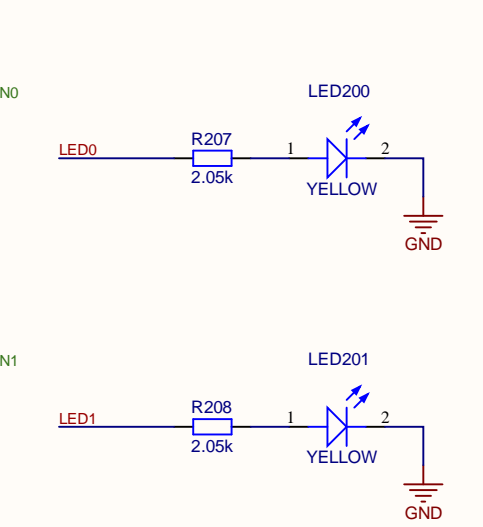
EFR32 Signal Assignments



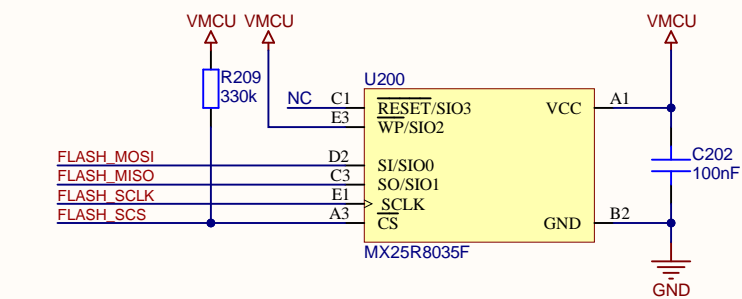
Push Buttons



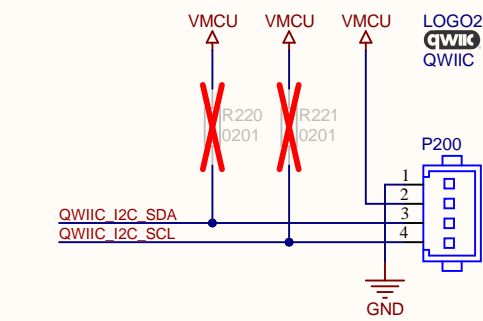
User LEDs



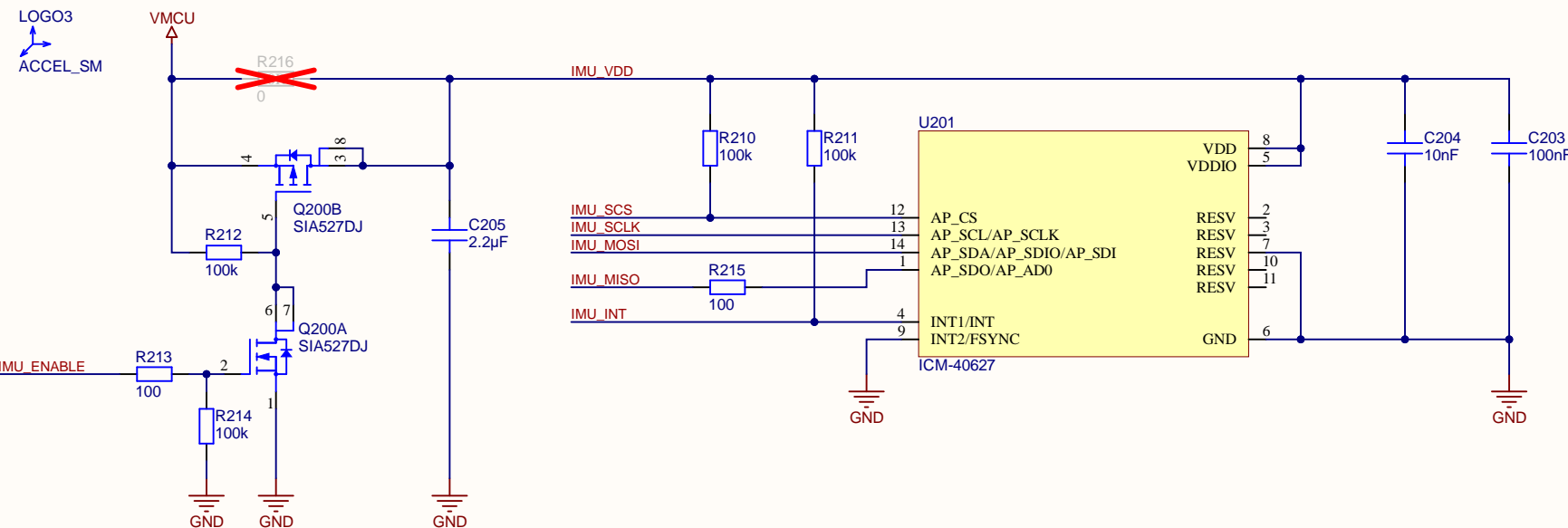
Serial Flash



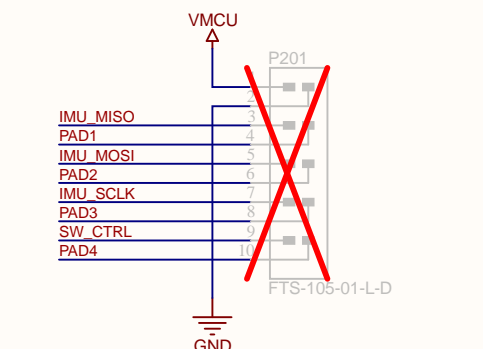
qwiic Header




6-axis Inertial Sensor (IMU)

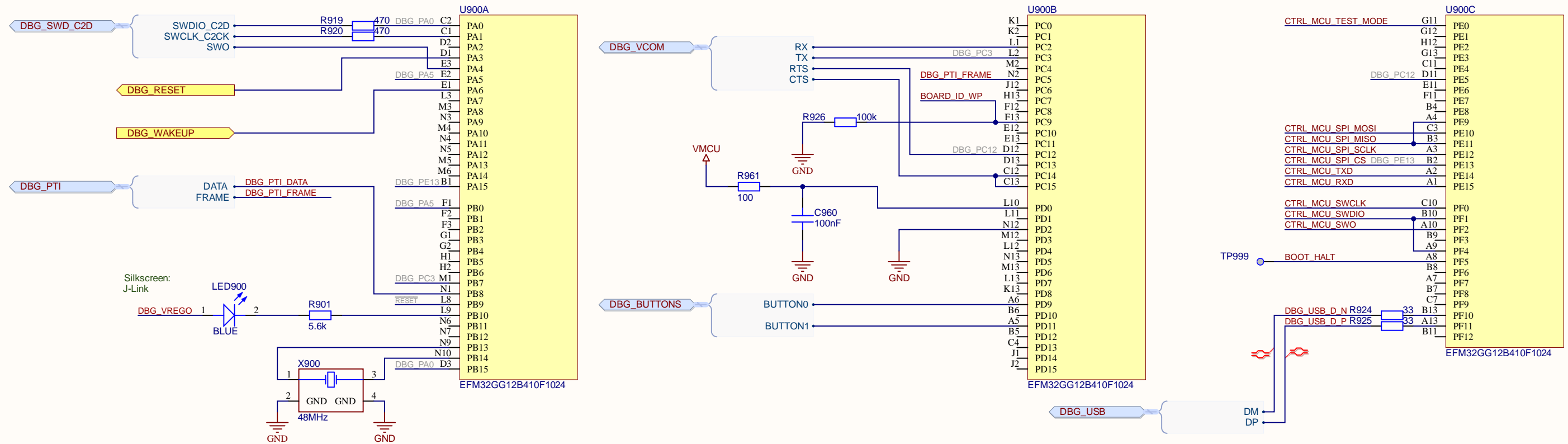


Breakout Header

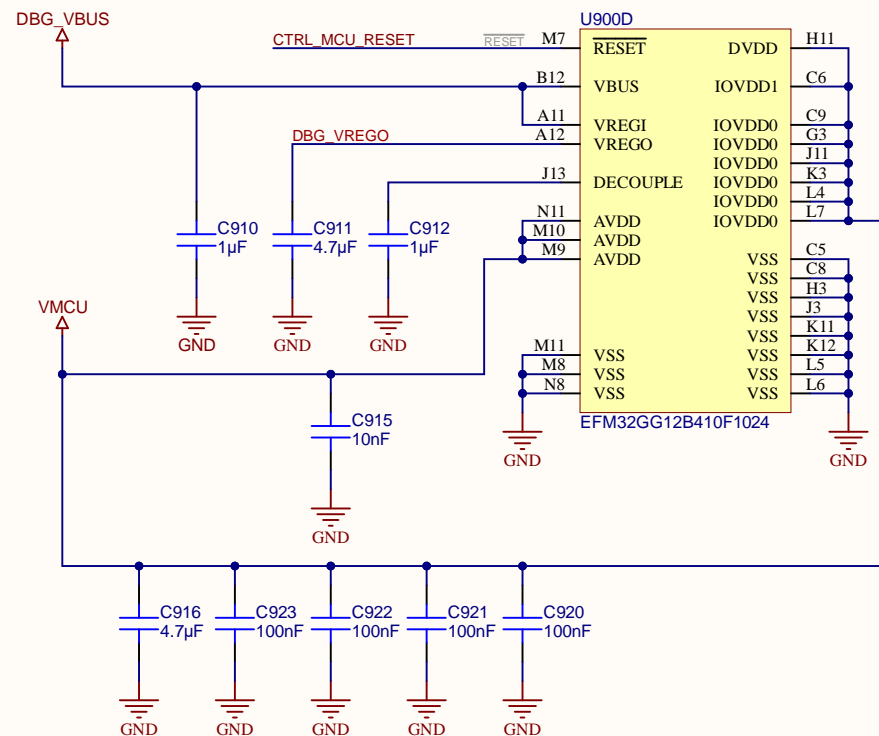


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Designed SZL	Approved RGU	Page Title Pin Assignment	
Size A3	Sheet Modified Date 2024. 05. 16.	Board number BRD2606A	Revision A03--
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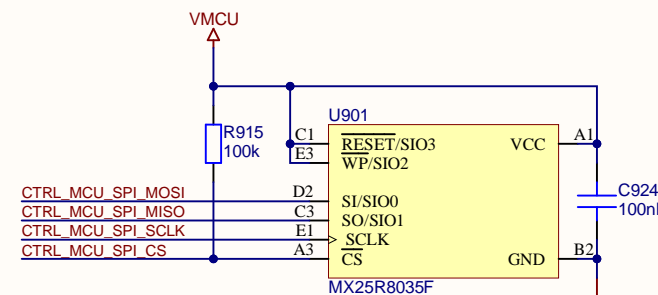
On-board Debugger



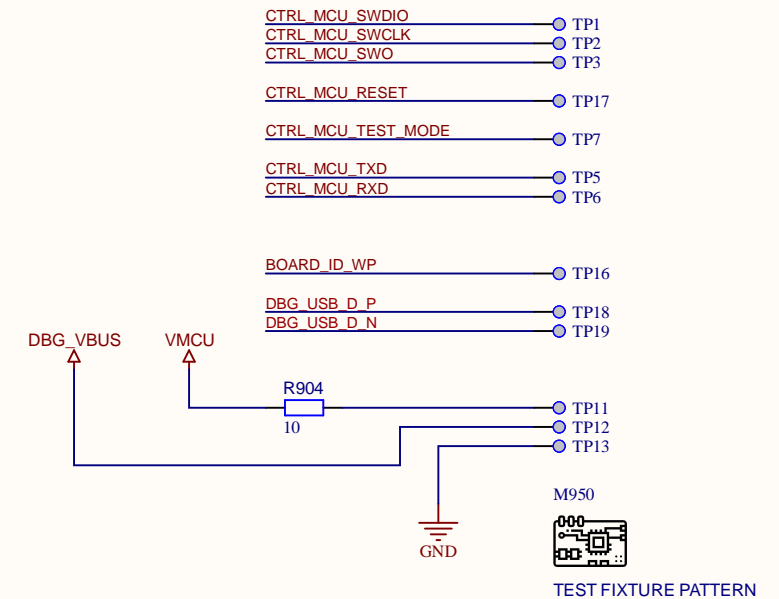
Debugger Power



Serial Flash



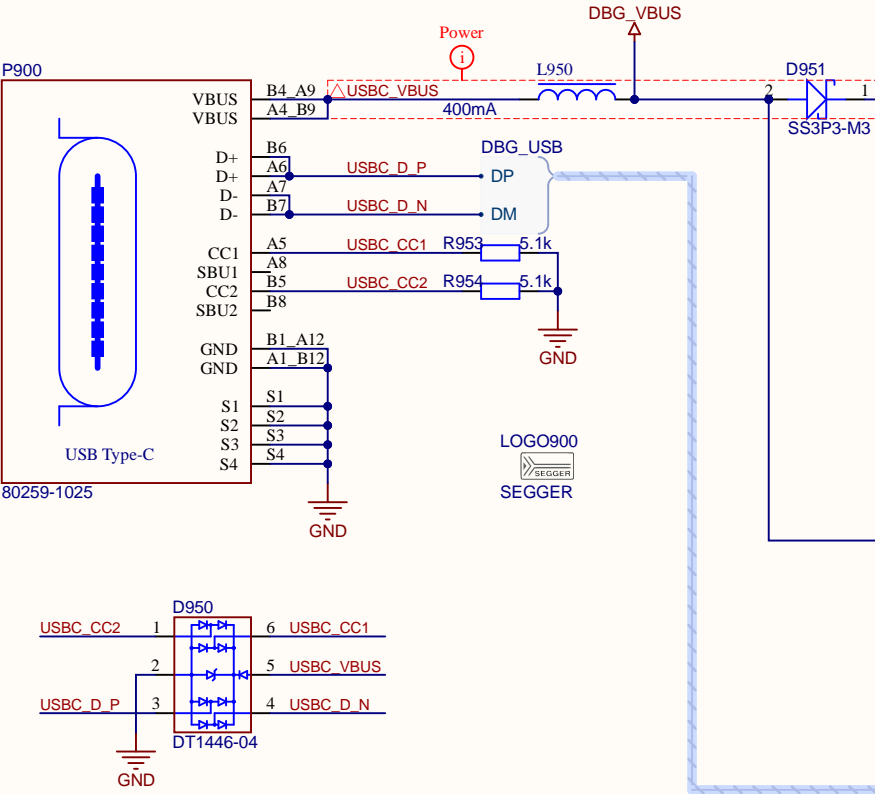
Test Points



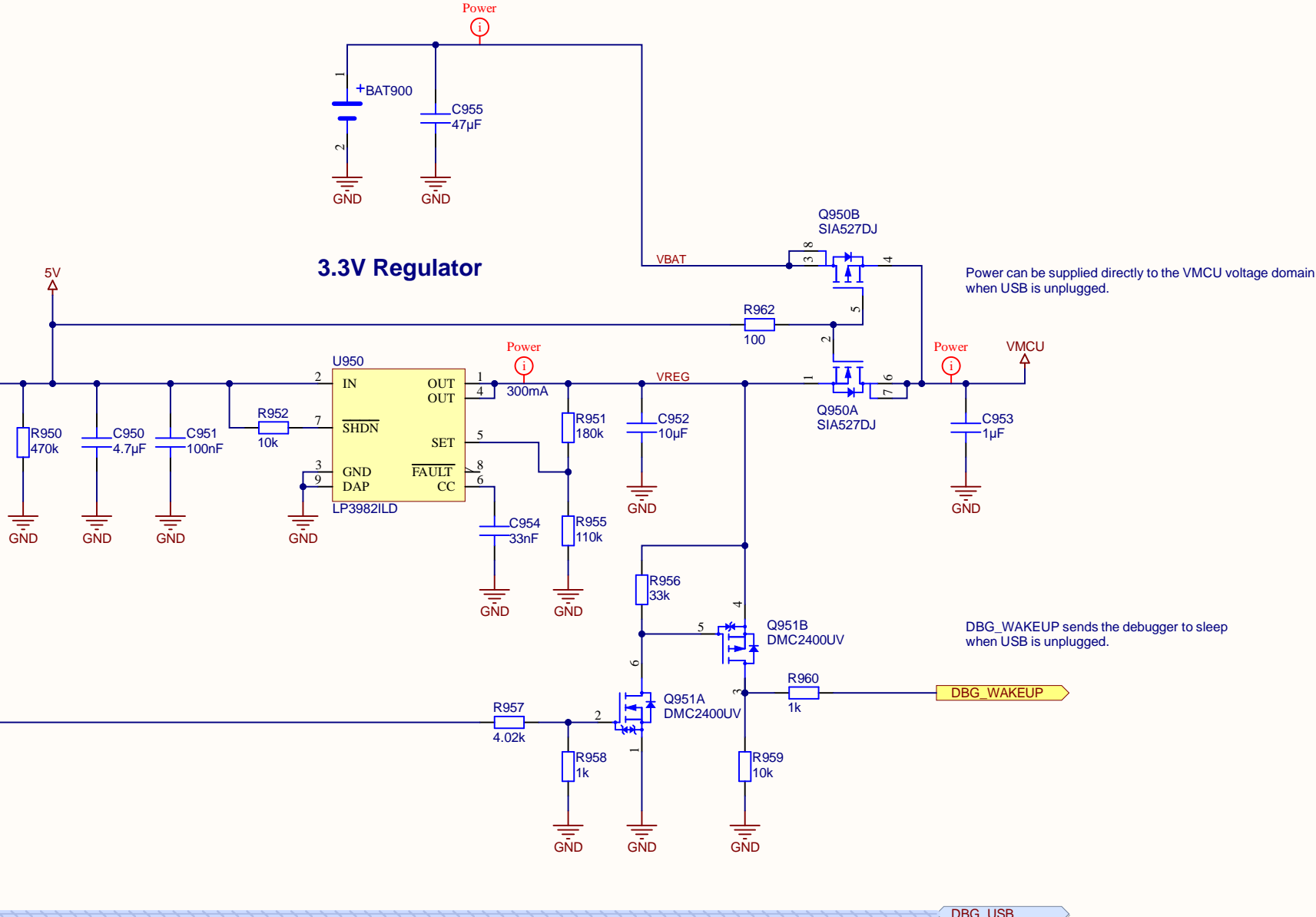
Board name
EFR32MG24 HADM Dev Kit

Designed SZL	Approved RGU	Page Title On-board Debugger	
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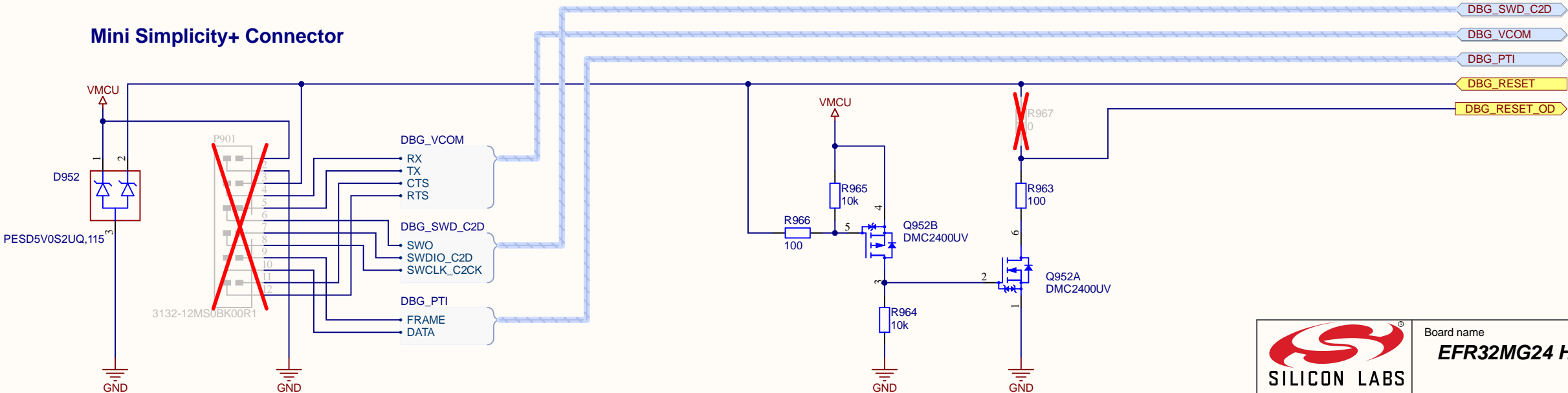
Debug USB Connection




3.3V Regulator



Mini Simplicity+ Connector



Target device has an internal pullup to VDD on reset.
This non-inverting open drain buffer prevents
external debuggers from backpowering VDD.

 SILICON LABS		Board name EFR32MG24 HADM Dev Kit	
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