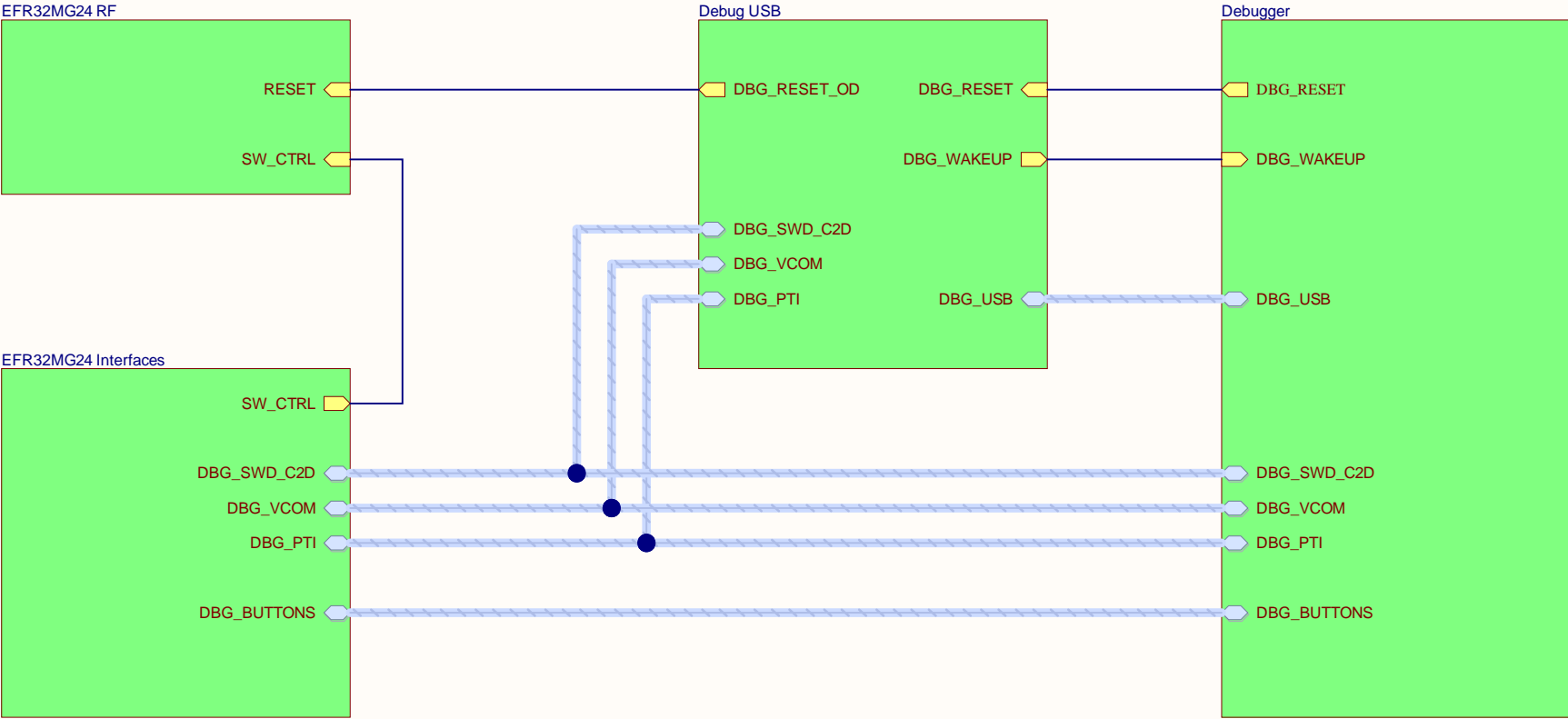


TABLE OF CONTENTS:

- Table of Contents and Revision History
- Antenna, RF & Power Sections
- Pin Assignment
- On-board Debugger
- Debugger USB and Power

REVISION HISTORY:

- |     |  |
|-----|--|
| A01 | Initial Release                        |
| A02 | Increased via size                     |
| A03 | Tuned for new stackup                  |
| A04 | Added 'Channel Sounding' to silkscreen |



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LOGO1

MARK1

MARK2

MARK3

MARK4

SILABS 14MM

RoHS

ESD

Pb-free

BOARD INFO FRAME 14MM



Board name  
**EFR32MG24 HADM Dev Kit**

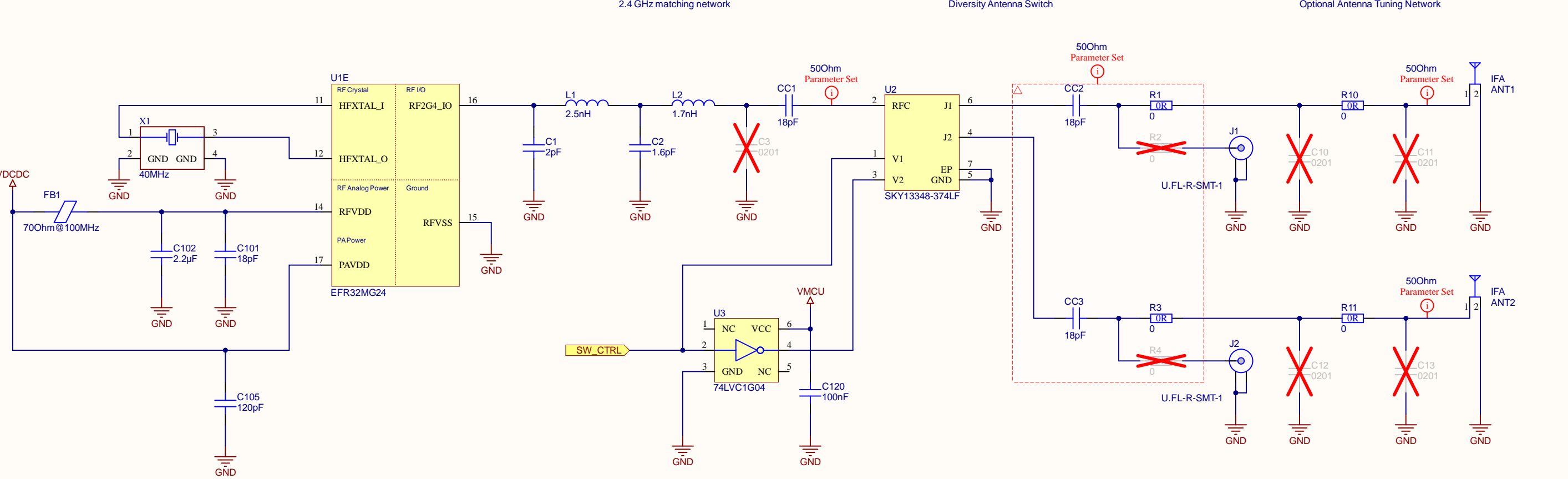
Designed SZL	Approved RGU	Page Title Table of Contents and Revision History
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Size A3	Sheet Modified Date 2024. 05. 29.	Board number BRD2606A	Revision A04--
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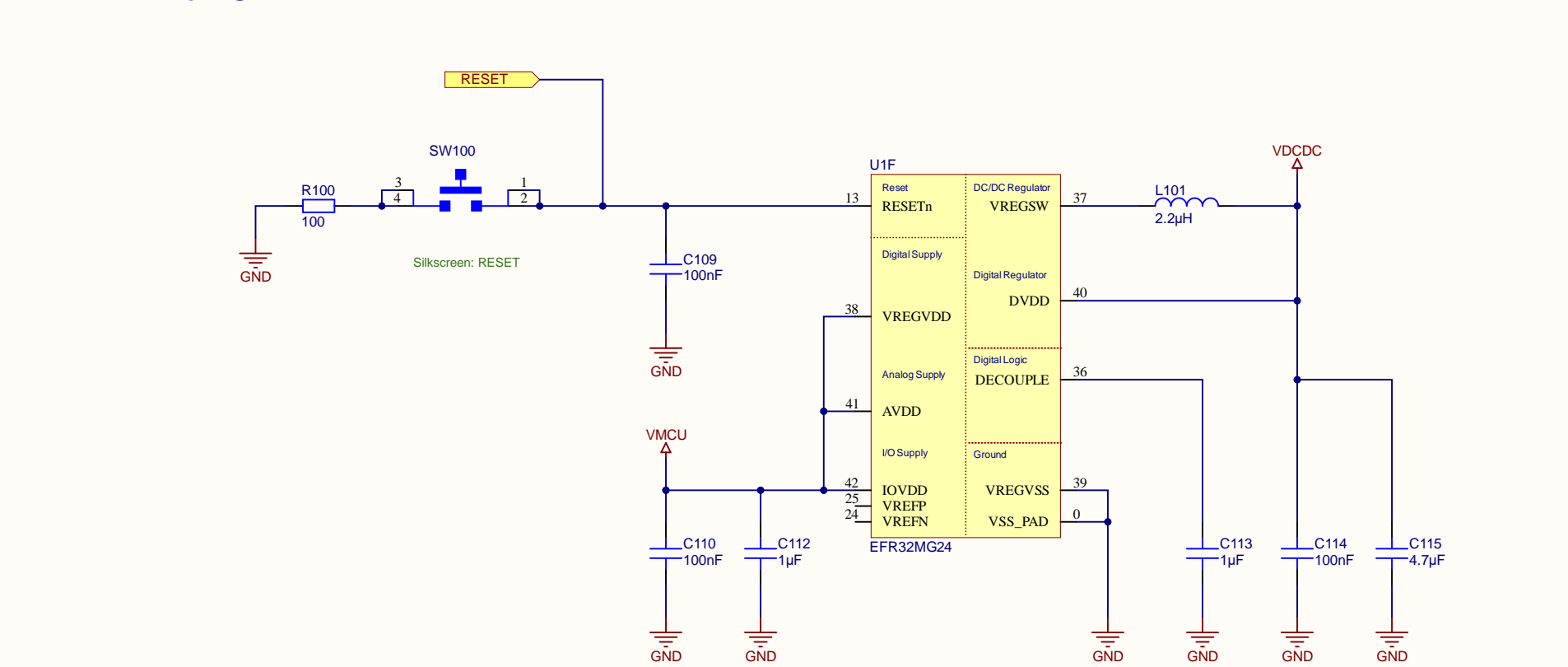
Sheet  
1 of 5


Radio Interface



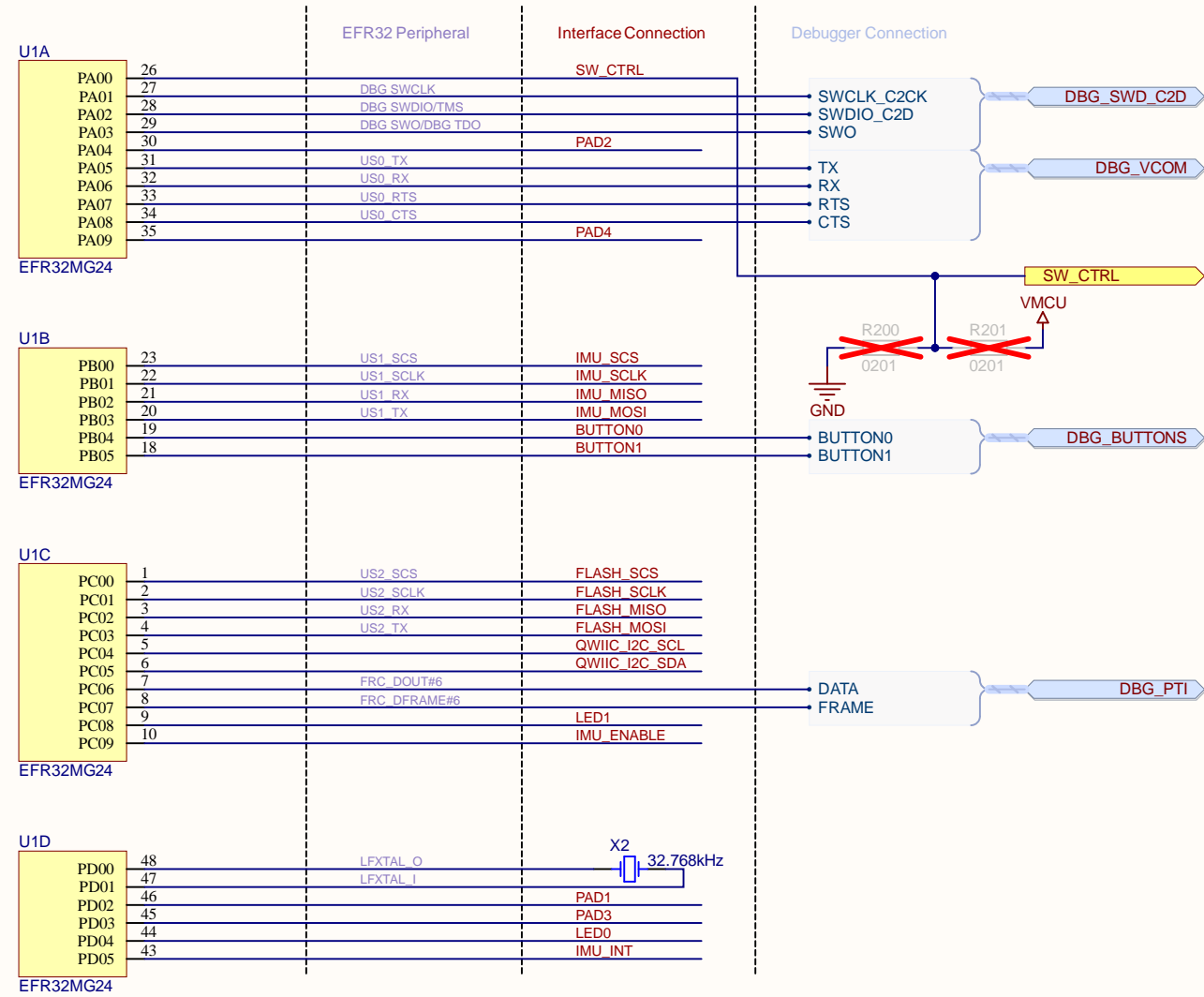
Minimal BOM supply filtering based on AN933.2

Power & Decoupling

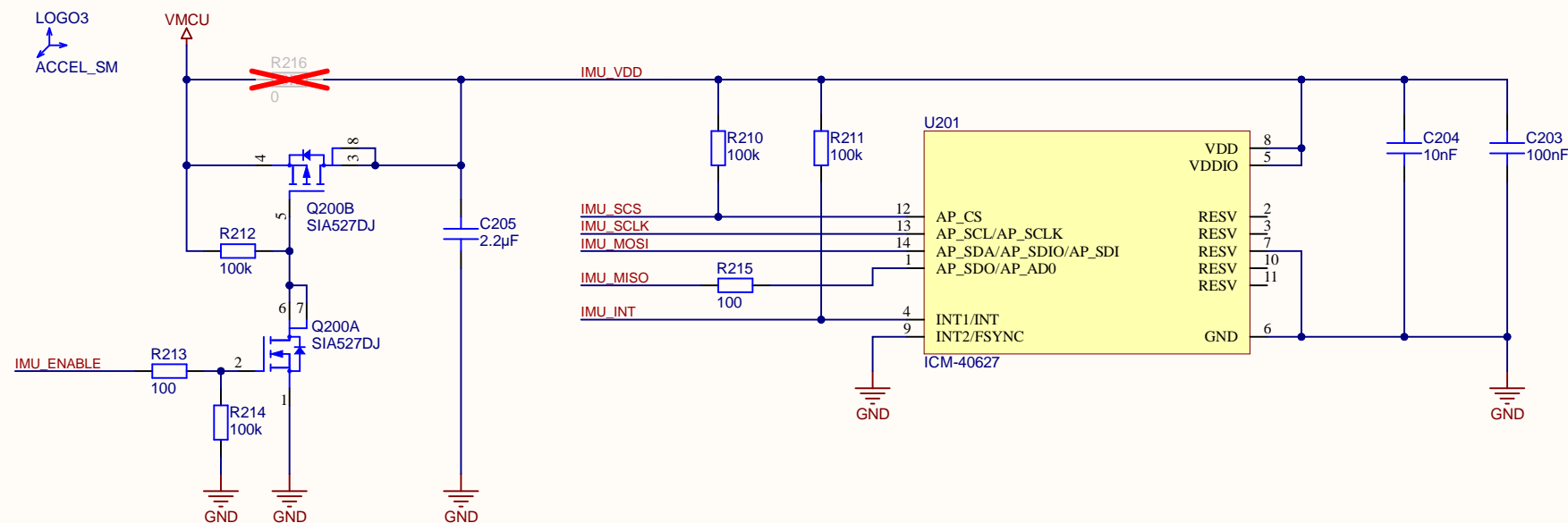


		Board name <b>EFR32MG24 HADM Dev Kit</b>	
Designed SZL	Approved RGU	Page Title Antenna, RF & Power Sections	
Size A3	Sheet Modified Date 2024. 05. 24.	Board number BRD2606A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2023 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 2 of 5

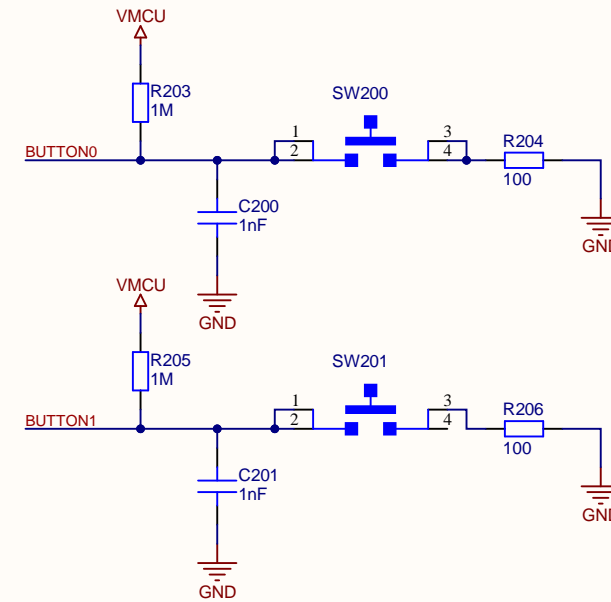
## EFR32 Signal Assignments



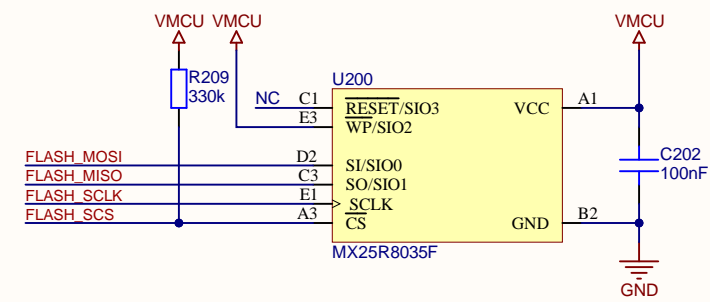
## 6-axis Inertial Sensor (IMU)



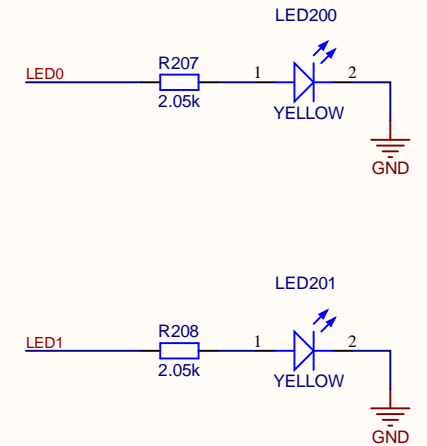
## Push Buttons



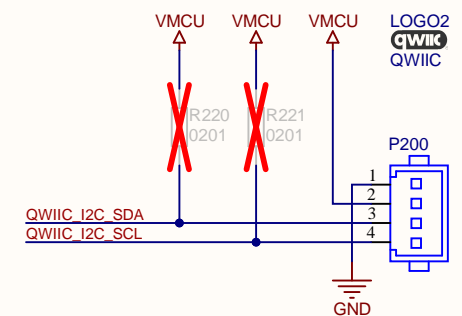
## Serial Flash



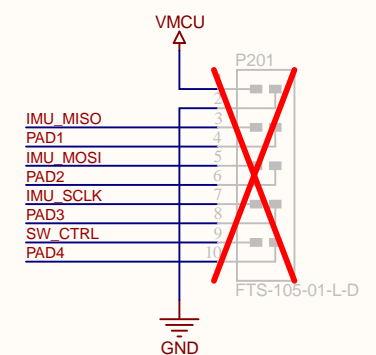
## User LEDs



## qwiic Header

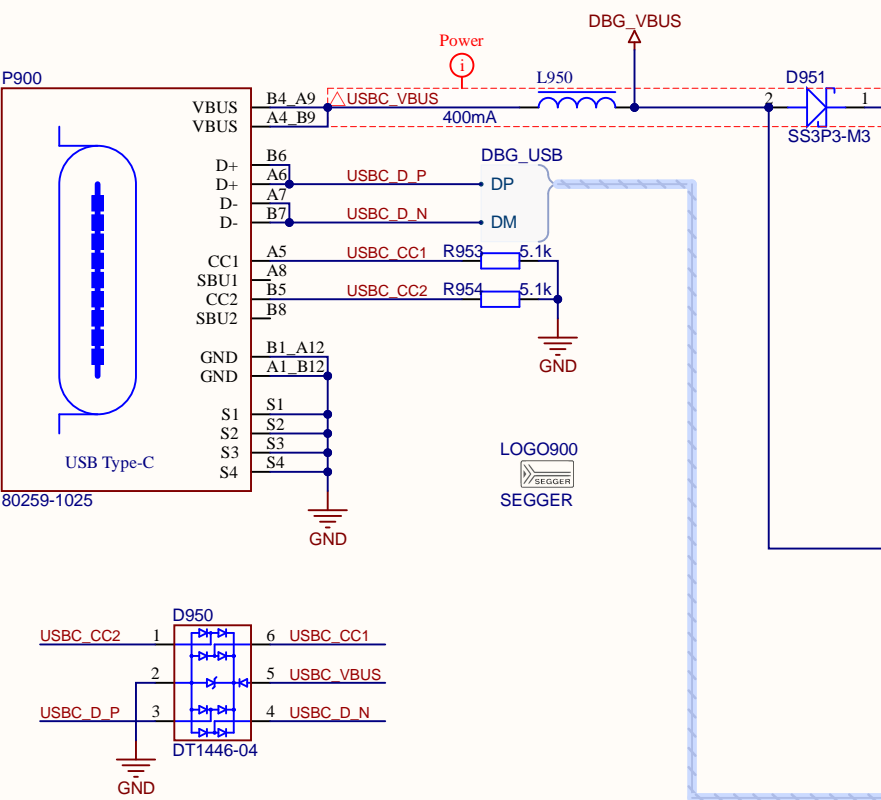


## Breakout Header

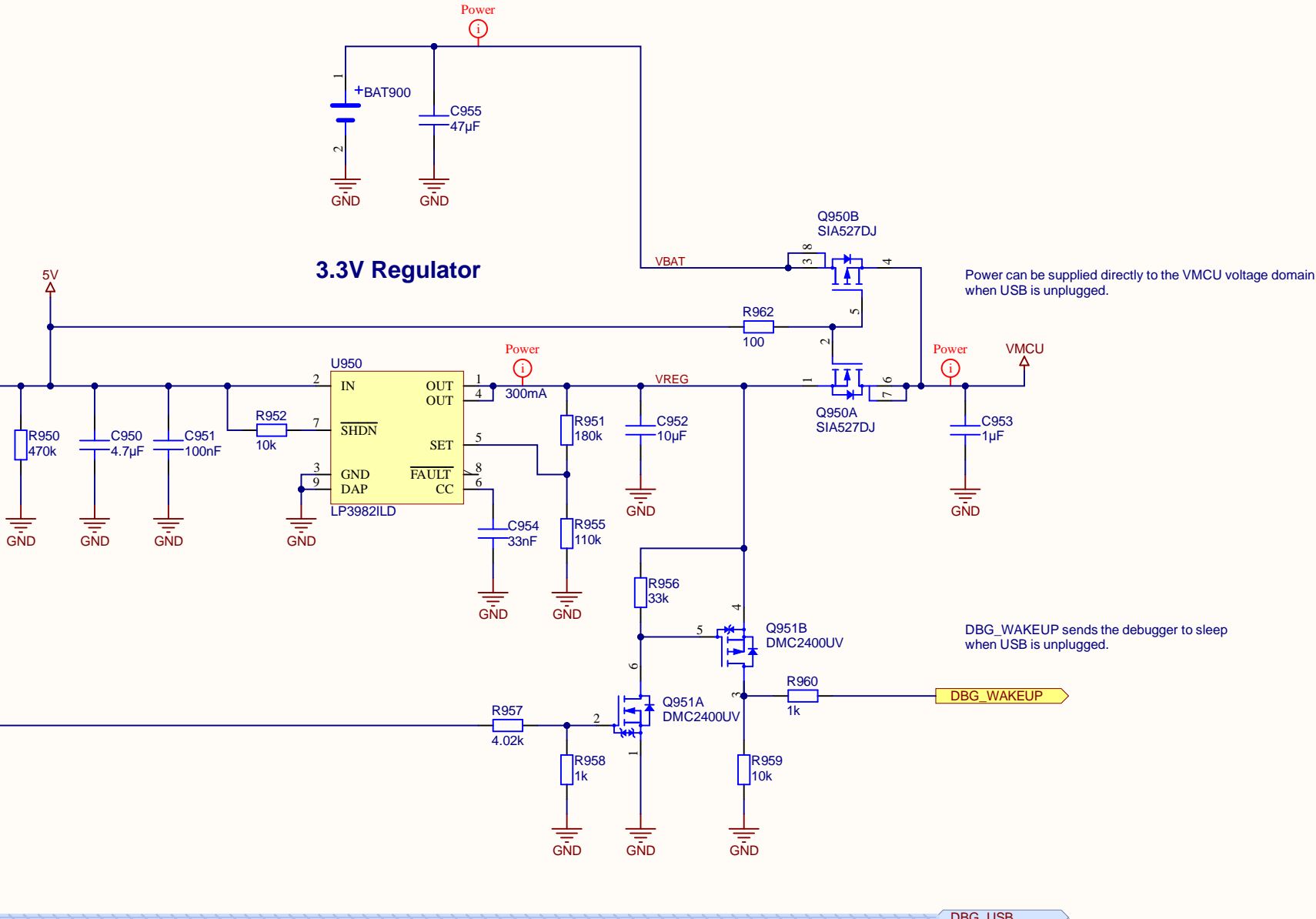




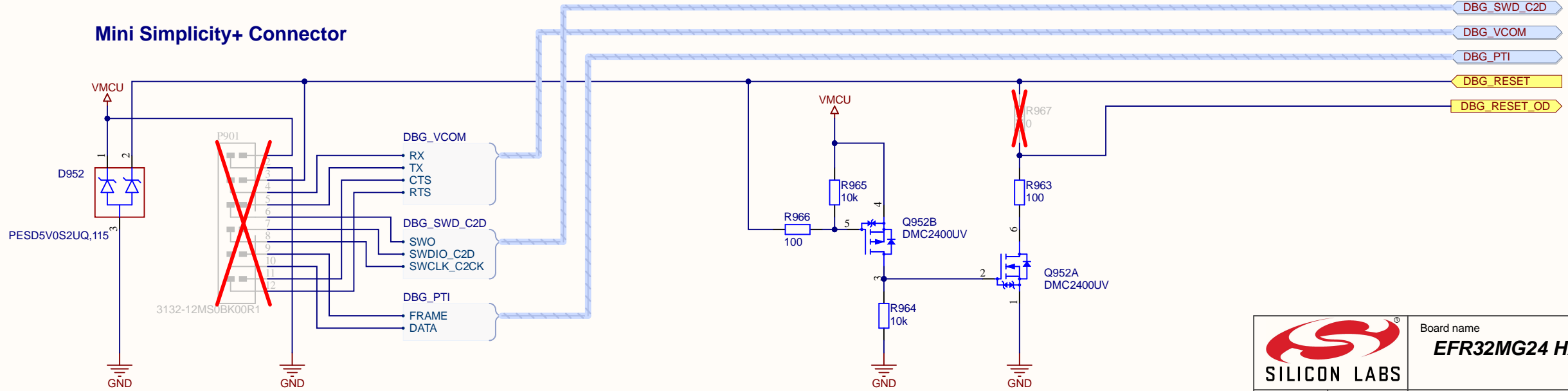
Debug USB Connection



3.3V Regulator



Mini Simplicity+ Connector



Target device has an internal pullup to VDD on reset.  
This non-inverting open drain buffer prevents  
external debuggers from backpowering VDD.