

TABLE OF CONTENTS:

Page 1	Table of contents, Revision history
Page 2	Hierarchical Overview
Page 3	EFR32xG26 Power and RF
Page 4	EFR32xG26 IO and signal assigments
Page 5	Sensors and Peripherals
Page 6	Microphones and Analog
Page 7	User Interface
Page 8	Power and Mini-Simplicity
Page 9	On-board debugger

REVISION HISTORY:

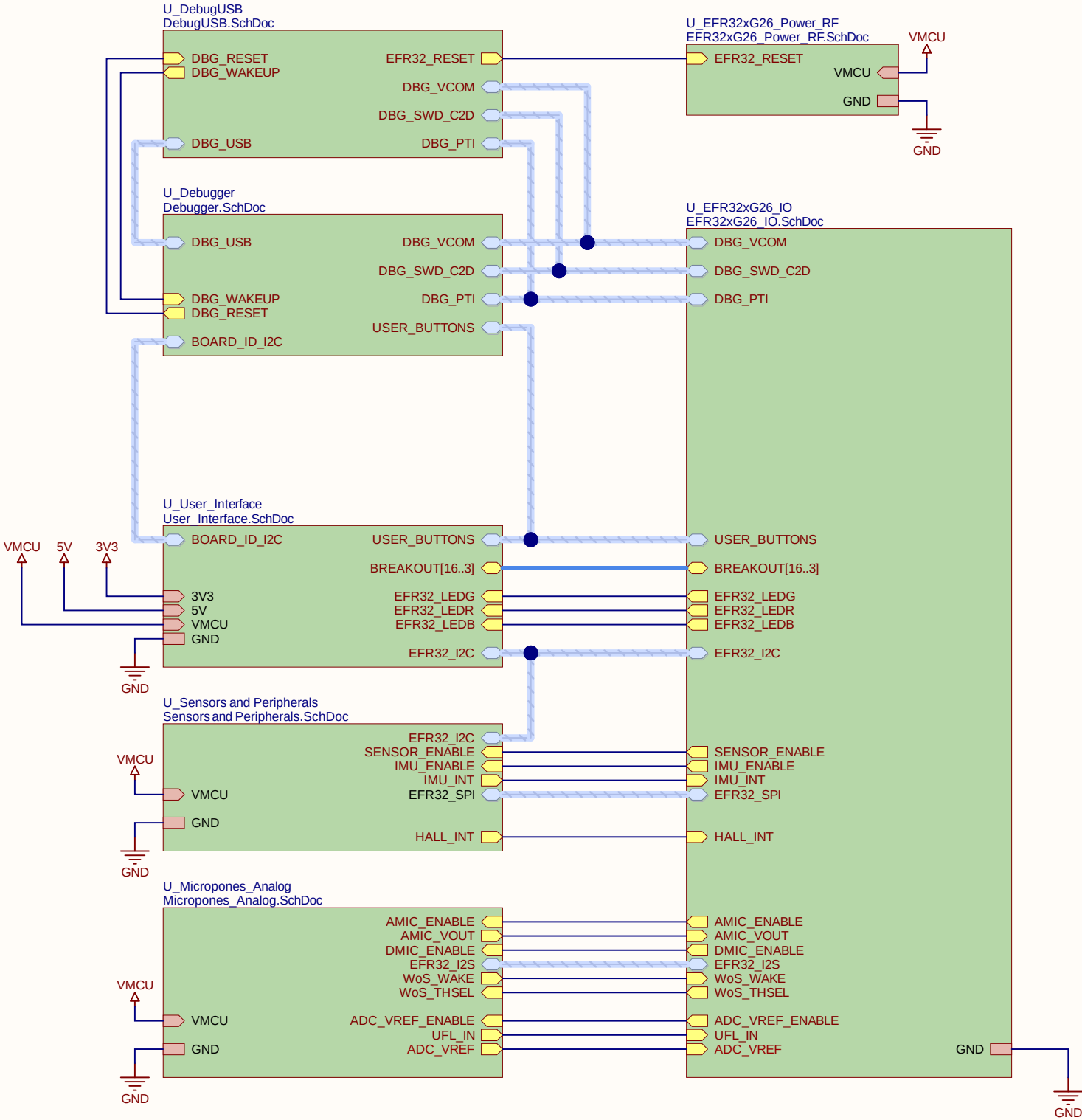
A01	Initial Release
A02	U107 OPN change (MX25R6435FZAIH0)
A03	SCH update, C6 to NF, R100,R101 value change U1 OPN update
A04	SCH update

LEGAL NOTICE:
Silicon Laboratories inc. ("Silicon Labs") and/or its licensors do not warrant the accuracy or completeness of this schematic or any information contained within this schematic. It is provided "as-is" for reference only. Silicon Labs does not warrant that this design will meet the specifications, be suitable for your application or fit for any particular purpose, or will operate in your implementation. Silicon Labs and its licensors do not warrant that the design implied in this schematic is production-worthy. You should completely validate and test your design implementation to confirm system functionality for your application.

 SILICON LABS		Board name xG26 Dev Kit	
Designed CZD	Approved RGU	Page Title Table of contents, Revision history	
Size A3	Sheet Modified Date 2024. 09. 18.	Board number BRD2608A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 1 of 9



PCB2608A A02



Board name
xG26 Dev Kit

Designed
CZD

Approved
RGU

Page Title
Hierarchical Overview

Size
A3

Sheet Modified Date
2024. 09. 18.

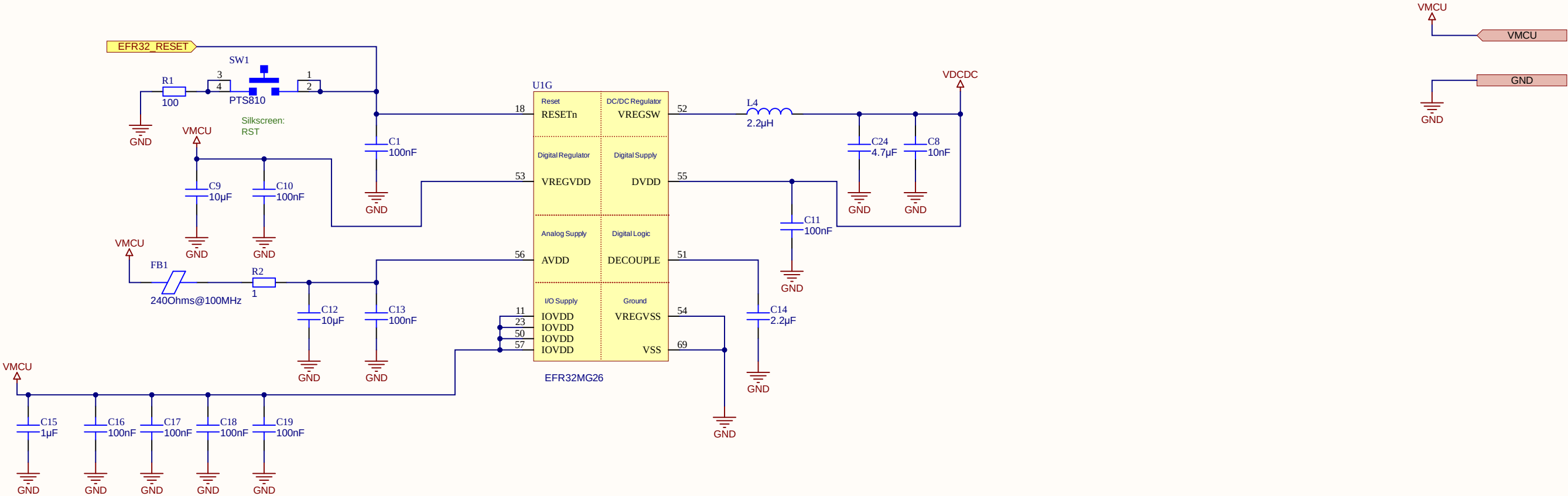
Board number
BRD2608A

Revision
A04--

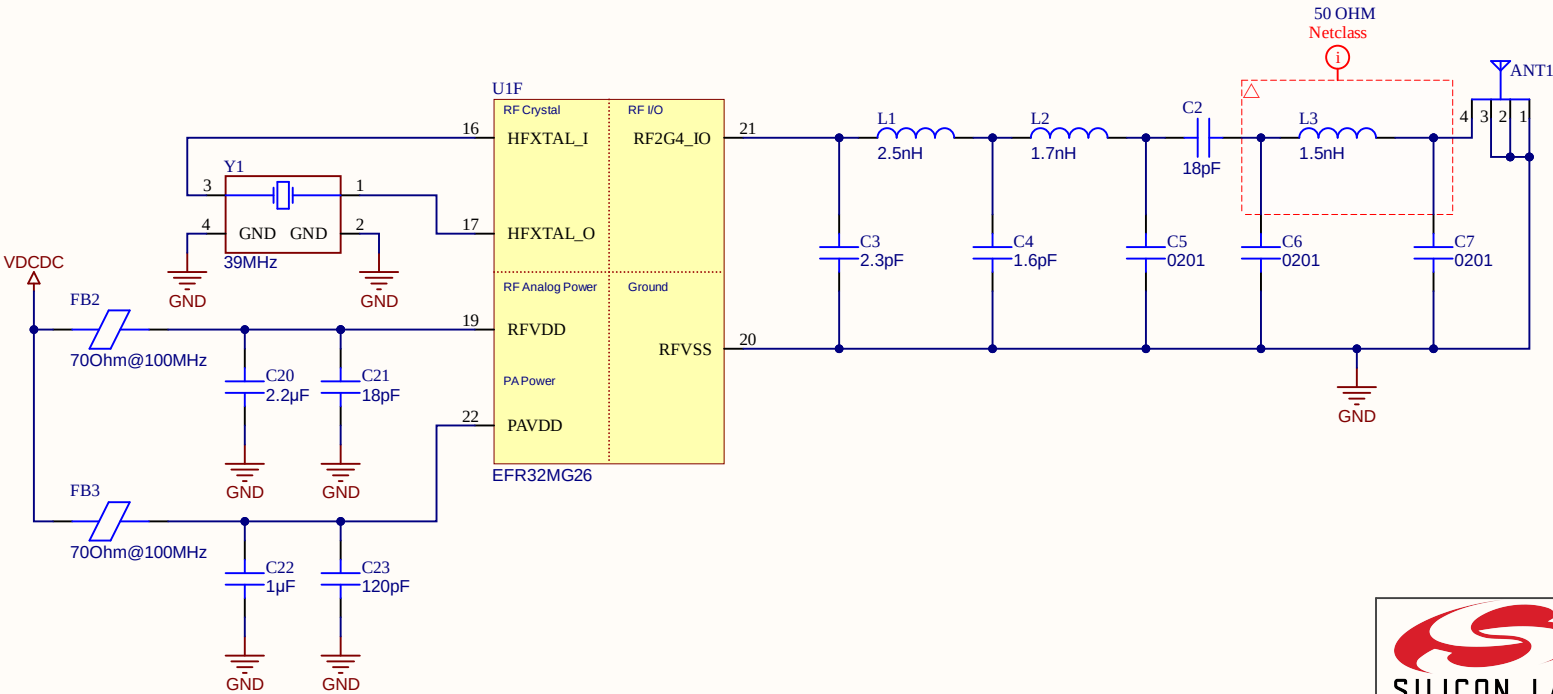
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE

Sheet
2 of 9

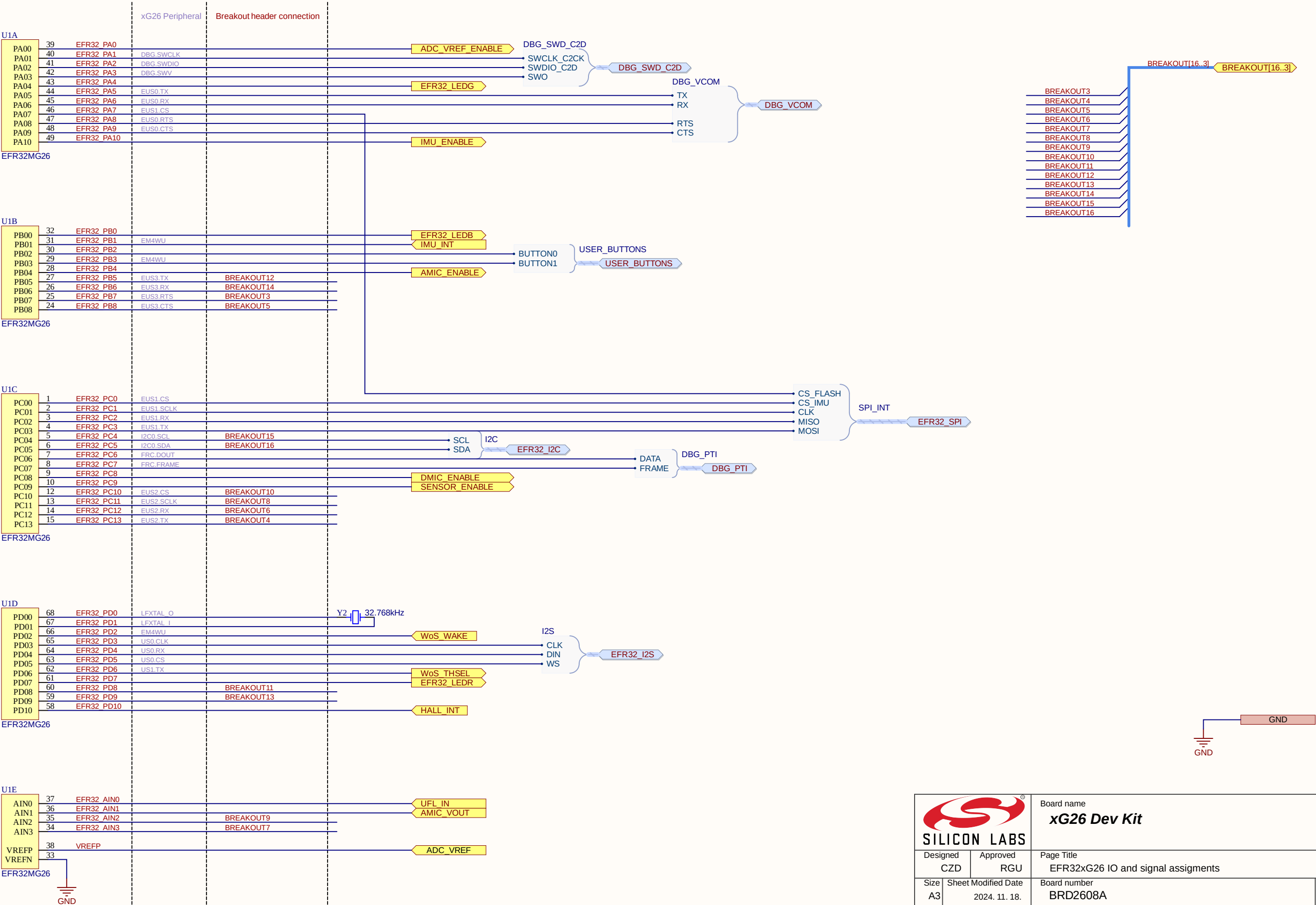
EFR32xG26 Power Selection



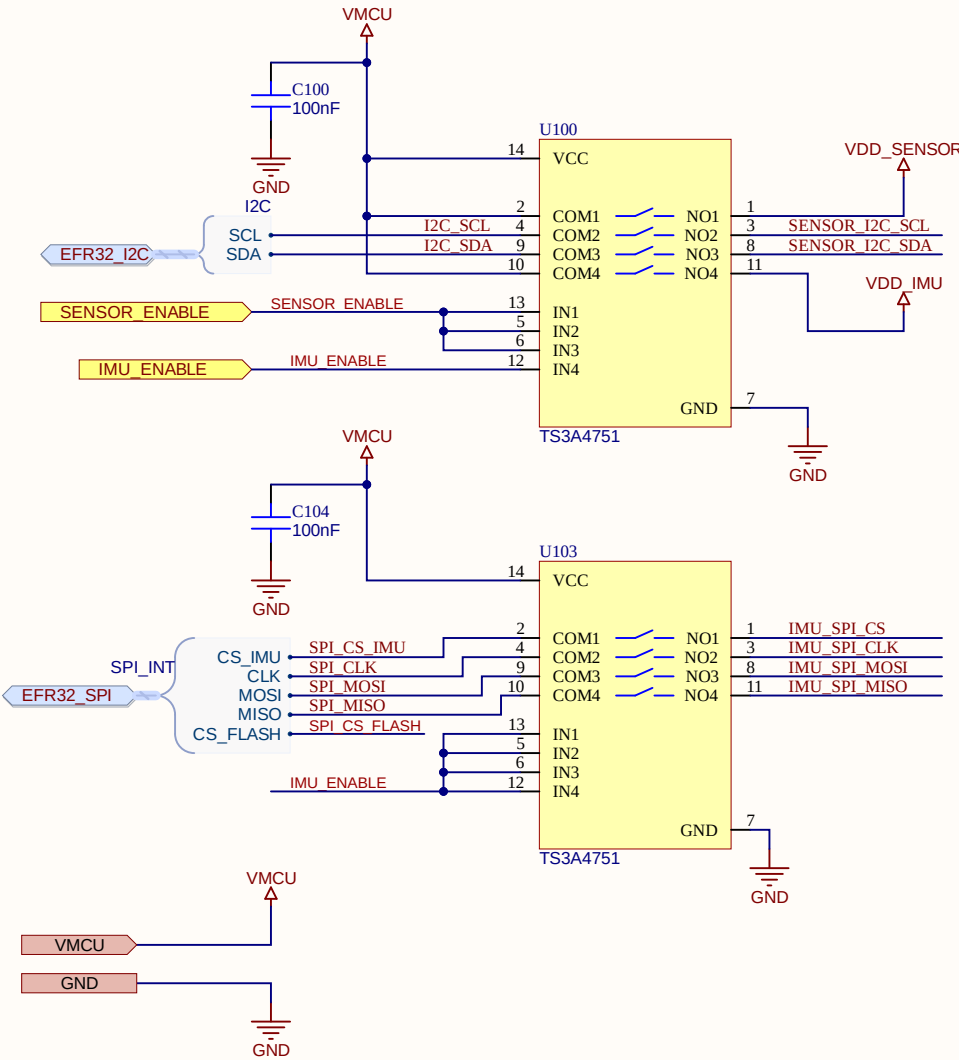
EFR32xG26 RF Selection



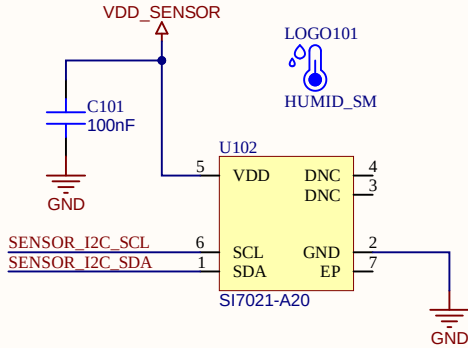
EFR32xG26 IOs and Signal Assignments



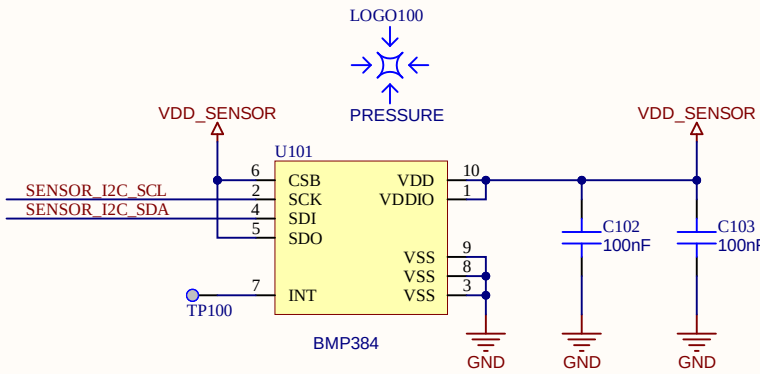
Sensor Power/Isolation



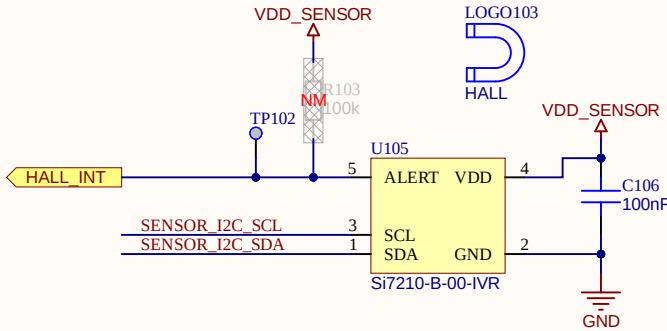
RH/Temp Sensor



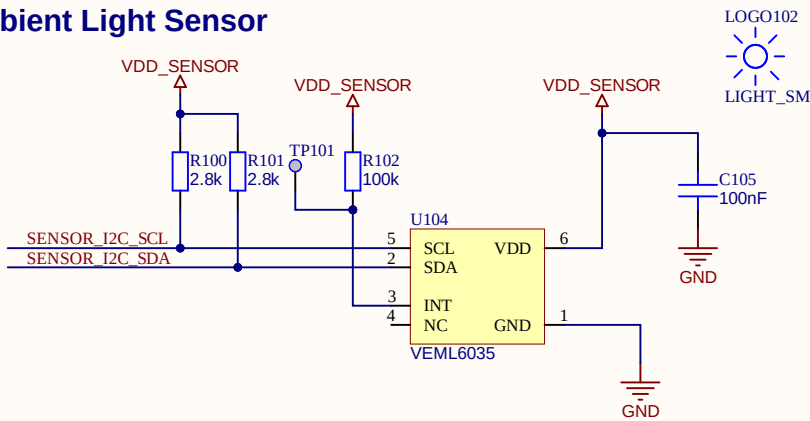
Pressure Sensor



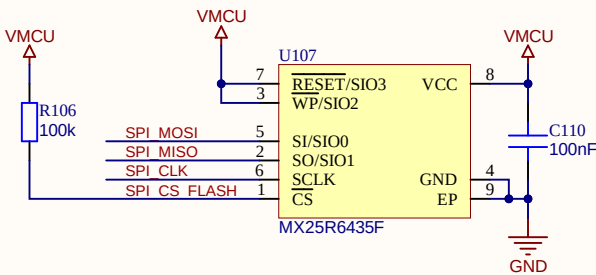
Hall-Effect Sensor



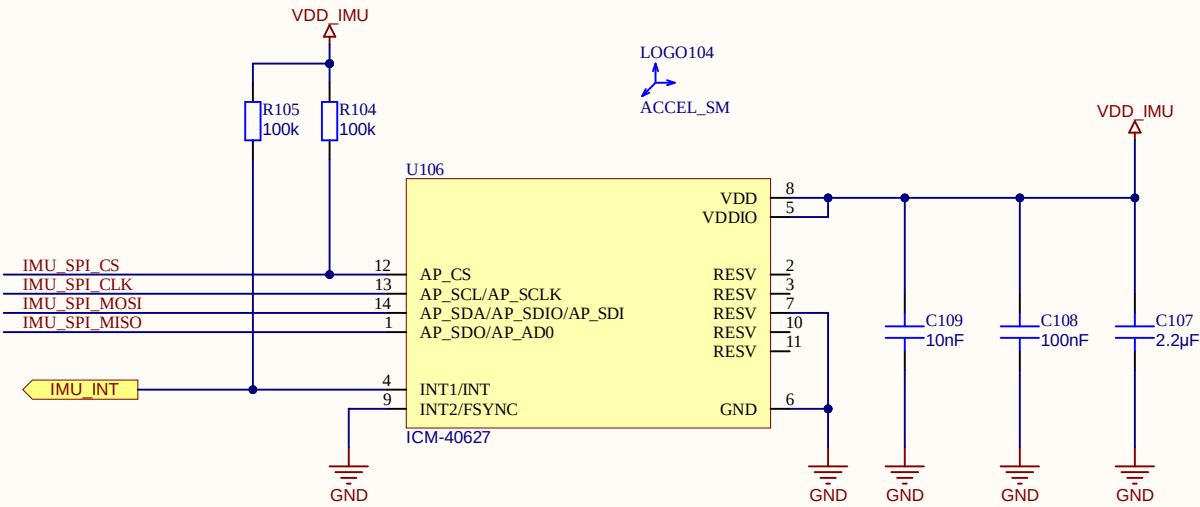
Ambient Light Sensor



SPI Flash

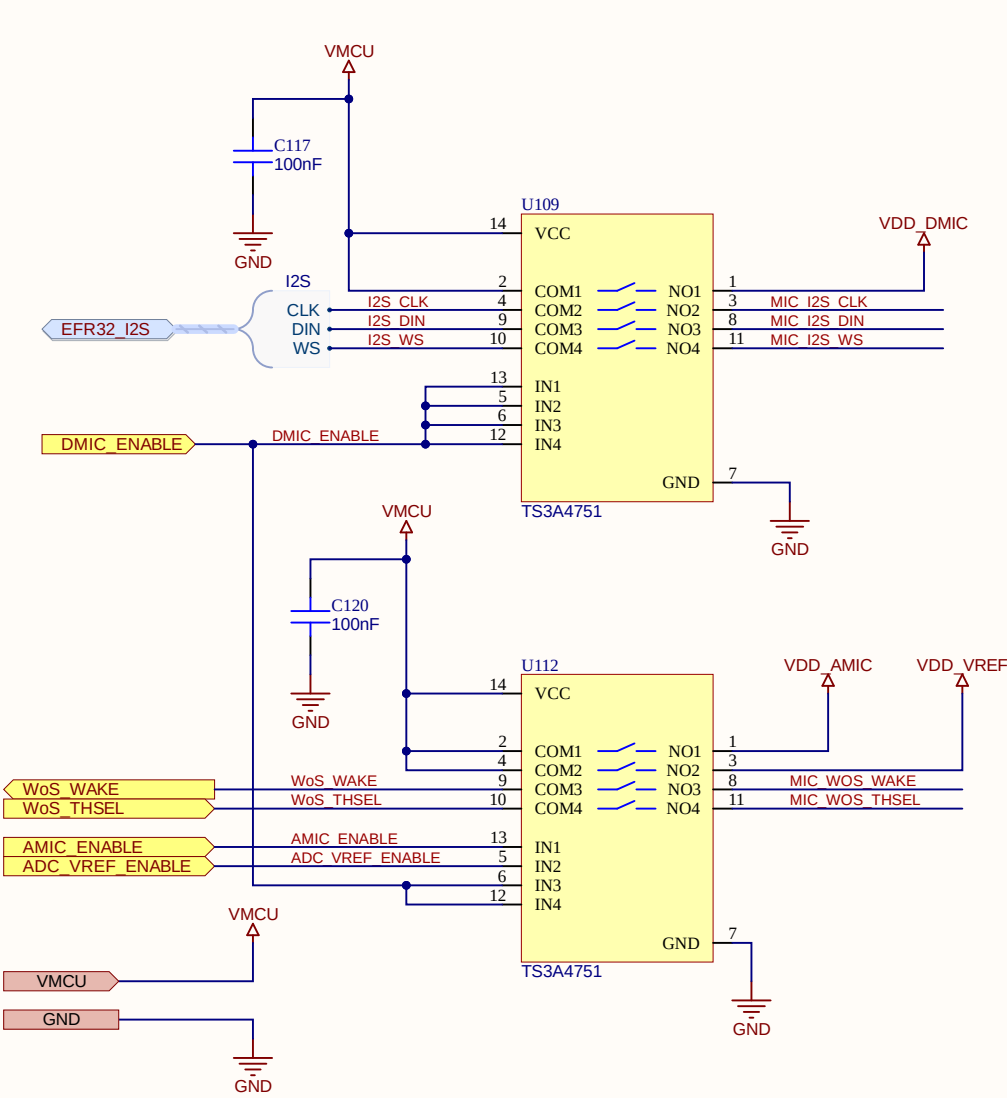


6-axis Inertial Sensor (IMU)

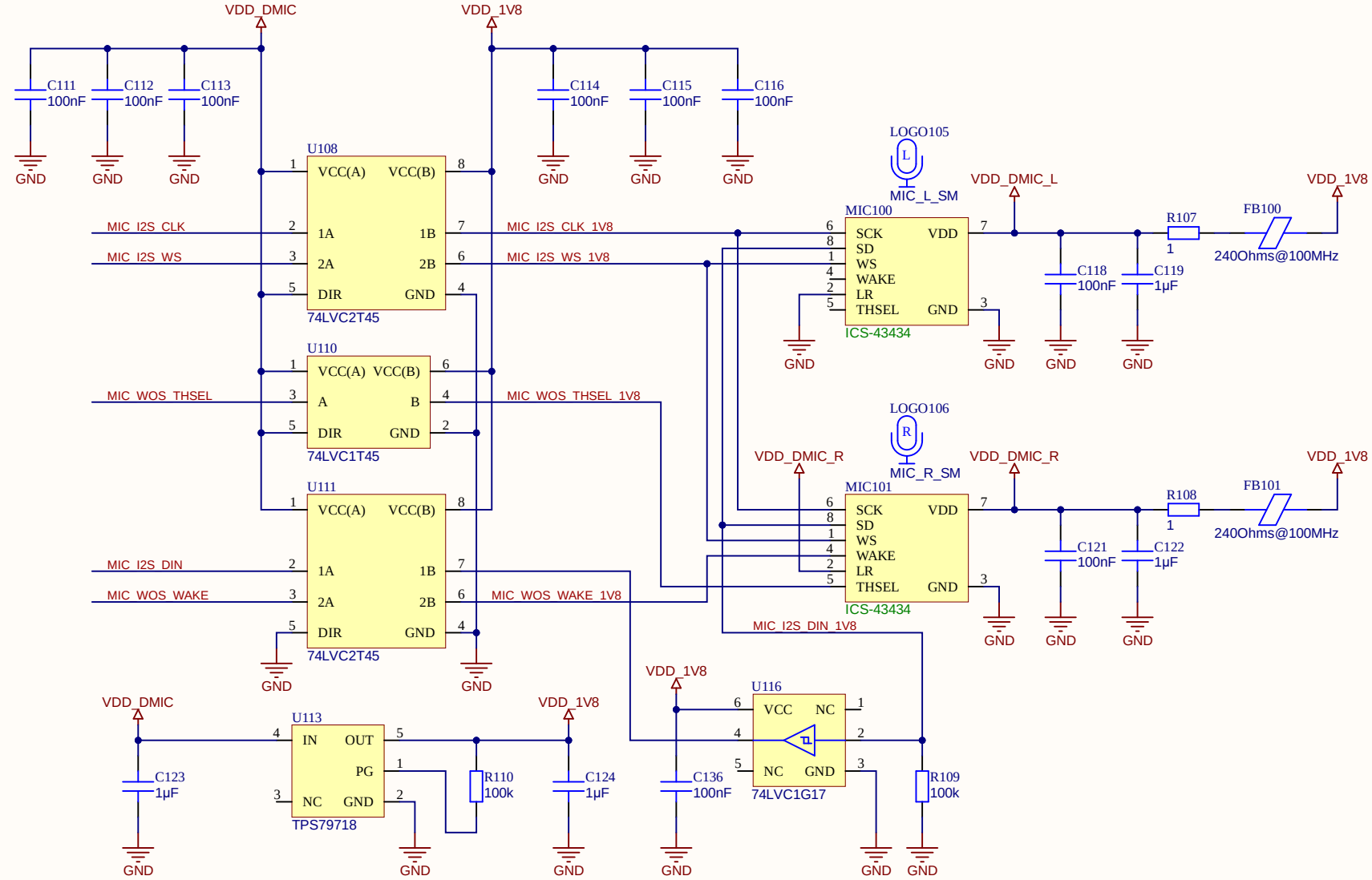


 SILICON LABS		Board name xG26 Dev Kit	
Designed CZD	Approved RGU	Page Title Sensors and Peripherals	
Size A3	Sheet Modified Date 2024. 11. 14.	Board number BRD2608A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 5 of 9

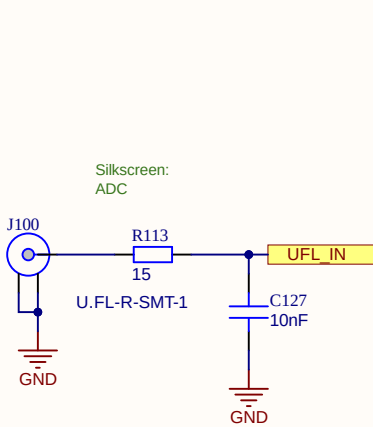
Power/Isolation



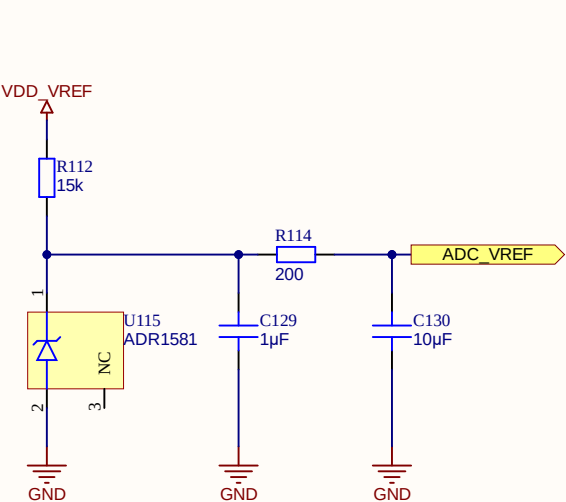
Digital Microphones with WOS



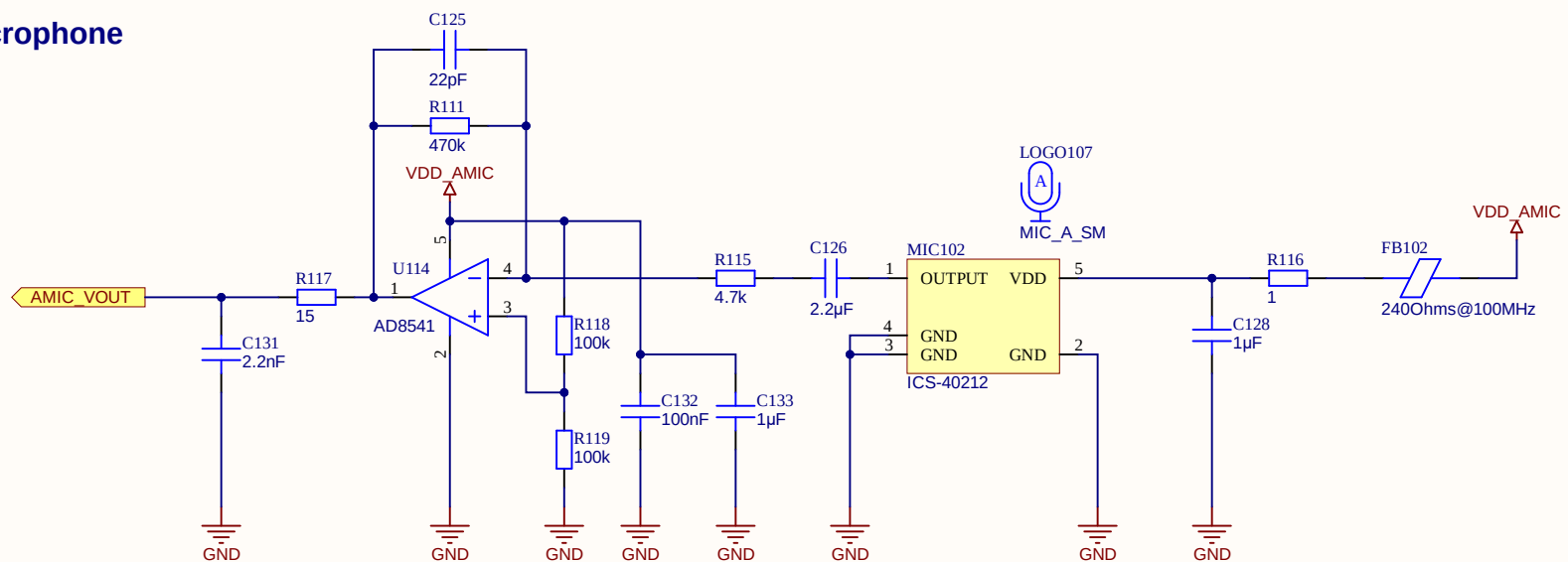
U.FL ADC Connector



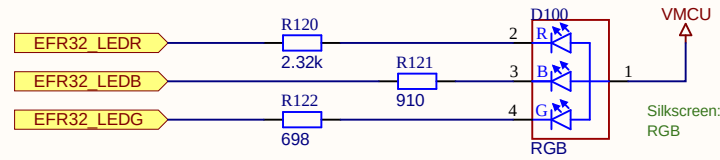
ADC VREF



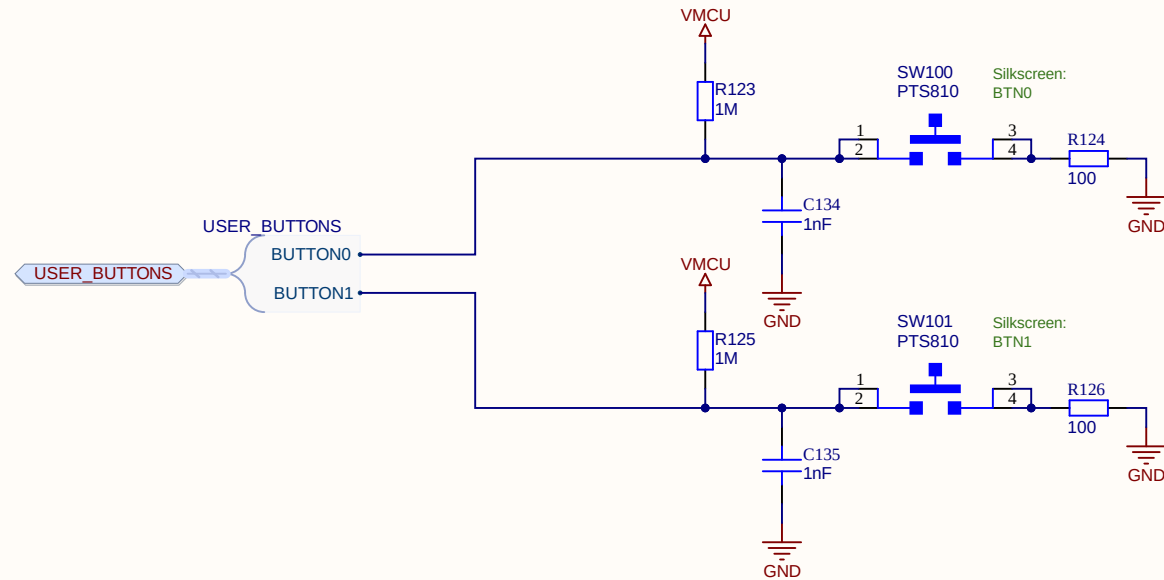
Analog Microphone



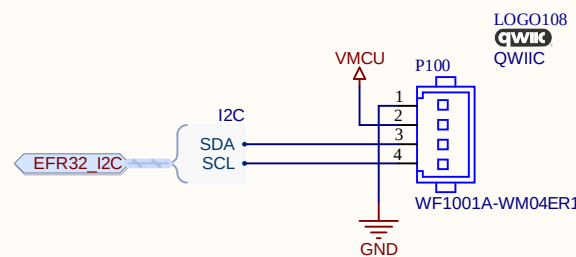
RGB LED



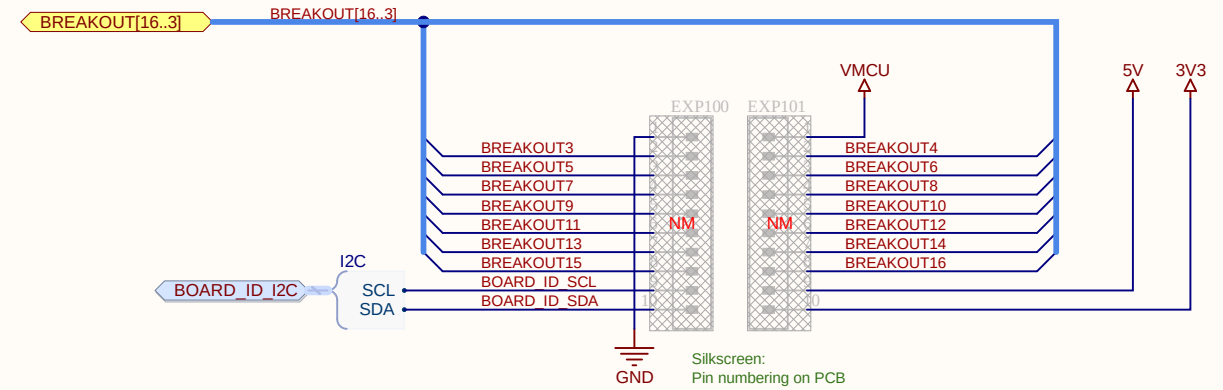
Push Buttons



QWIIC Connector



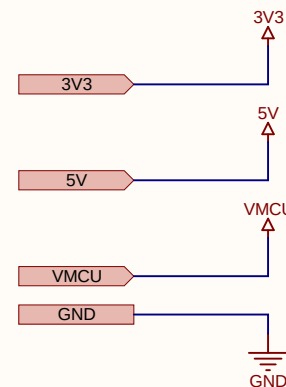
Breakout Pads - EXP Header



BREAKOUT Header Functionality

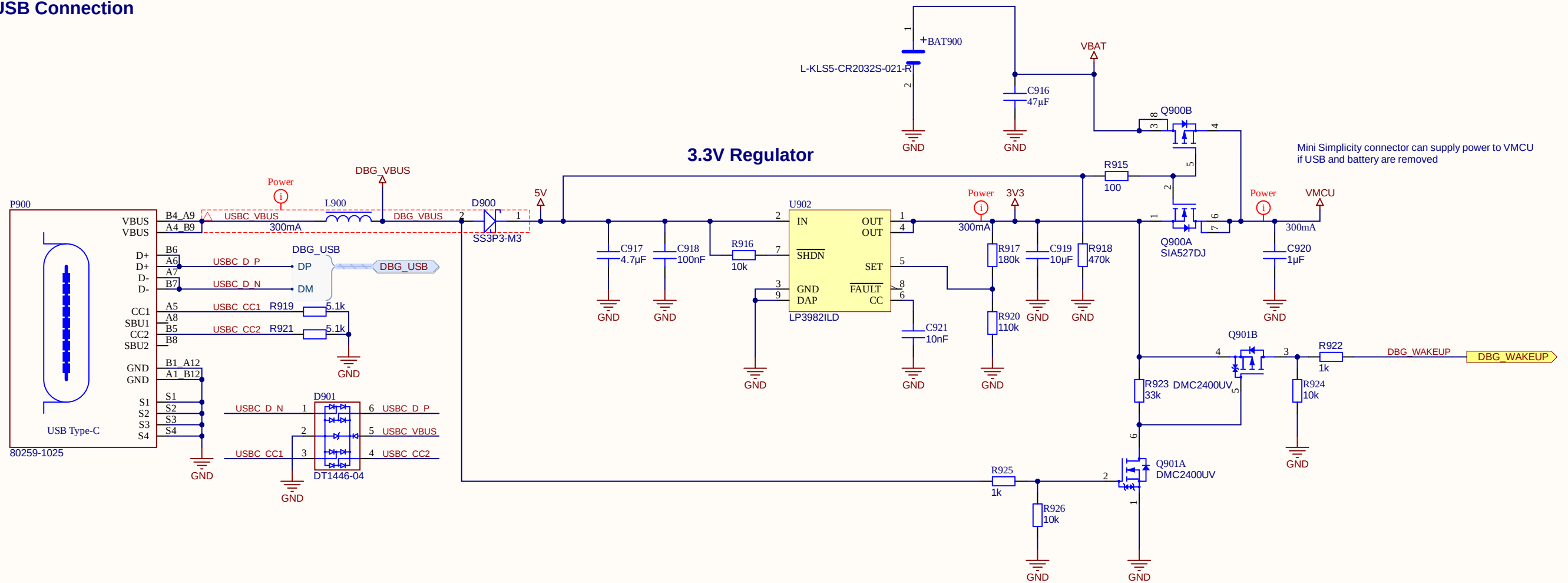
Pin Nr.	Pin Name	Alternate Function
1	GND	
3	PB07	UART_CTS
5	PB08	UART_RTS
7	AIN3	ADC_INN
9	AIN2	ADC_INP
11	PD08	I00
13	PD09	I01
15	PC04	I2C_SCL
17	BOARD_ID_SCL	
19	BOARD_ID_SDA	

Pin Nr.	Pin Name	Alternate Fuction
2	VMCU	
4	PC13	SPI_COPI
6	PC12	SPI_CIPO
8	PC11	SPI_CLK
10	PC10	SPI_CS
12	PB05	UART_TX
14	PB06	UART_RX
16	PC05	I2C_SDA
18	5V	
20	3V3	

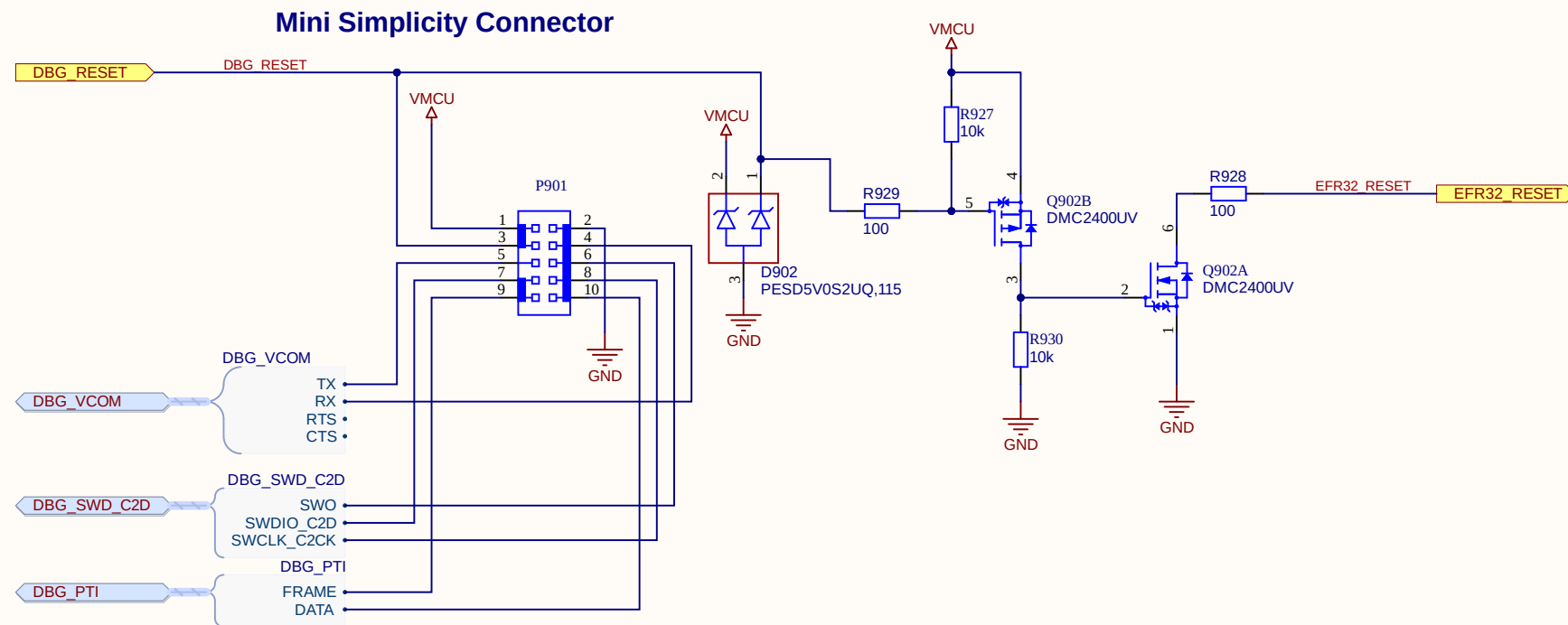


		Board name xG26 Dev Kit	
Designed CZD	Approved RGU	Page Title User Interface	
Size A3	Sheet Modified Date 2024. 09. 18.	Board number BRD2608A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 7 of 9

Debug USB Connection

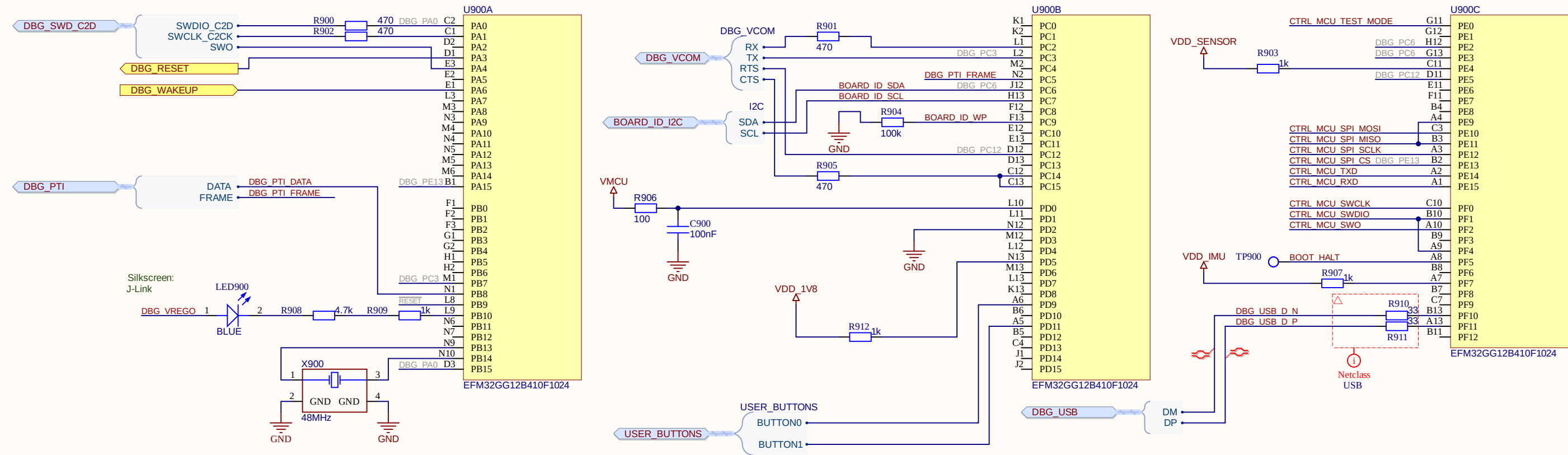


Non-inverting open drain buffer

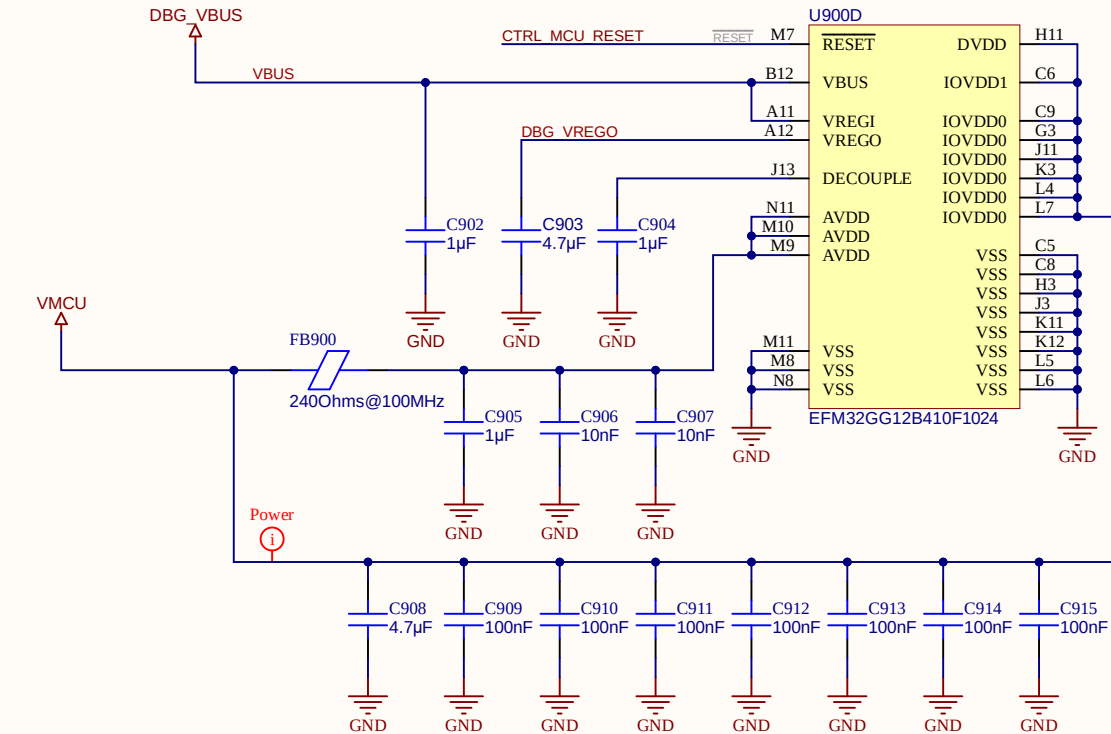


		Board name xG26 Dev Kit	
Designed CZD	Approved RGU	Page Title Power and Mini-Simplicity	
Size A3	Sheet Modified Date 2024. 11. 14.	Board number BRD2608A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 8 of 9

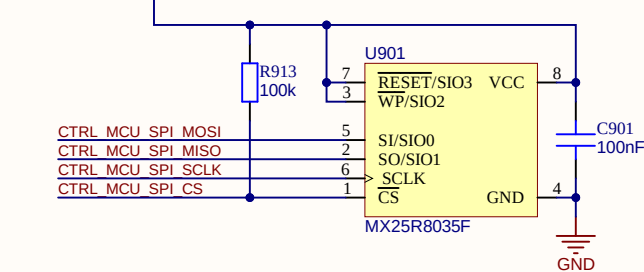
On-board Debugger



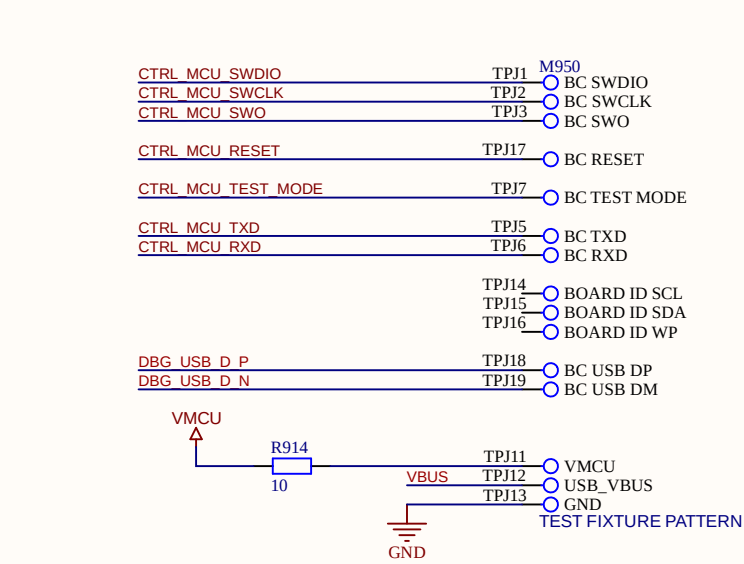
Debugger Power



Serial Flash



Test Points



		Board name xG26 Dev Kit	
Designed CZD	Approved RGU	Page Title On-board debugger	
Size A3	Sheet Modified Date 2024. 09. 18.	Board number BRD2608A	Revision A04--
COPYRIGHT SILICON LABORATORIES INC. 2024 CONFIDENTIAL - SUBJECT TO TERMS OF USE			
Sheet 9 of 9			