



MG24 Explorer Kit

Board Function

Page

Title Page

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EFR32MG24

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User Interface

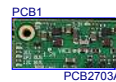
3

On-board Debugger

4

Power & Mini Simplicity

5



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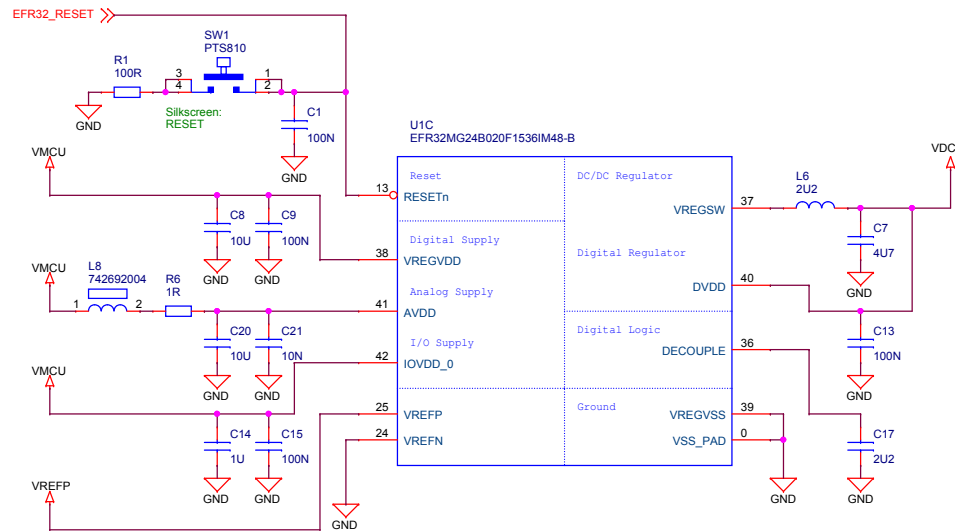
Revision History

Rev. Description

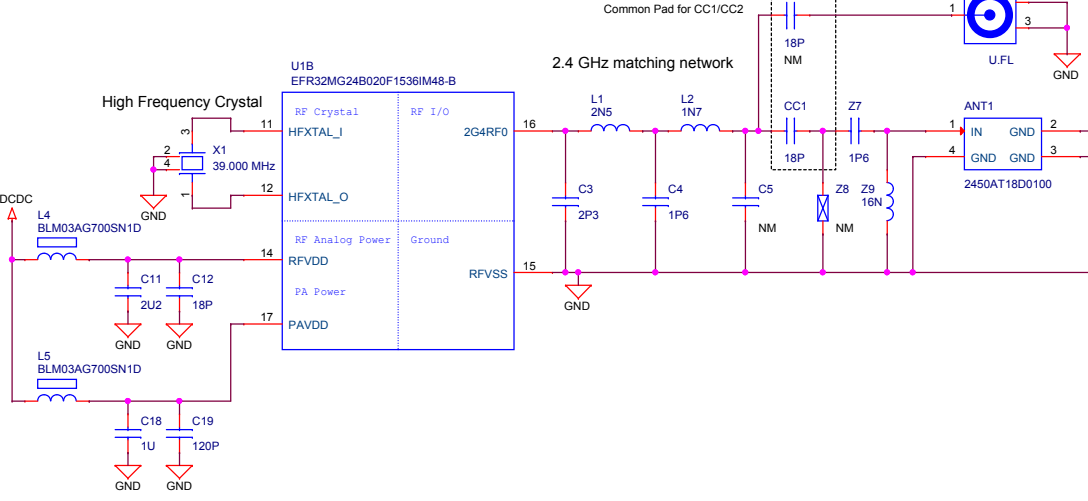
A00 Initial version.

 SILICON LABS		Board Name EFR32MG24 Explorer Kit	
		Page Title Title Page	
Designed DDC	Approved RGU	Board Number BRD2703A	Revision A00
Size A3	Sheet Modified Date Friday, April 08, 2022		Sheet 1 of 5
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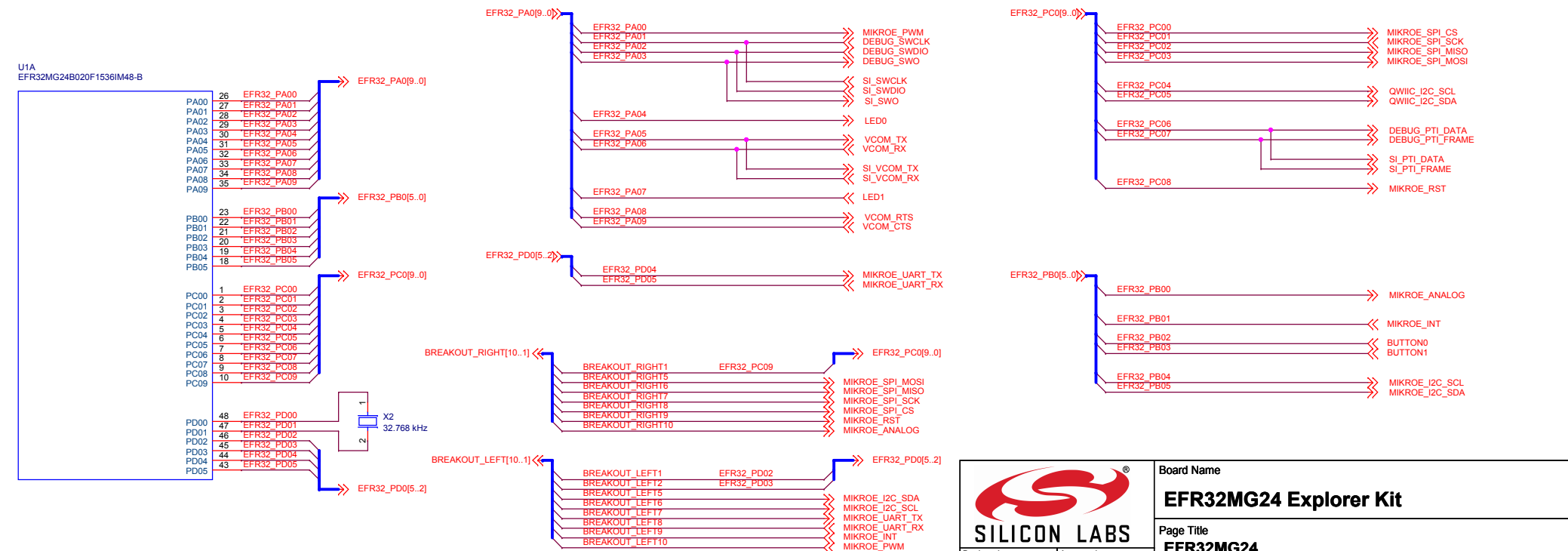
EFR32MG24 Power Section



EFR32MG24 RF Section

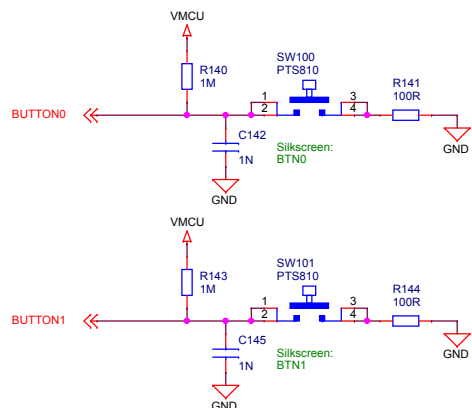


EFR32MG24 I/O & Signal Assignments

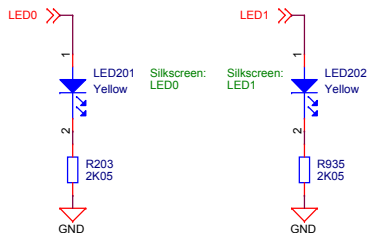


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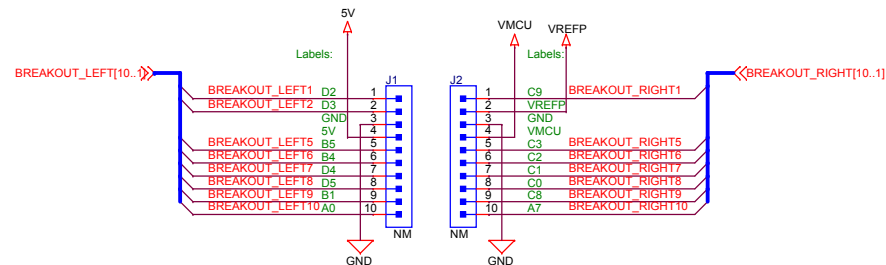
Push Buttons



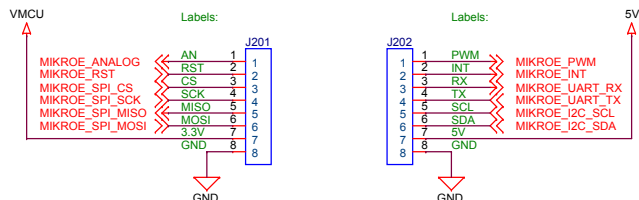
RGB LED



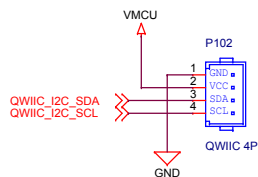
Breakout Pads



MikroE Socket

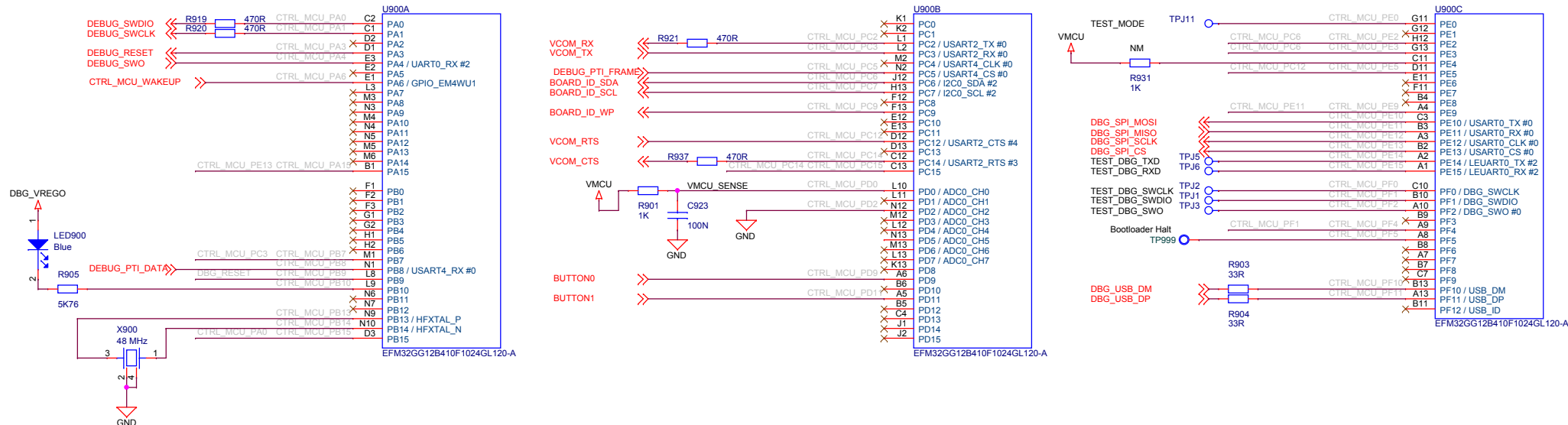


Qwiic Connector

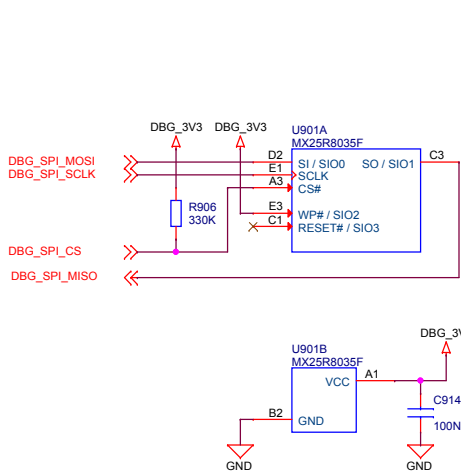


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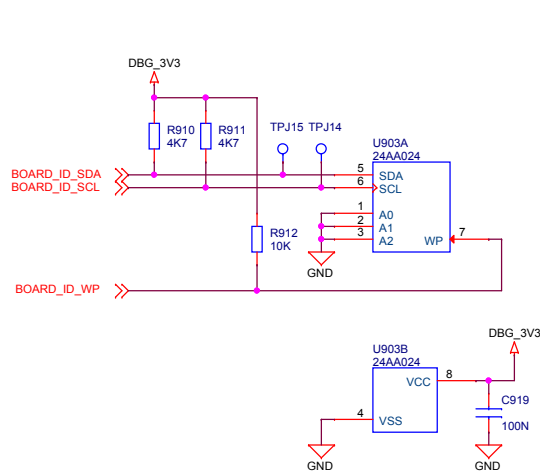
On-board Debugger



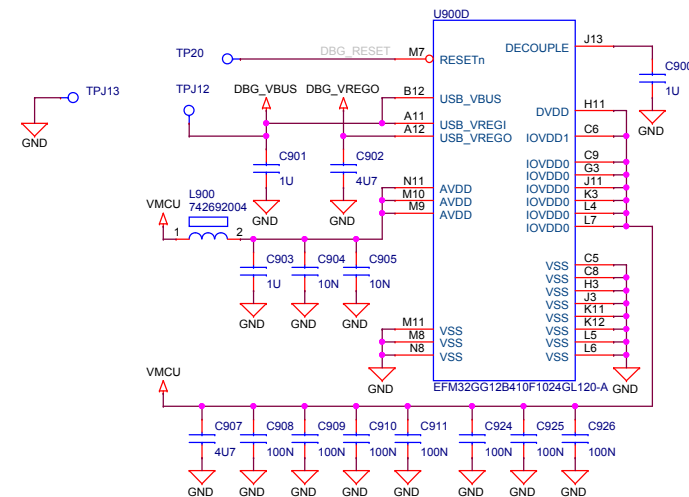
Serial Flash



Board ID



Power & Decoupling

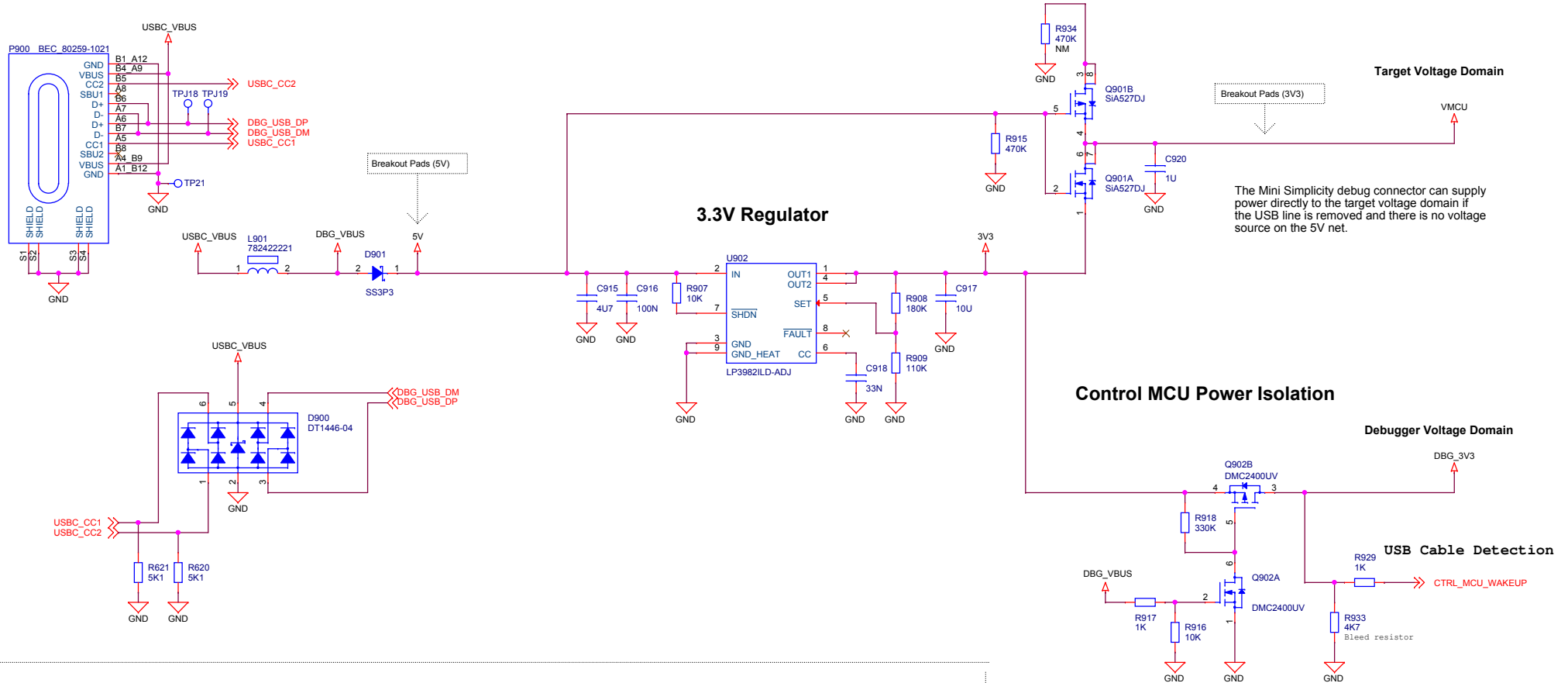


Mechanical

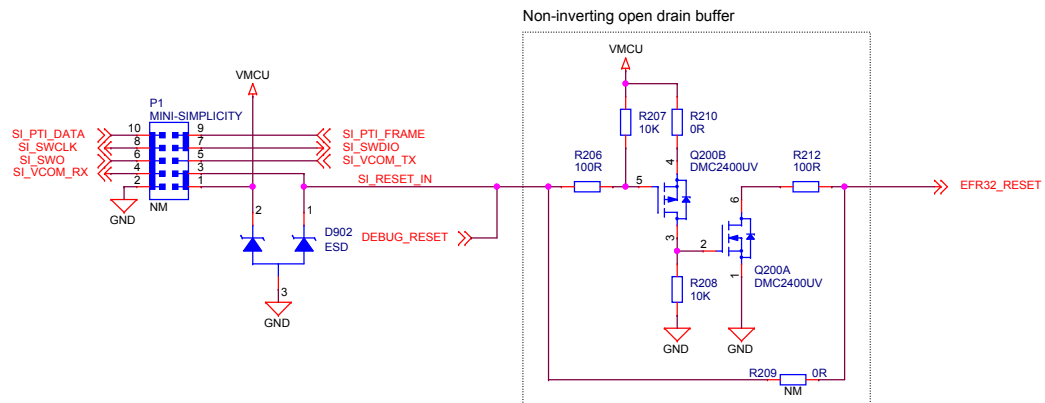


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Debug USB Connection



Mini Simplicity Connector



RESETn on U1 has an internal pull-up to DVDD. The purpose of this circuit is to prevent current flowing from VMCU to DVDD through this pull-up if a connected debugger has a pull-up resistor from reset to VMCU.

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