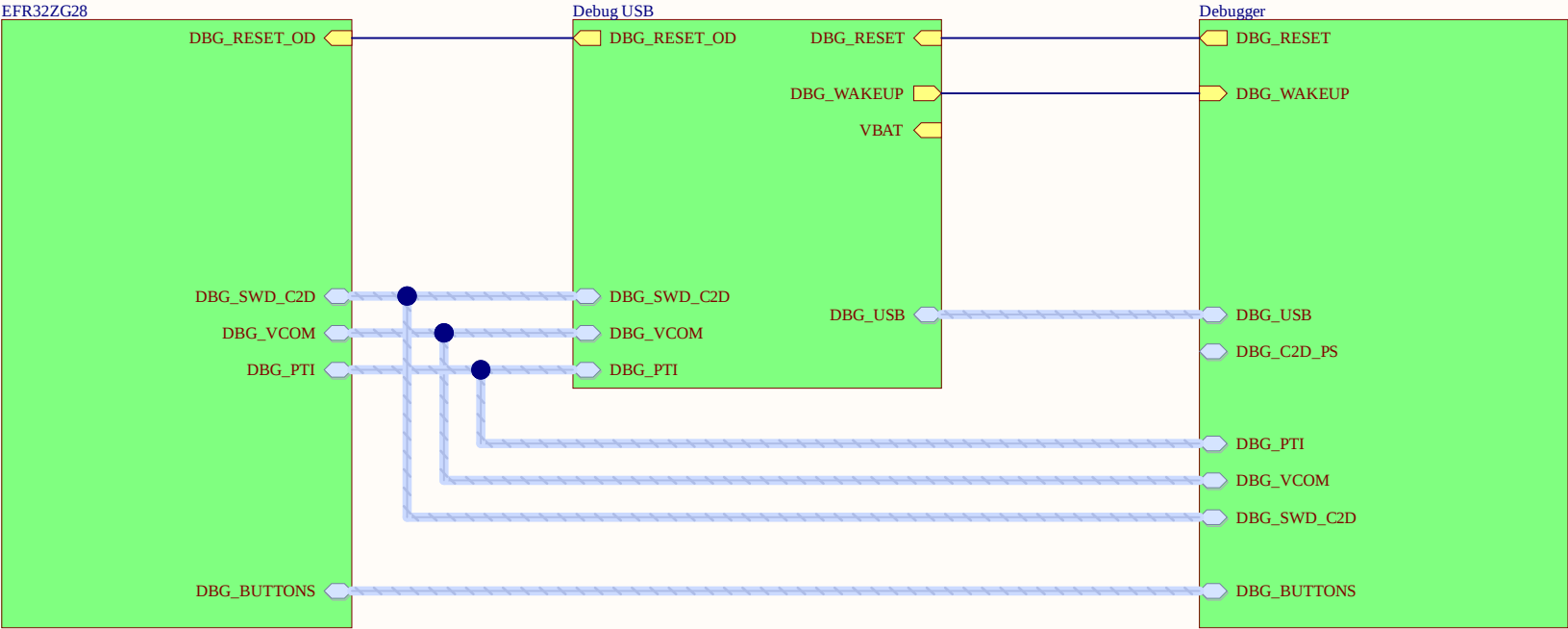


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REVISION HISTORY

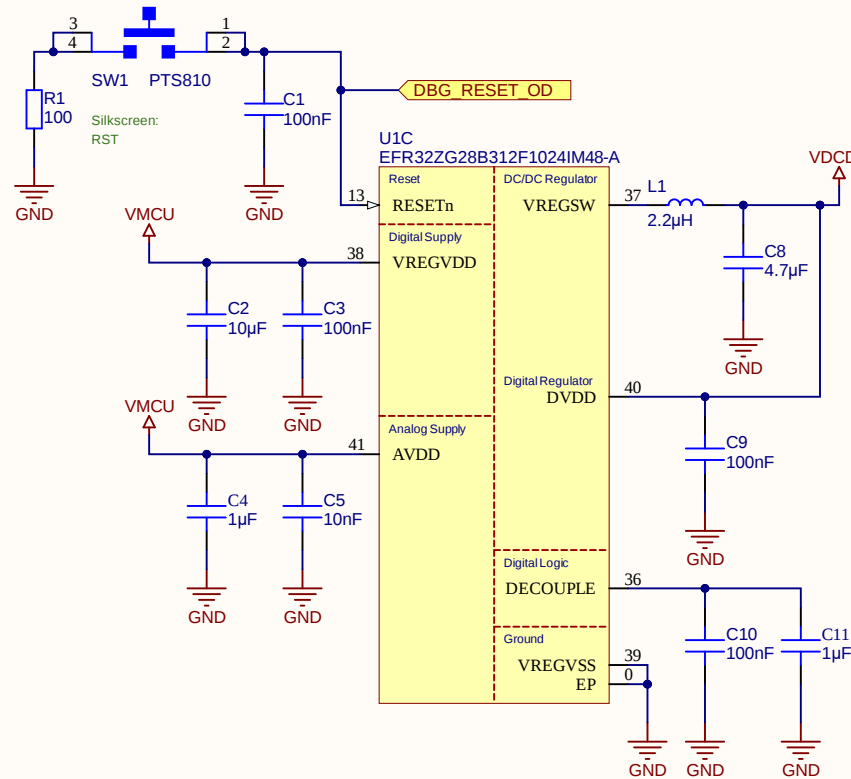
Rev.	Description
A01	First release
A02	BOM changes for the RF matching section L8, L10, C28 changed L9, C29 made DNP



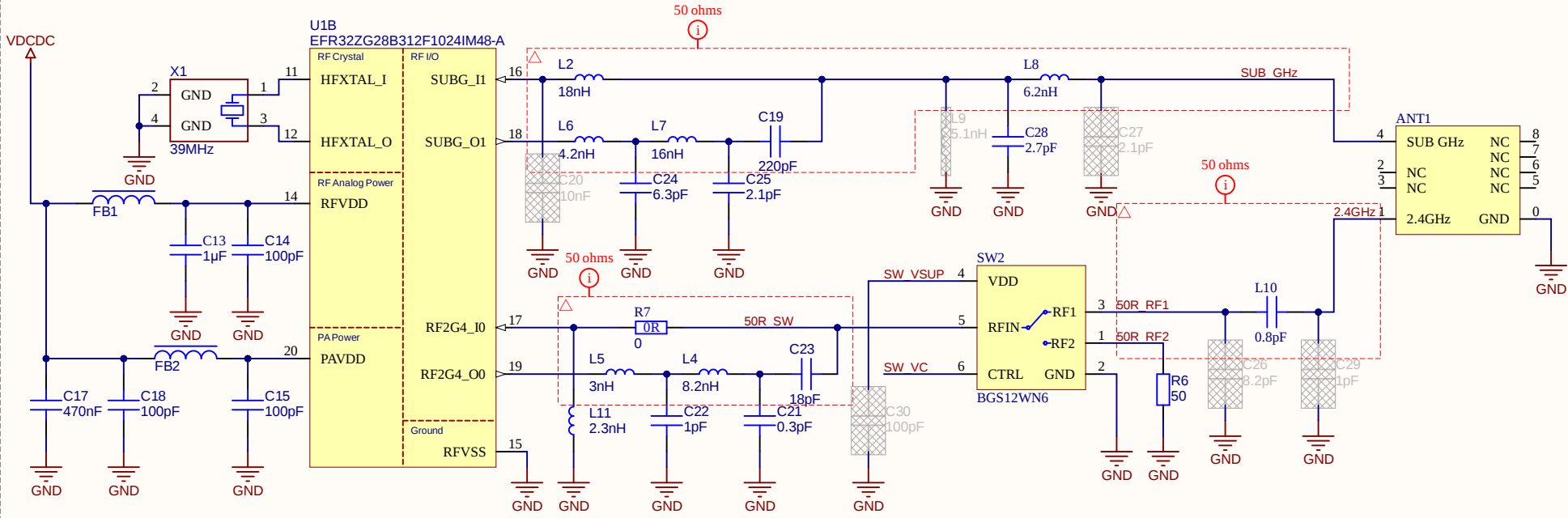
Board name
EFR32ZG28 Explorer Kit

Designed DDC	Approved RGU	Page Title Table of Contents and Revision History	
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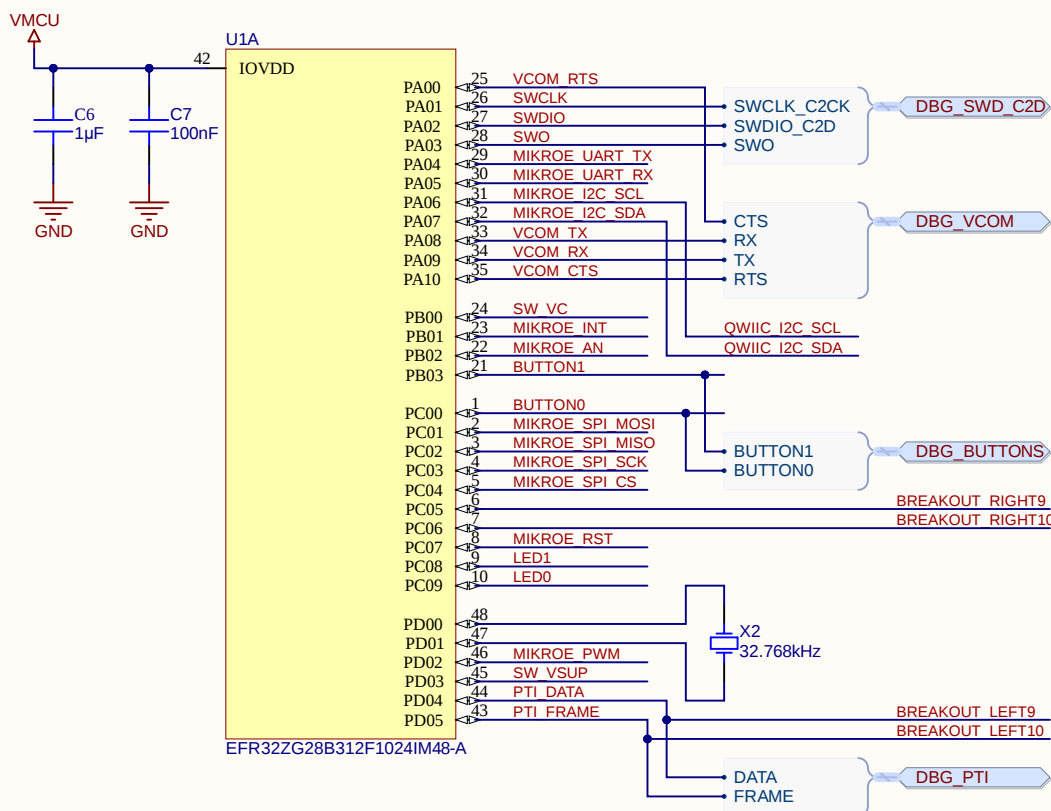
EFR32ZG28 Power



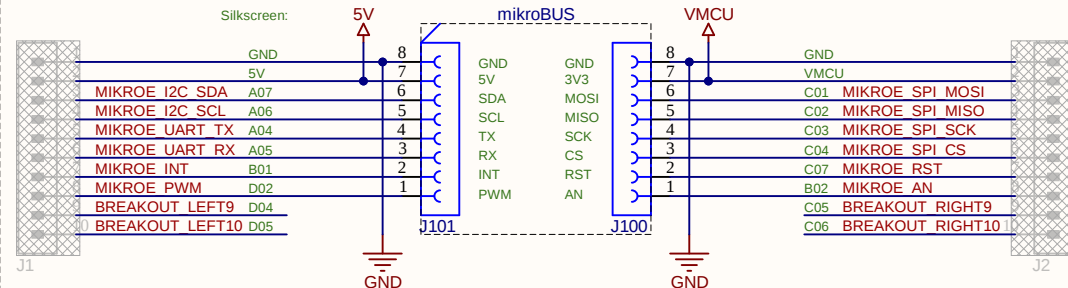
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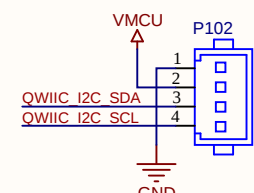
EFR32ZG28 I/O & Signal Assignments



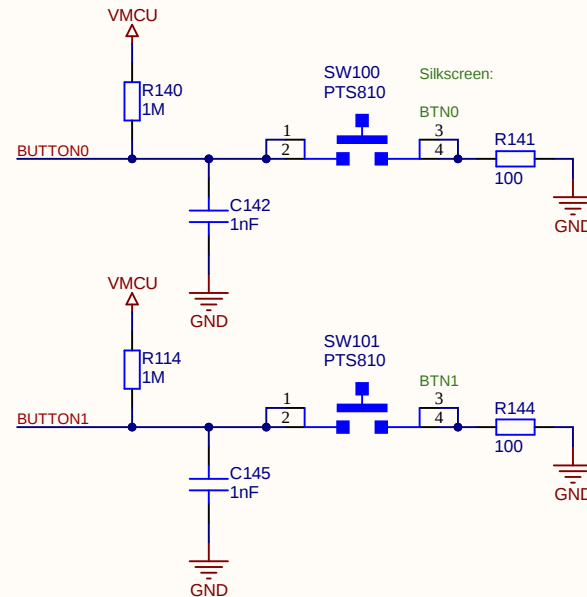
Breakout Connections & mikroBUS Socket



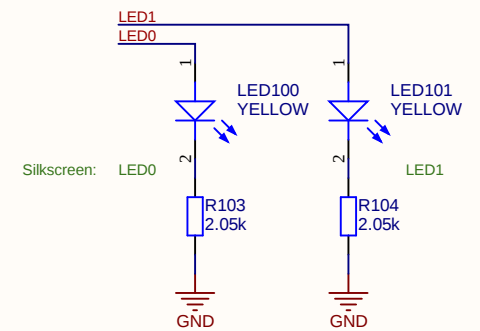
qwiiic Header



Push Buttons



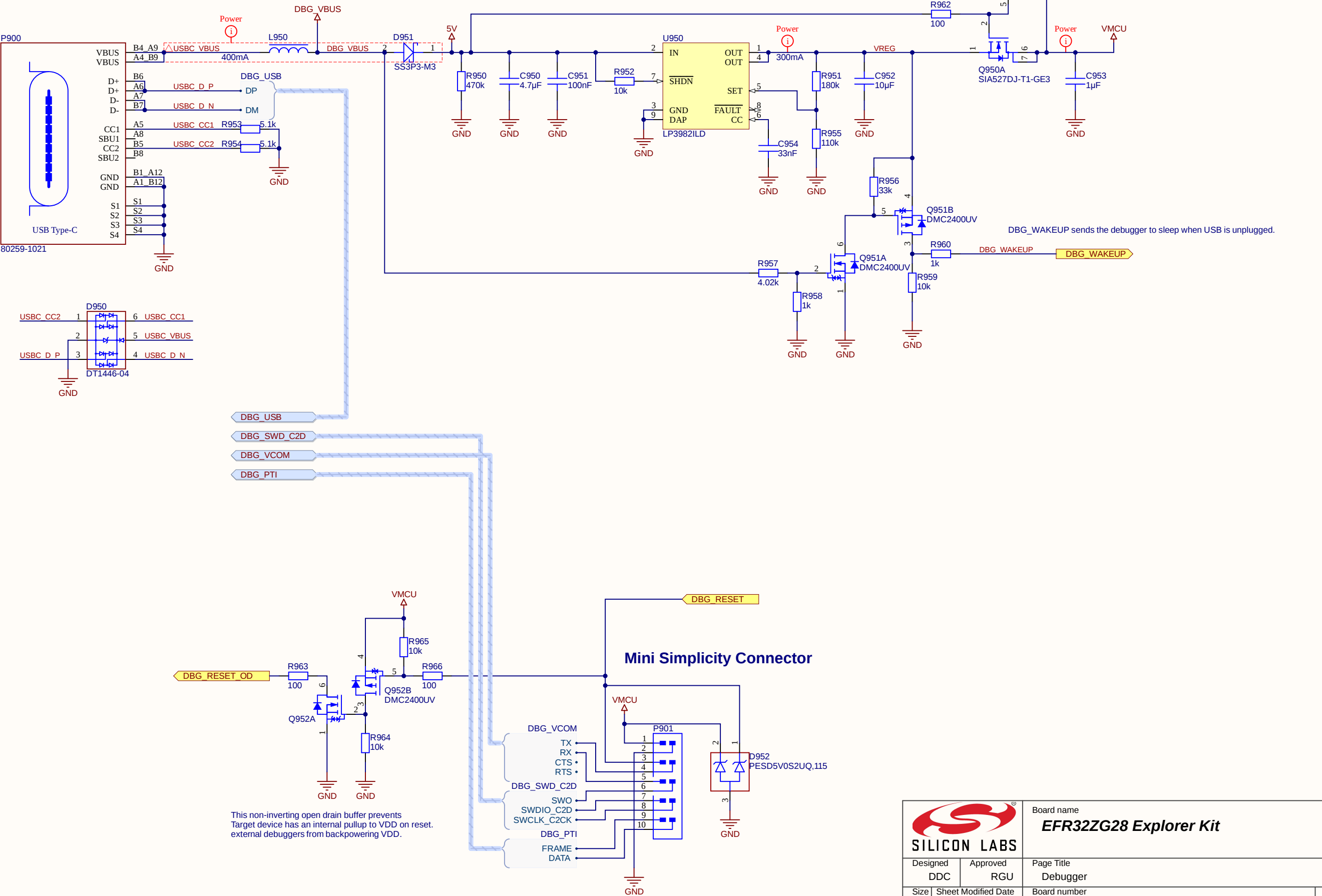
User LEDs



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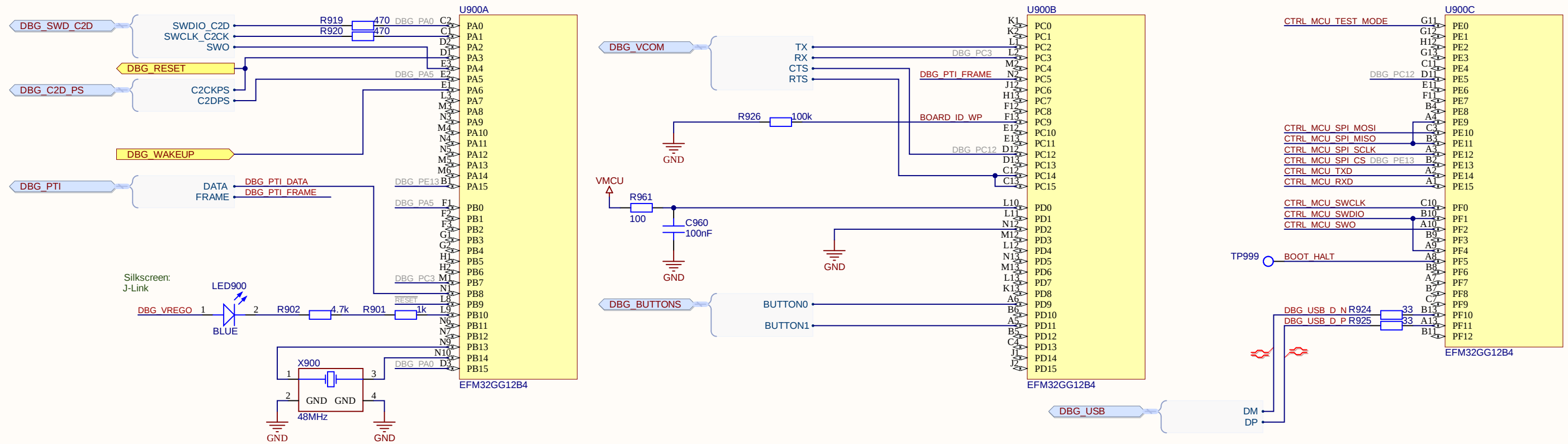
Debug USB Connection

3.3V Regulator

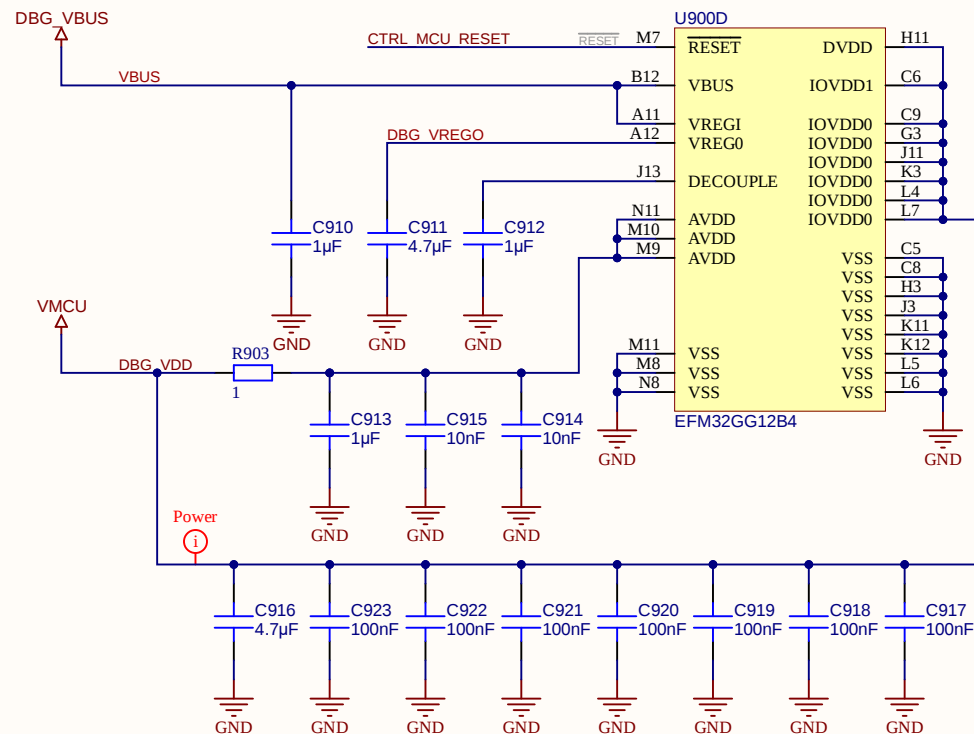


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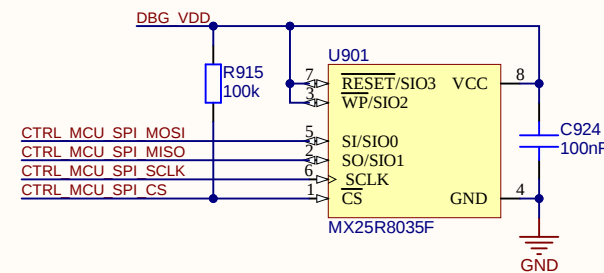
On-board Debugger



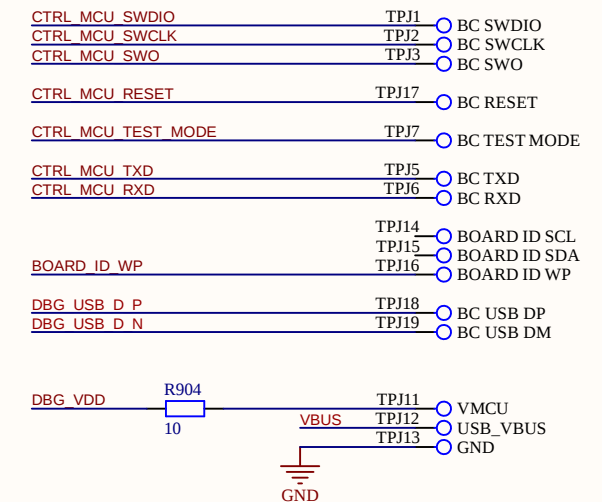
Debugger Power



Serial Flash



Test Points



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