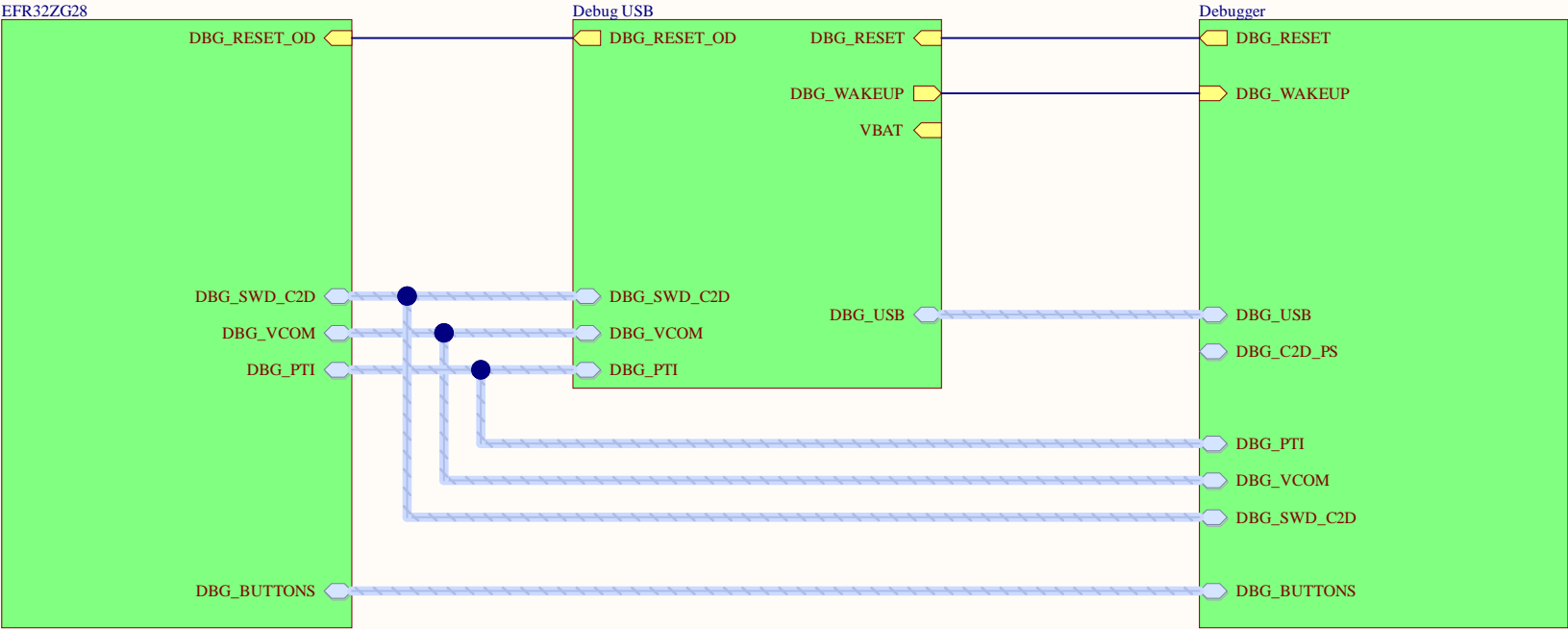


TABLE OF CONTENTS:

Page 1	Hierarchical Overview
Page 2	Target Device Power
Page 3	On-board Debugger
Page 4	Debugger

REVISION HISTORY

Rev.	Description
A01	First release



Board name
EFR32ZG28 Explorer Kit

Designed
DDC

Approved
RGU

Page Title
Table of Contents and Revision History

Size
A3

Sheet Modified Date
5/18/2023

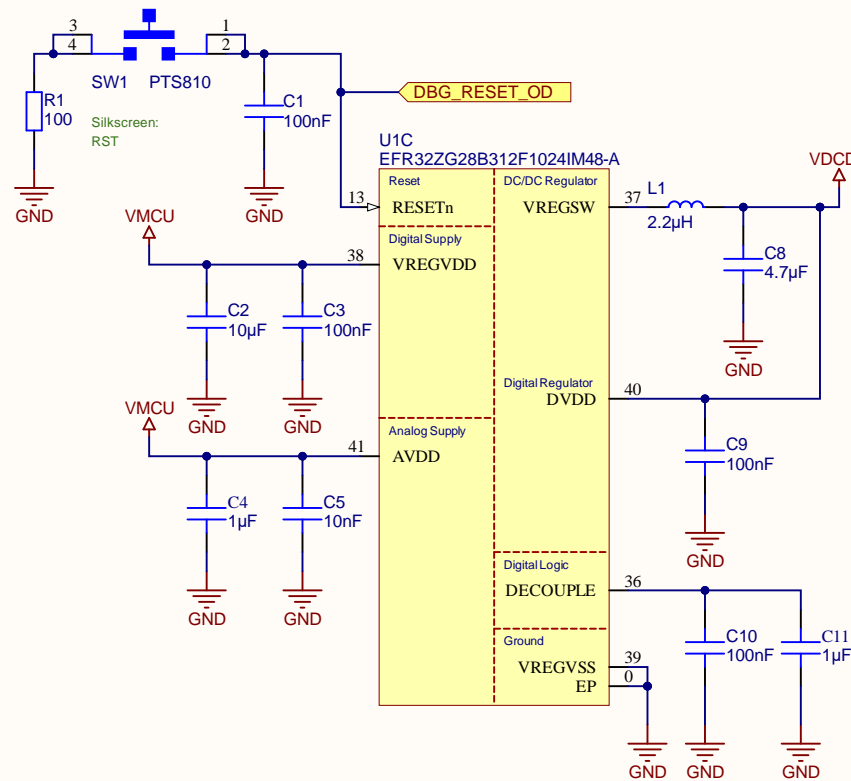
Board number
BRD2705A

Revision
A01--

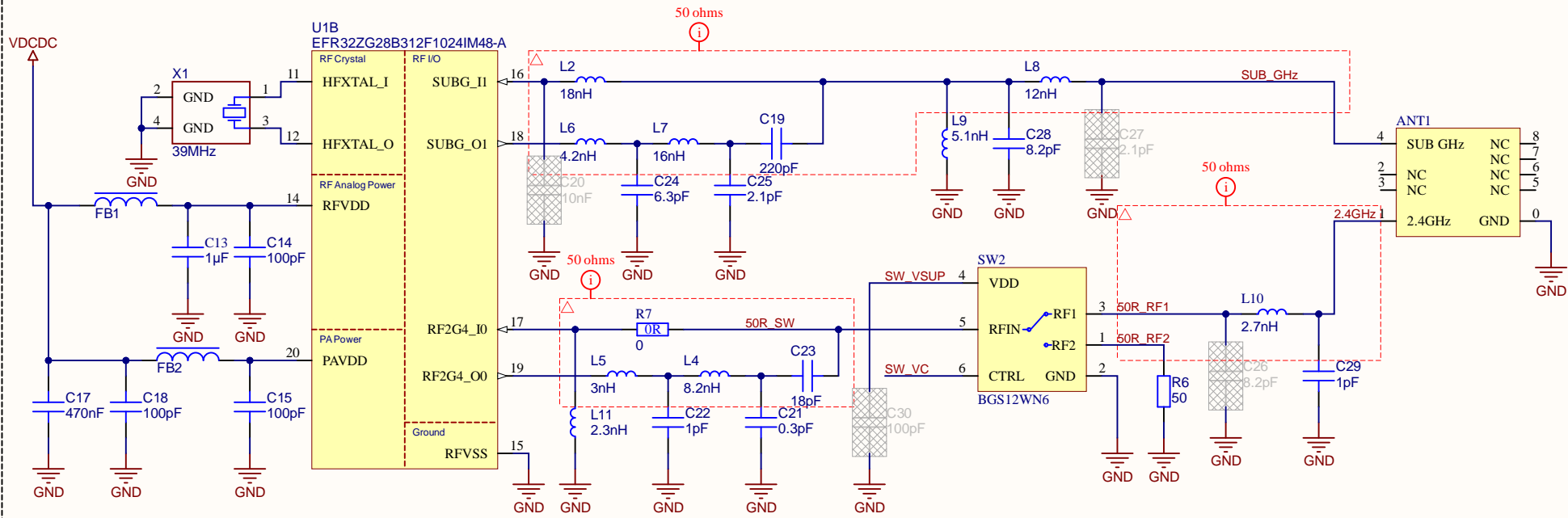
COPYRIGHT SILICON LABORATORIES INC. 2023 CONFIDENTIAL - SUBJECT TO TERMS OF USE

Sheet
1 of 4

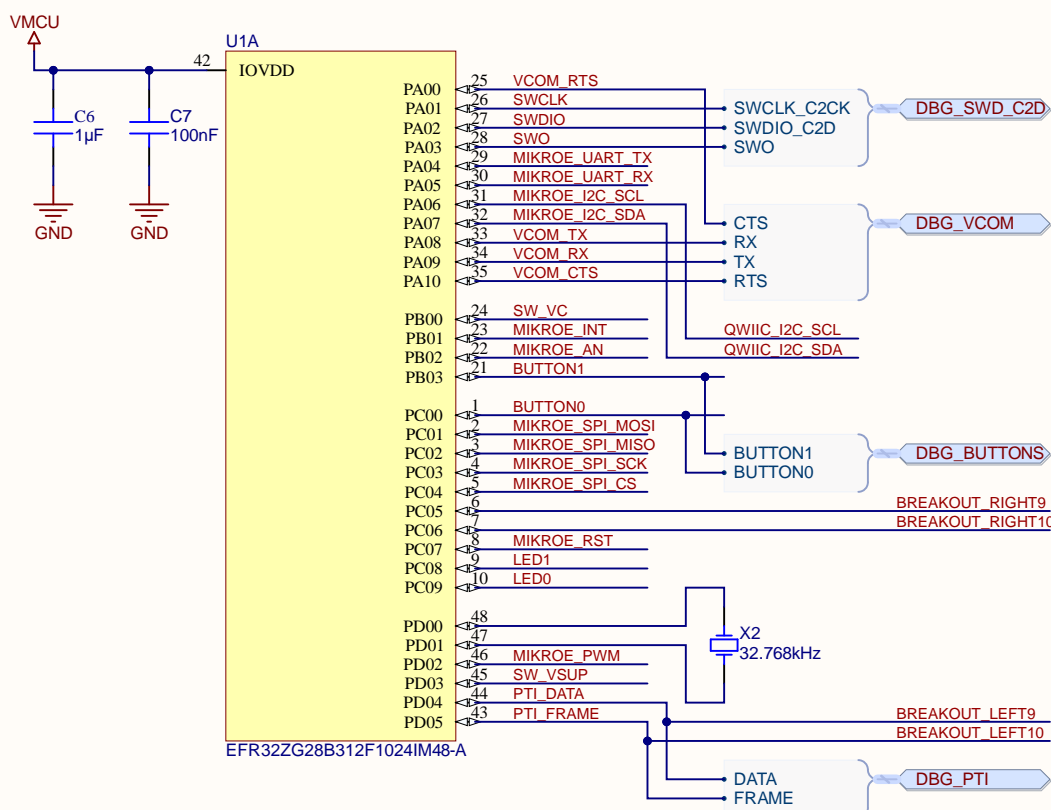
EFR32ZG28 Power



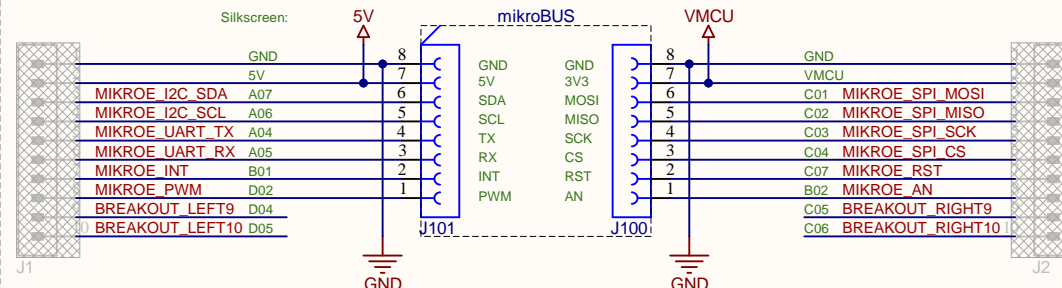
EFR32ZG28 RF



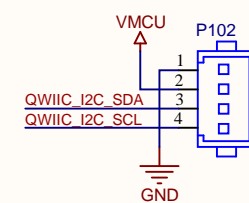
EFR32ZG28 I/O & Signal Assignments



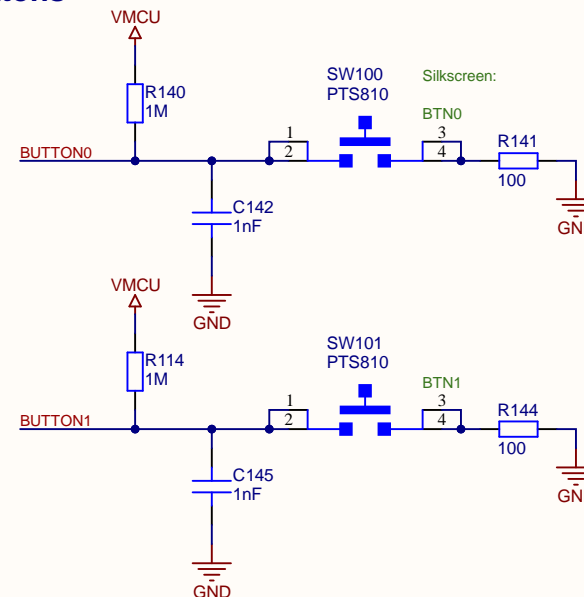
Breakout Connections & mikroBUS Socket



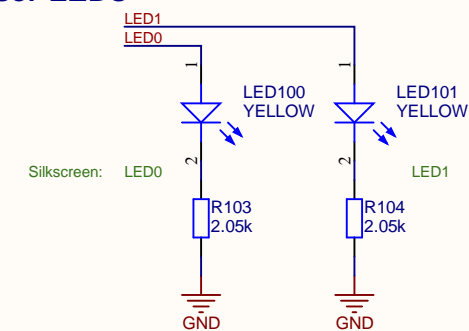
qwiic Header




Push Buttons



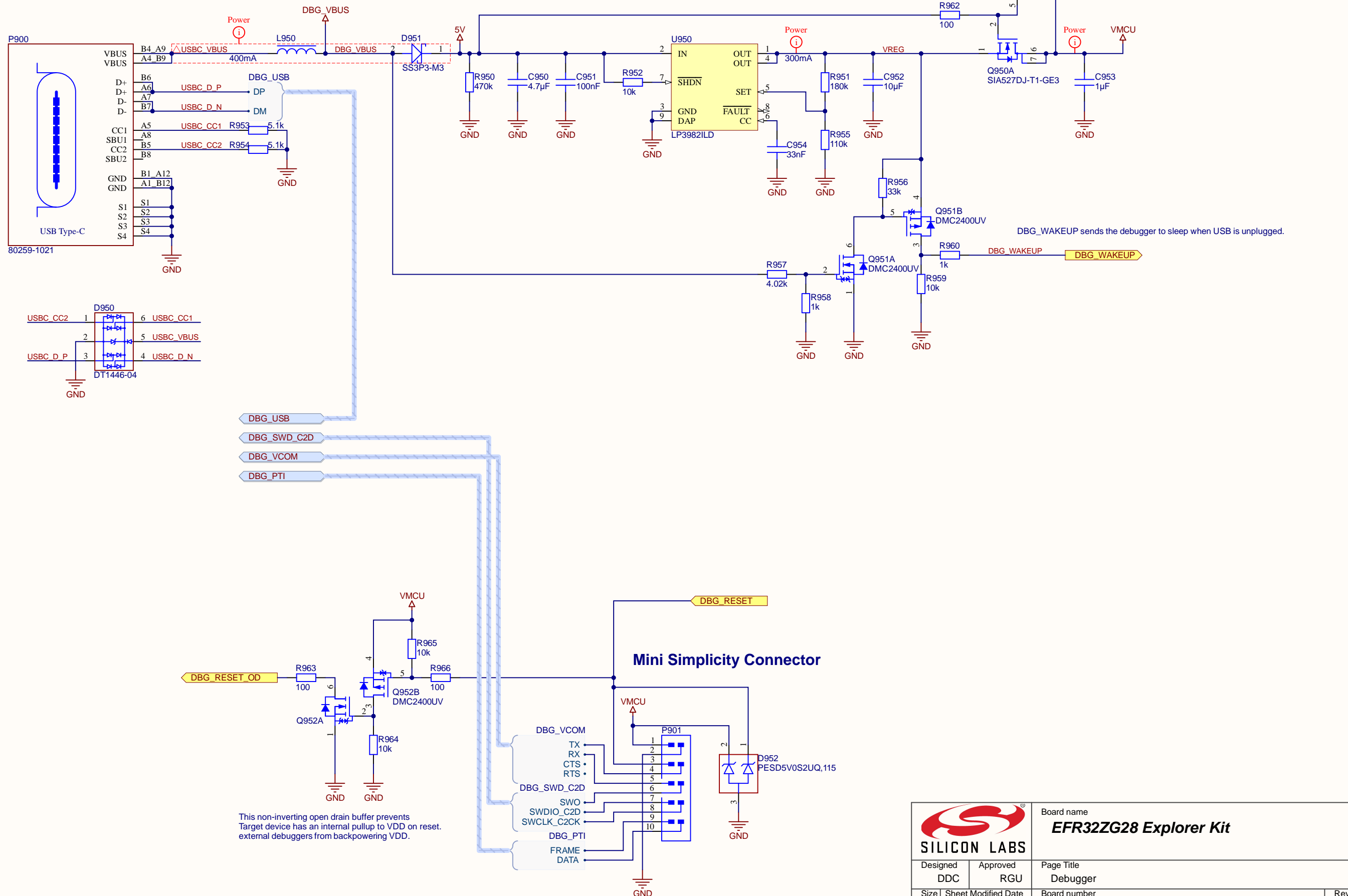
User LEDs




 SILICON LABS		Board name <i>EFR32ZG28 Explorer Kit</i>	
Designed DDC	Approved RGU	Page Title Target Device Power	
Size A3	Sheet Modified Date 5/17/2023	Board number BRD2705A	Revision A01--
COPYRIGHT SILICON LABORATORIES INC. 2023 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 2 of 4

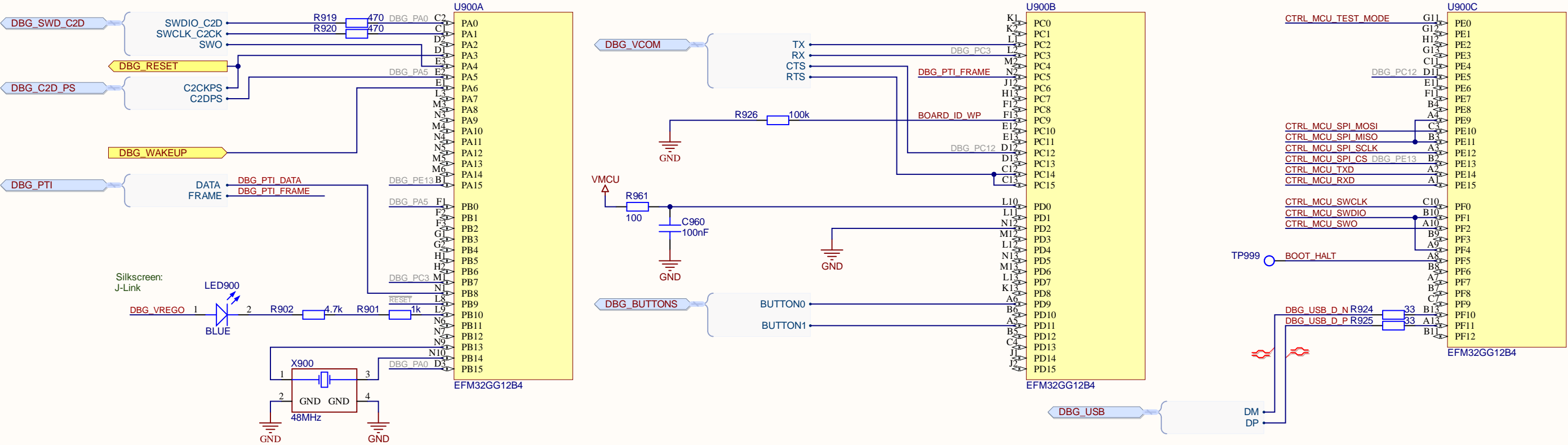
Debug USB Connection

3.3V Regulator

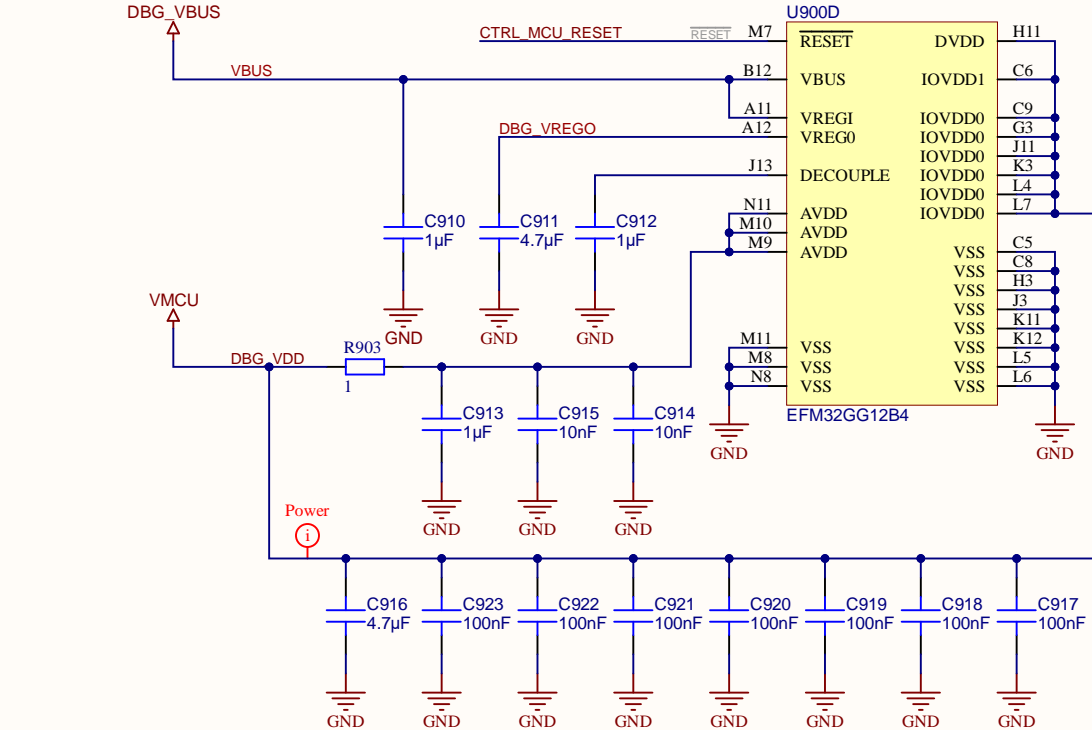


 SILICON LABS		Board name <i>EFR32ZG28 Explorer Kit</i>	
Designed DDC	Approved RGU	Page Title Debugger	
Size A3	Sheet Modified Date 5/17/2023	Board number BRD2705A	Revision A01--
COPYRIGHT SILICON LABORATORIES INC. 2023 CONFIDENTIAL - SUBJECT TO TERMS OF USE			Sheet 3 of 4

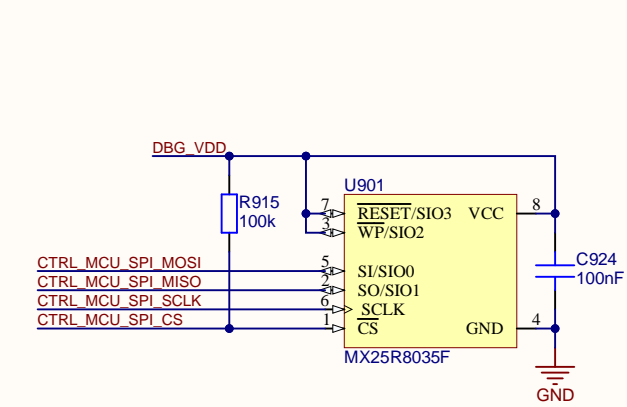
On-board Debugger



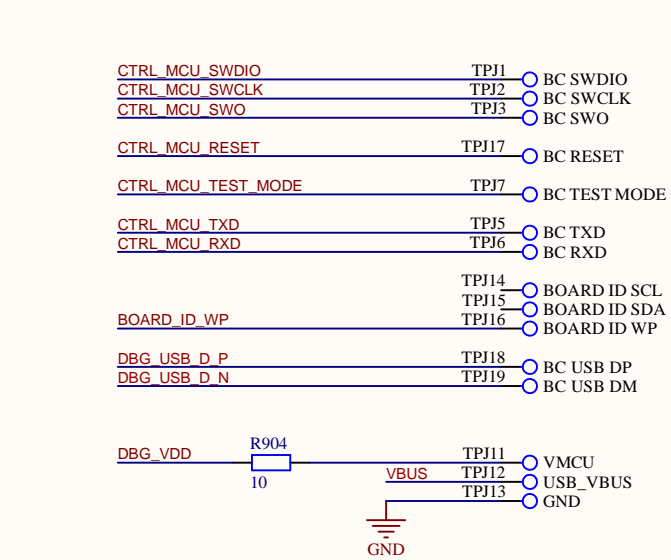
Debugger Power



Serial Flash



Test Points



Board name

EFR32ZG28 Explorer Kit

Page Title

Debugger

Board number

BRD2705A

Revision

A01--

ATORIES INC. 2023

CONFIDENTIAL - SUBJECT TO TERMS OF USE

Sheet

4 of 4