




LEGAL NOTICE:
SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC. IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.

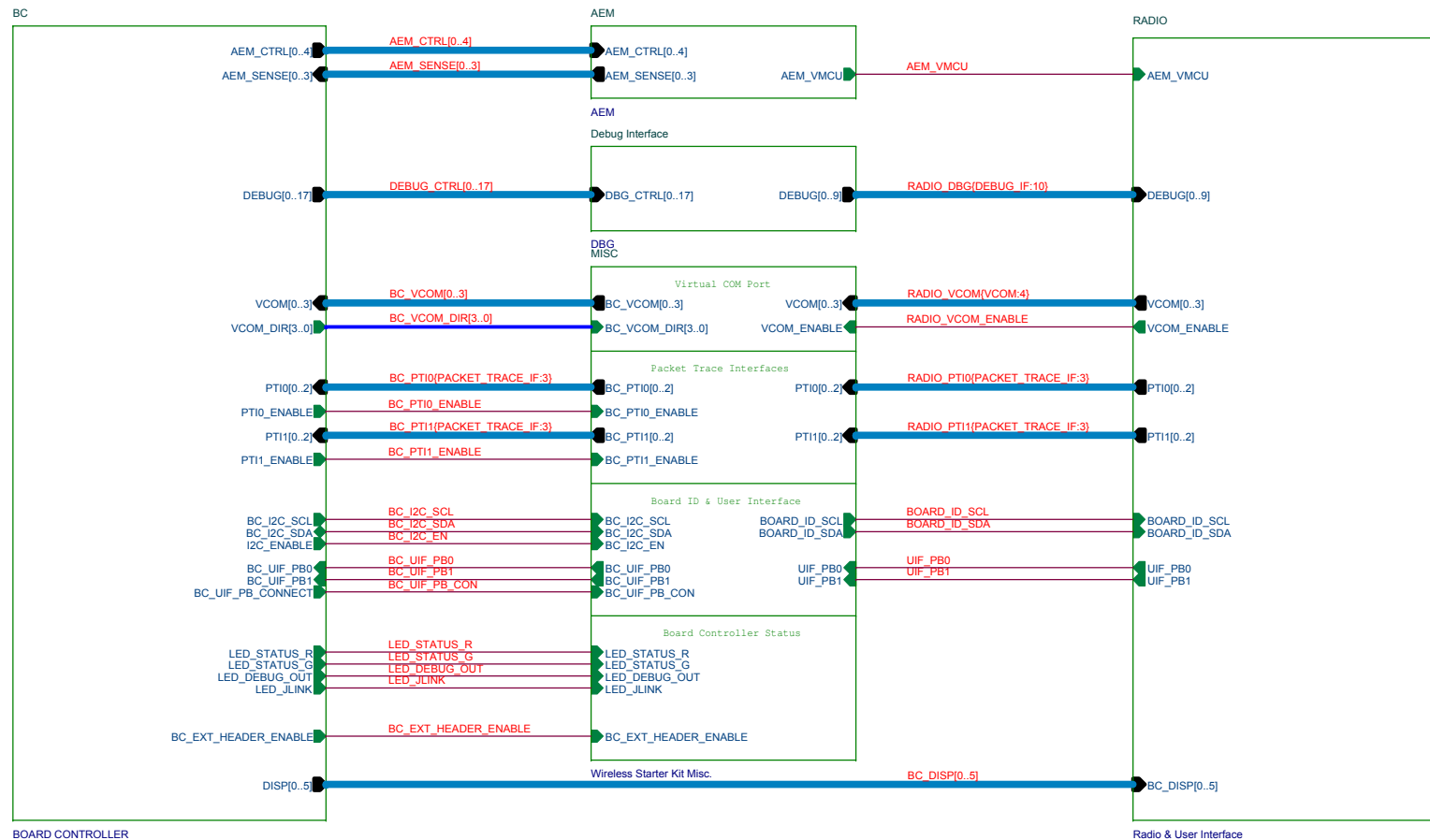


Wireless STK Mainboard	
Board Function	Page
Title Page	1
Top Level Hierarchy	2
User Interface	3
Radio Module Interface	4
Voltage Regulators	5
Isolation & Level Shifting	6
Debug Multiplexer	7
Advanced Energy Monitor	8


Revision History	
Rev.	Description
A00	Initial prodcuton version.
A01	Minor updates for pre-production version.

Wireless STK

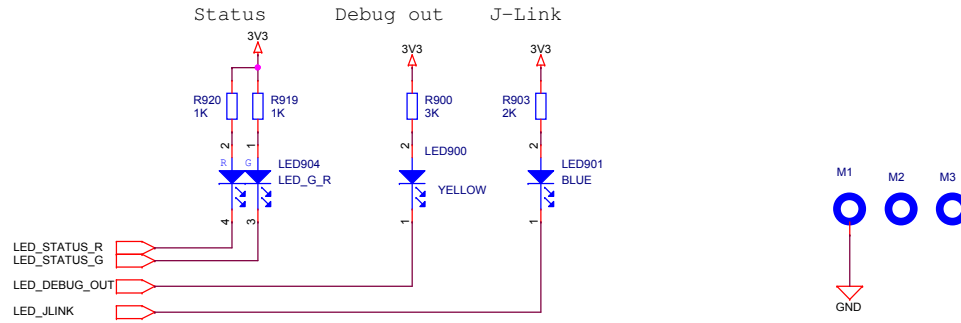
 SILICON LABS		Schematic Title	
		Wireless Starter Kit Mainboard	
Designed: DDB		Approved: JNO	
Size A3	BOM Doc No:	Document number BRD4001A	Revision A01
Design Created Date: Wednesday, December 03, 2008		Sheet Created Date Saturday, March 21, 2009	Sheet Modified Date Wednesday, December 12, 2018
		Sheet 1 of 8	



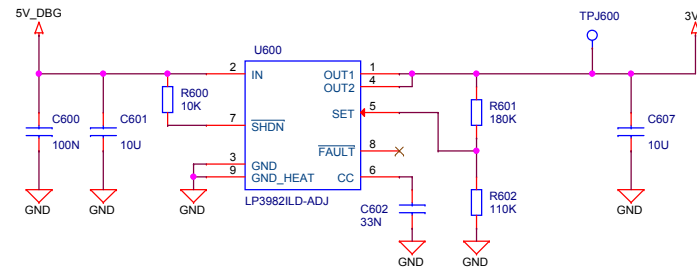
Wireless STK

 SILICON LABS		Schematic Title	
		Wireless Starter Kit Mainboard	
Designed: DDB		Page Title	
Size A3		Top Level Hierarchy	
BOM Doc No:		Document number	Revision
Design Created Date:		BRD4001A	A01
Wednesday, December 03, 2008		Sheet Created Date Wednesday, June 19, 2013	Sheet Modified Date Thursday, December 13, 2018
		Sheet 2 of 8	

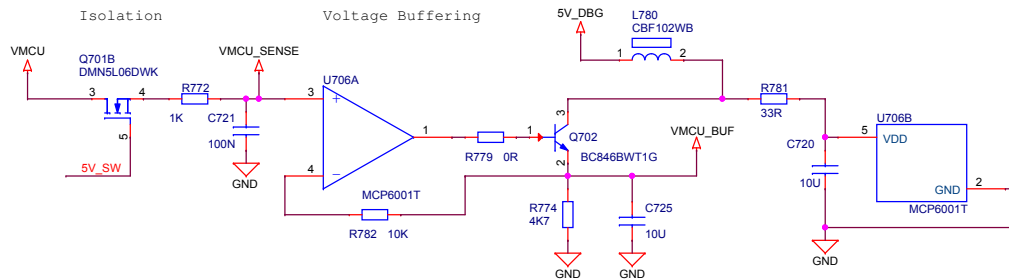
Indicators:



3V3 Regulator



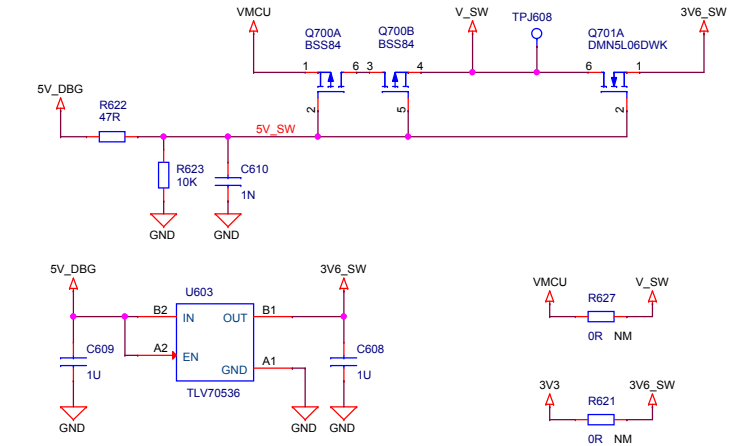
VMCU Voltage Mirror



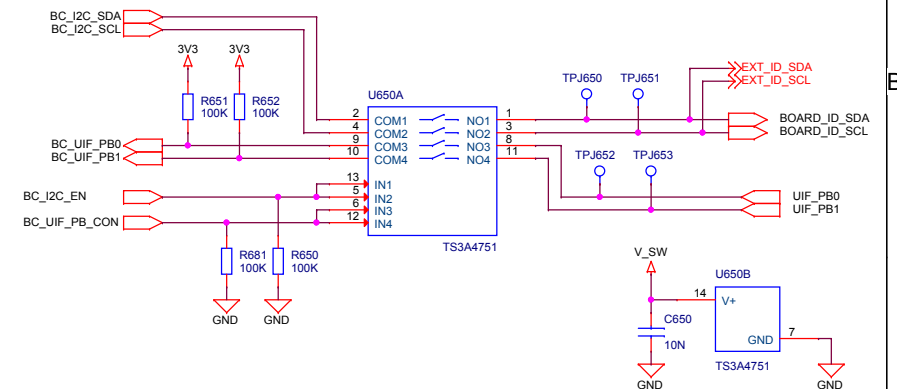
Power Supply for Analog Switches

Analog switches used for isolation are powered by 3V6_SW when the USB cable is connected, otherwise by VMCU.

J-Link USB Cable	PMOS State	NMOS State	V_SW	VMCU_SENSE
Connected	Off	ON	3.6V VMCU	VMCU
Disconnected	ON	OFF	3.6V VMCU	Isolated

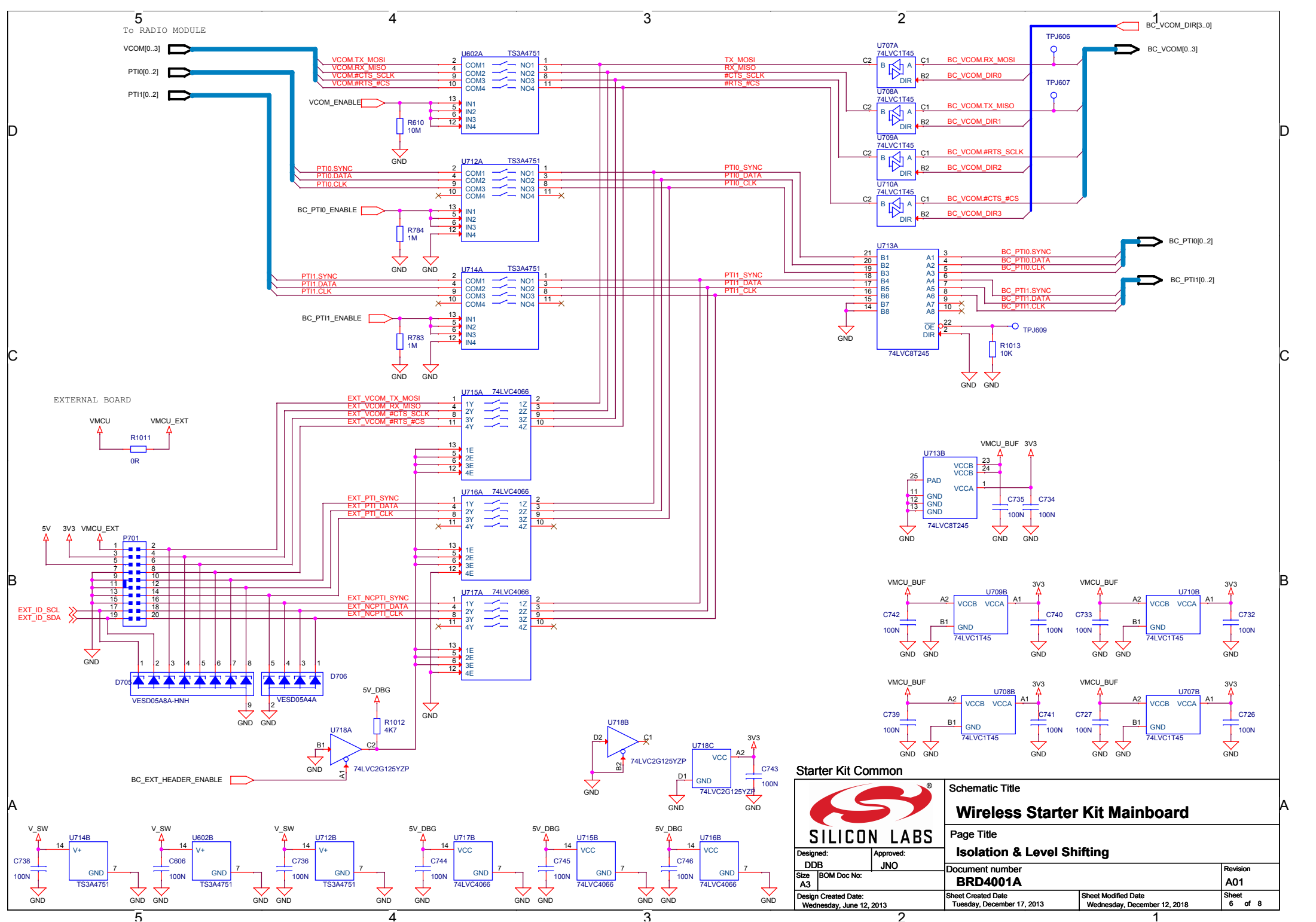


I2C & Push Button <-> Board Controller Isolation



Starter Kit Common

		Schematic Title	
		Wireless Starter Kit Mainboard	
Designed: DDB		Page Title	
Size A3		Voltage Regulators	
BOM Doc No:		Document number	
Design Created Date: Wednesday, June 12, 2013		BRD4001A	
Approved: JNO		Revision	
Sheet Created Date: Tuesday, June 18, 2013		A01	
Sheet Modified Date: Wednesday, December 12, 2018		Sheet 5 of 8	





SILICON LABS

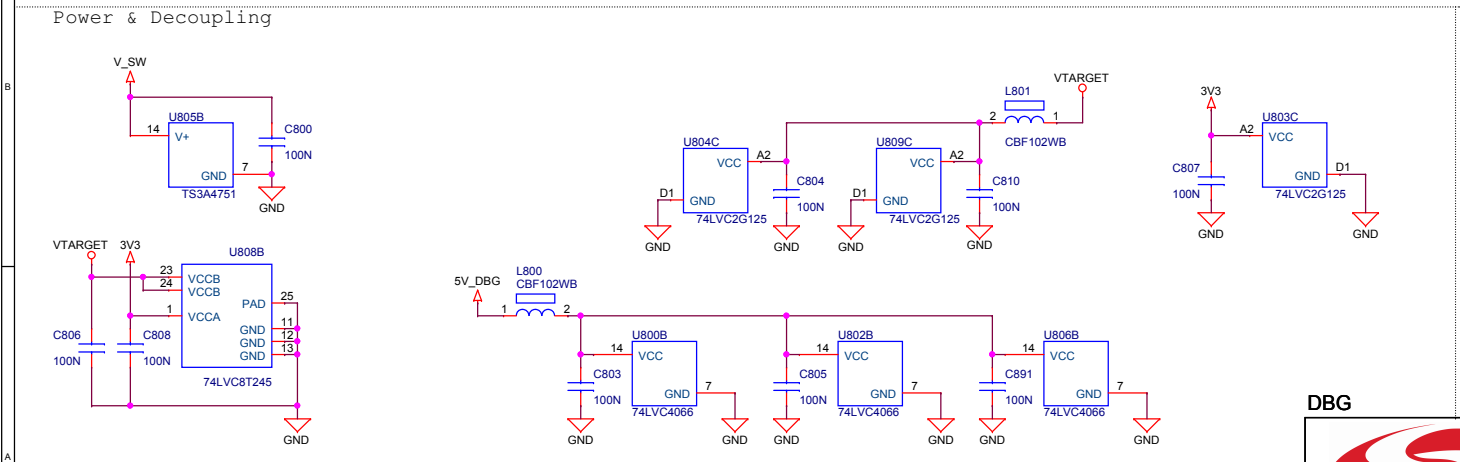
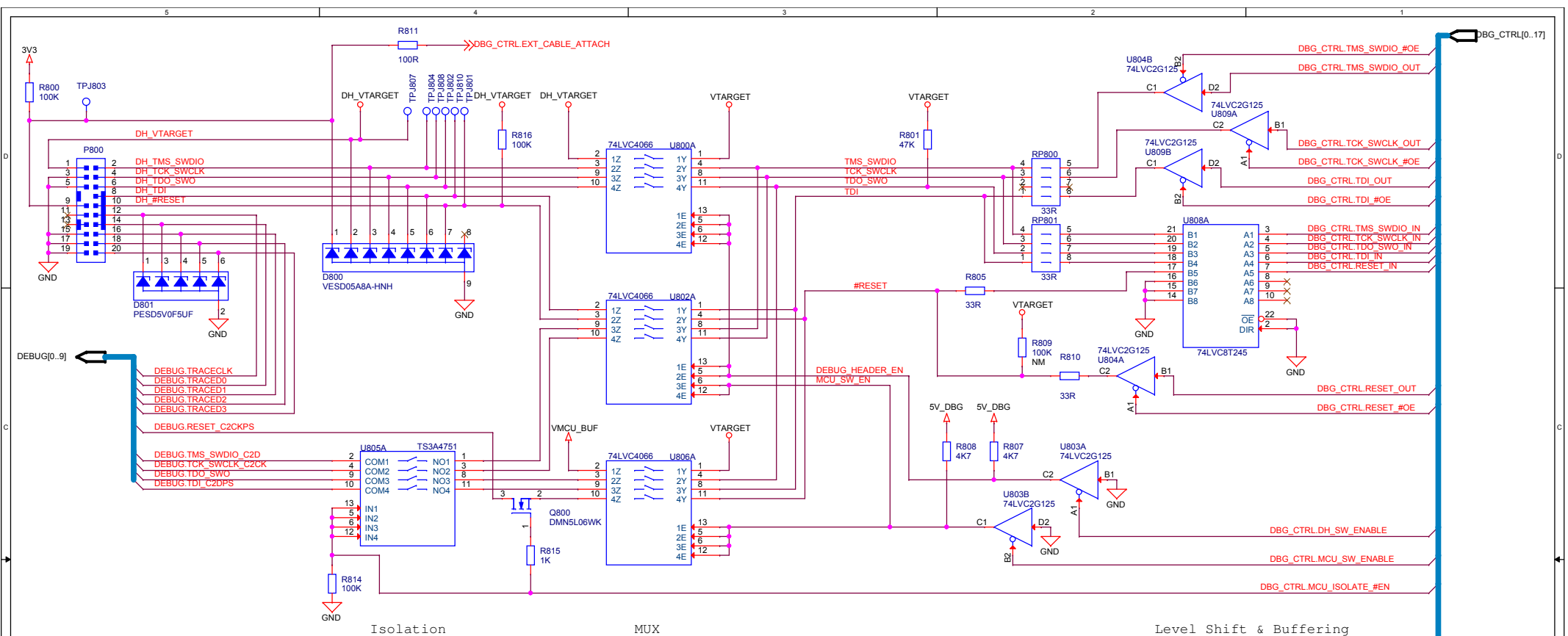
Schematic Title

Wireless Starter Kit Mainboard


Page Title

Isolation & Level Shifting

Designed: DDB	Approved: JNO
Size A3	BOM Doc No:
Design Created Date: Wednesday, June 12, 2013	
Document number BRD4001A	
Sheet Created Date Tuesday, December 17, 2013	Sheet Modified Date Wednesday, December 12, 2018
Revision A01	
Sheet 6 of 8	



Mode	DEBUG_MCU_SW_ENABLE	DEBUG_DH_SW_ENABLE	DEBUG_BUF_#OE	ISOLATE_#EN	DH_VTARGET	VTARGET
Debug Out	0	1	0	0	External voltage	External voltage
MCU Debug	1	0	0	1	Disconnected	VMCU
Debug In	1	1	1	1	VMCU	VMCU
Debug Off	0	0	1	0	-	-



SILICON LABS

Schematic Title

Wireless Starter Kit Mainboard

Page Title

Debug Multiplexer

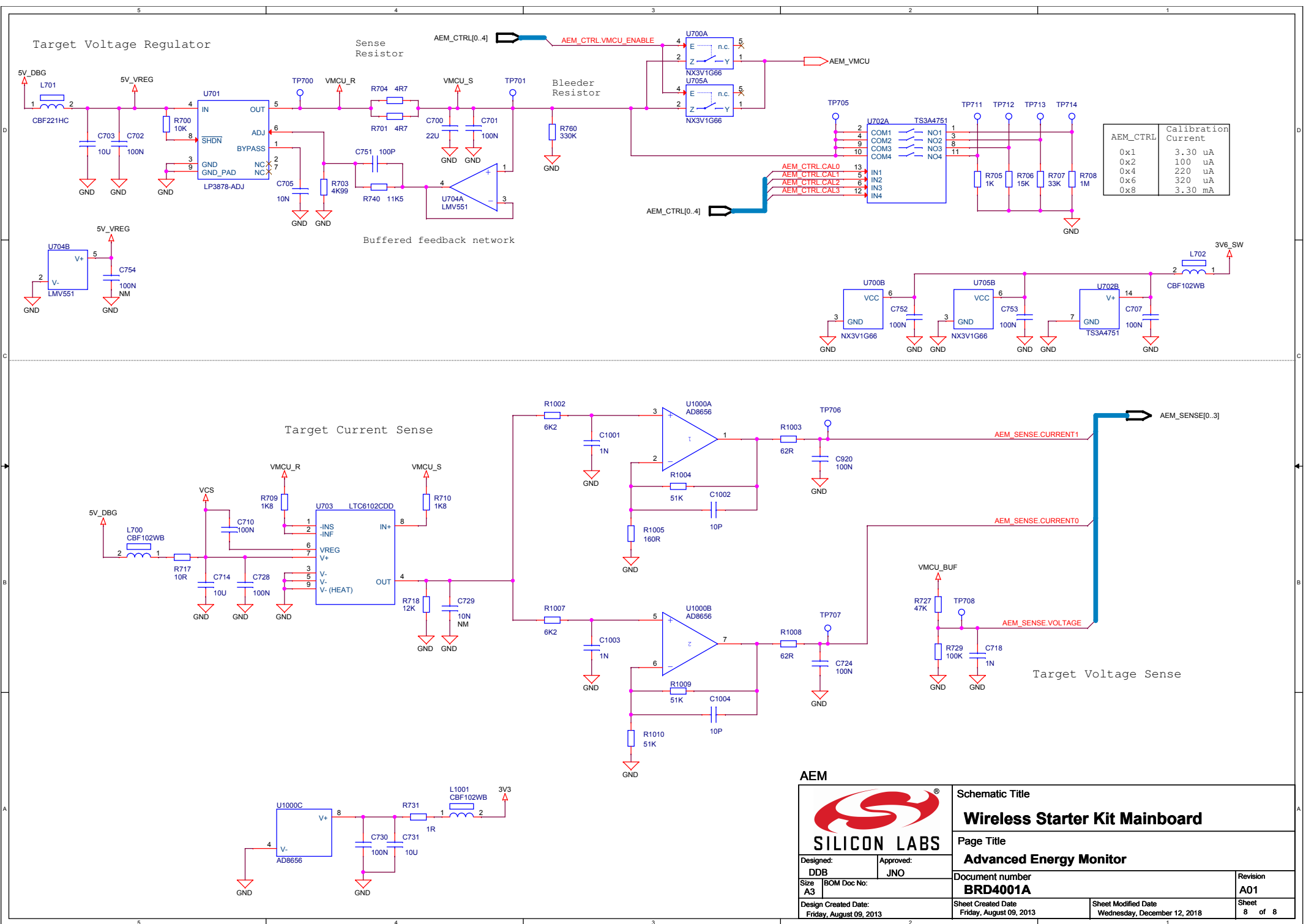
Designed: DDB Approved: JNO


Size A3 BOM Doc No: Document number **BRD4001A**

Design Created Date: Monday, August 12, 2013

Sheet Created Date: Monday, August 12, 2013 Sheet Modified Date: Wednesday, December 12, 2018 Sheet 7 of 8

Revision **A01**





SILICON LABS

Schematic Title

Wireless Starter Kit Mainboard

Page Title

Advanced Energy Monitor

Document number		Revision
BRD4001A		A01

Design Created Date:	Sheet Created Date	Sheet Modified Date	Sheet
Friday, August 09, 2013	Friday, August 09, 2013	Wednesday, December 12, 2018	8 of 8