



EFR32 Blue Gecko Radio Board


2.4 GHz 10 dBm, VDCDC to PAVDD

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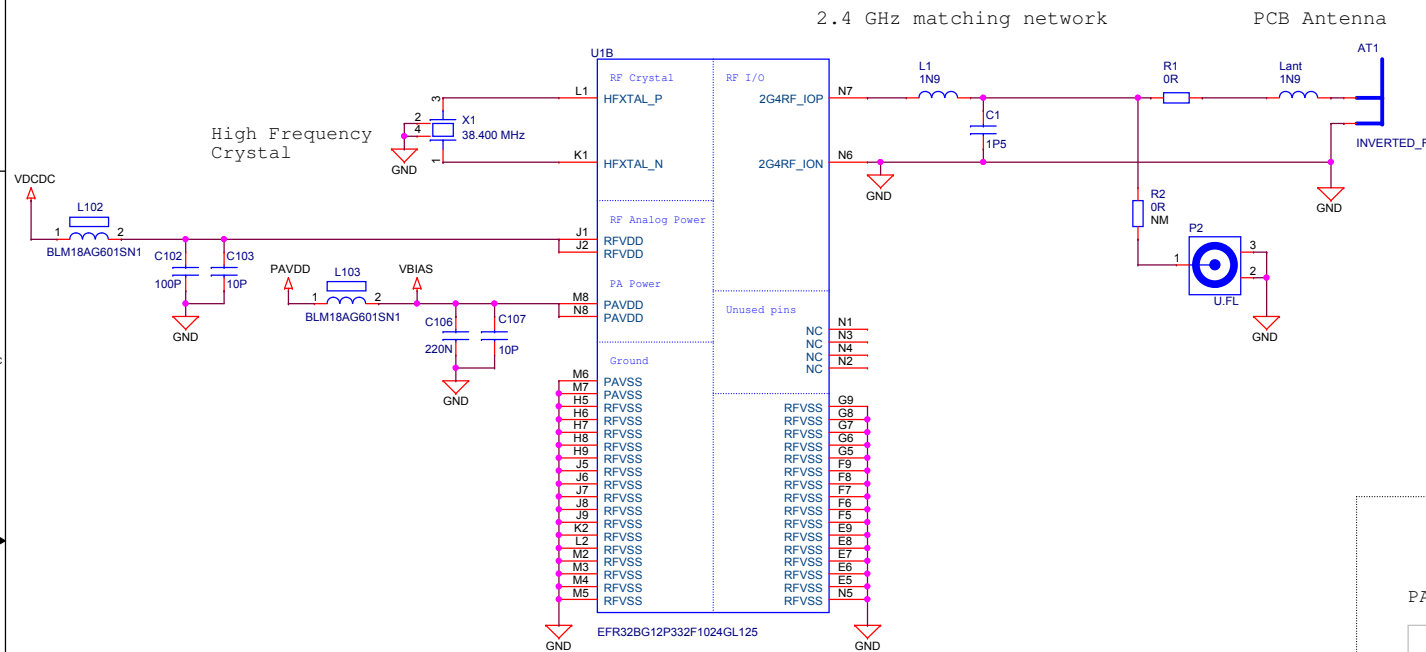
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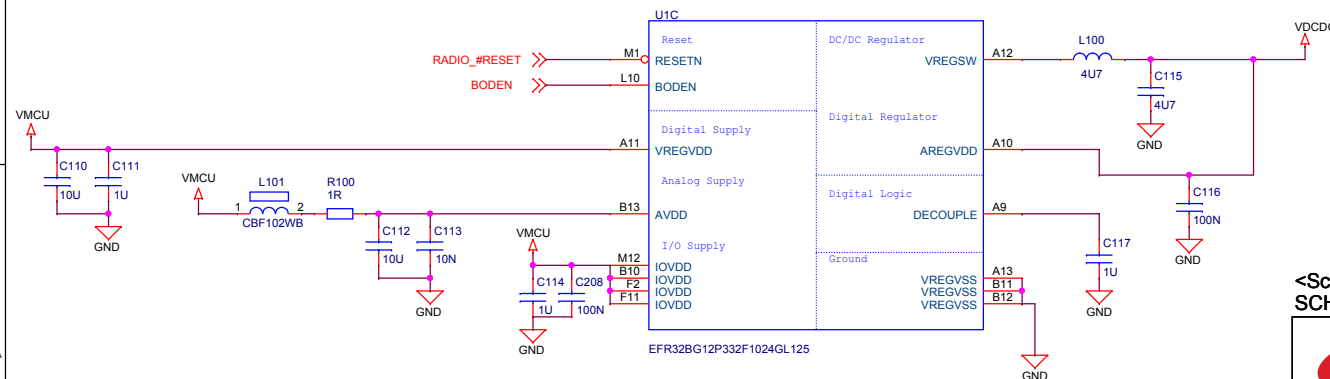
<Schematic Path> SCHEMATIC1

 SILICON LABS		Schematic Title EFR32BG12 2.4 GHz Radio Board	
Designed: TAB		Approved: <OrgAddr2>	
Size A3	BOM Doc No: <Cage Code>	Page Title Title Page	
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Antenna & Radio Interface

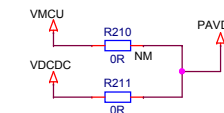


Power & Decoupling

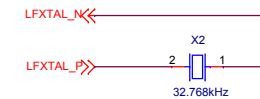


PAVDD Configuration

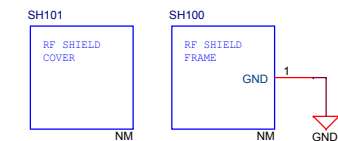
	Power Config 1 VMCU to PAVDD	Power Config DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



<Schematic Path>
SCHEMATIC1



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------------------	-------------------------

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	Schematic Title
1	1.1
2	2.1
3	3.1
4	4.1
5	5.1
6	6.1
7	7.1
8	8.1
9	9.1
10	10.1
11	11.1
12	12.1
13	13.1
14	14.1
15	15.1
16	16.1
17	17.1
18	18.1
19	19.1
20	20.1
21	21.1
22	22.1
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30	30.1
31	31.1
32	32.1
33	33.1
34	34.1
35	35.1
36	36.1
37	37.1
38	38.1
39	39.1
40	40.1
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43	43.1
44	44.1
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46	46.1
47	47.1
48	48.1
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52	52.1
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81	81.1
82	82.1
83	83.1
84	84.1
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87	87.1
88	88.1
89	89.1
90	90.1
91	91.1
92	92.1
93	93.1
94	94.1
95	95.1
96	96.1
97	97.1
98	98.1
99	99.1
100	100.1

EFR32BG12 2.4 GHz Radio Board

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	RF, Antenna & Power
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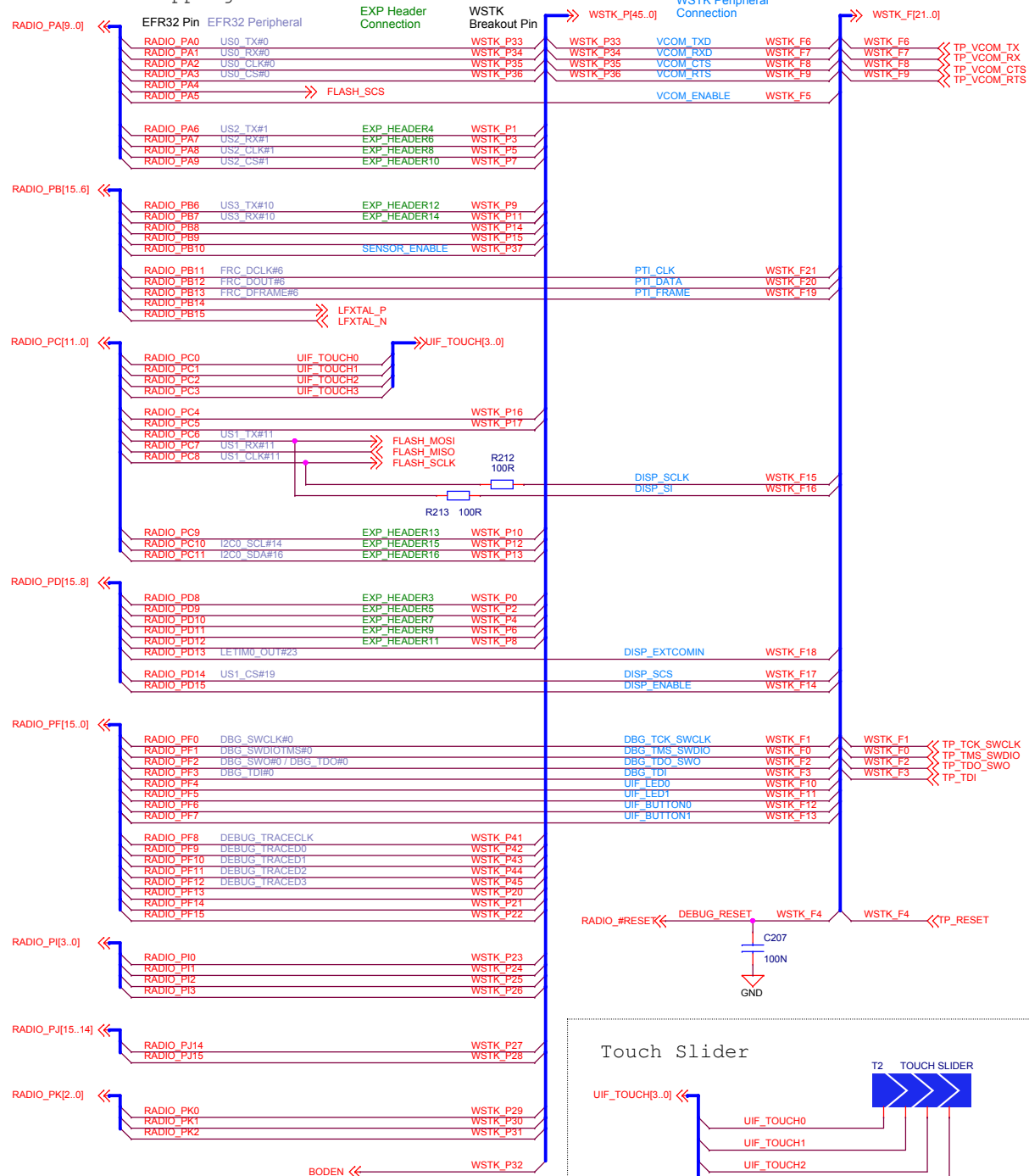
Revision
A00

Sheet Created Date
Monday, April 02, 2012

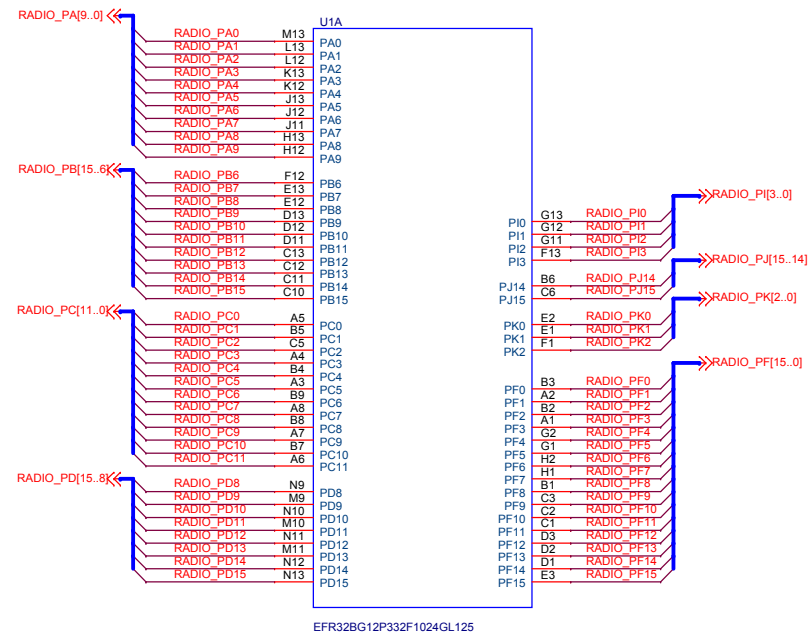
Sheet Modified Date	Wednesday, November 02, 2016
---------------------	------------------------------

Sheet
2 of 4

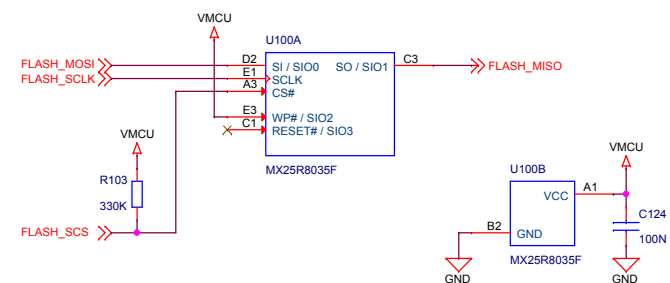
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



<Schematic Path>
SCHEMATIC1



Designed: TAB		Approved: <OrgAddr2>
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Schematic Title

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EFR32 I/O Port Connections

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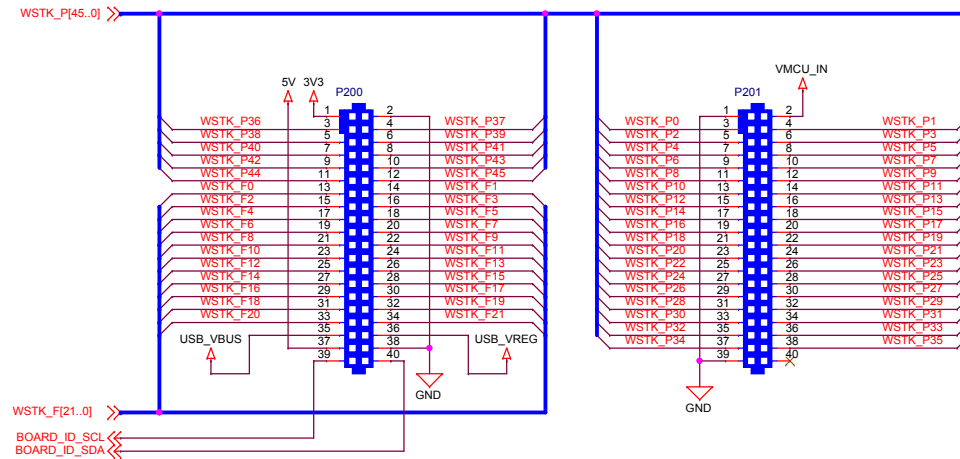
Sheet Created Date	Wednesday, April 11, 2012
--------------------	---------------------------

Sheet Modified Date	Wednesday, November 02, 2016
---------------------	------------------------------

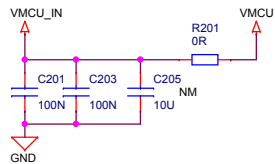
Revision
A00

Sheet
3 of 4

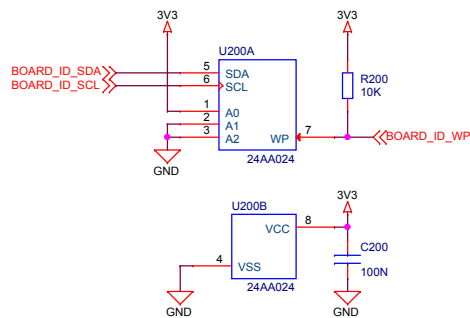
WSTK Connectors



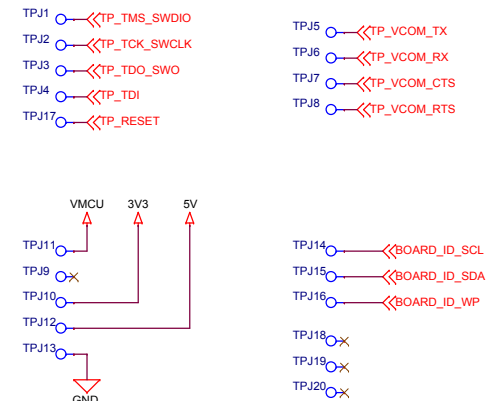
WSTK Power Decoupling




Board Identification



Test Points



<Schematic Path>
SCHEMATIC1

		Schematic Title	
		EFR32BG12 2.4 GHz Radio Board	
Designed:		Page Title	
TAB		WSTK Connectors & Board ID	
Size		Document number	
A3		BRD4103A	
Design Created Date:		Revision	
Monday, April 02, 2012		A00	
Sheet Created Date		Sheet Modified Date	
Tuesday, April 03, 2012		Wednesday, November 02, 2016	
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