



EFR32BG Blue Gecko Radio Board

2.4 GHz 10 dBm, DCDC to PAVDD

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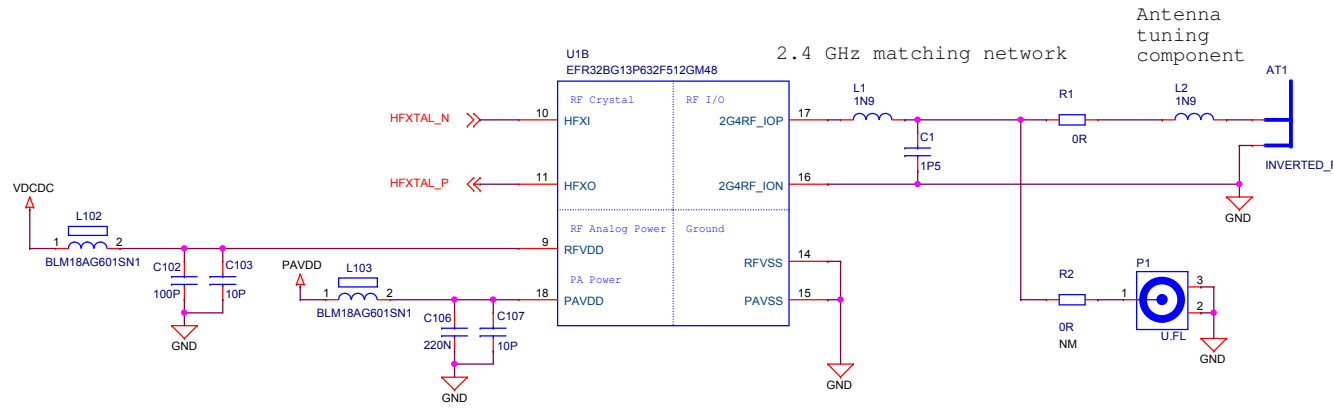


Revision History	
Rev.	Description
A00	Initial revision.

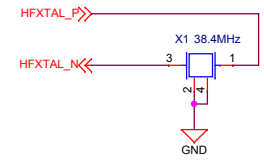
SCHEMATIC1

Schematic Title		Revision	
EFR32BG13 2.4 GHz 10 dBm Radio Board		A00	
Page Title		Document number	
Title Page		BRD4104A	
Designed: JSH	Approved: JNO	Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Wednesday, March 01, 2017
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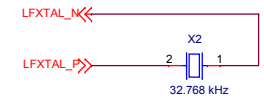
Antenna & Radio Interface



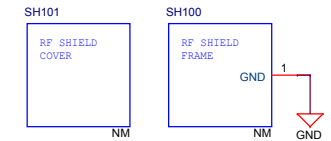
High Frequency Crystal



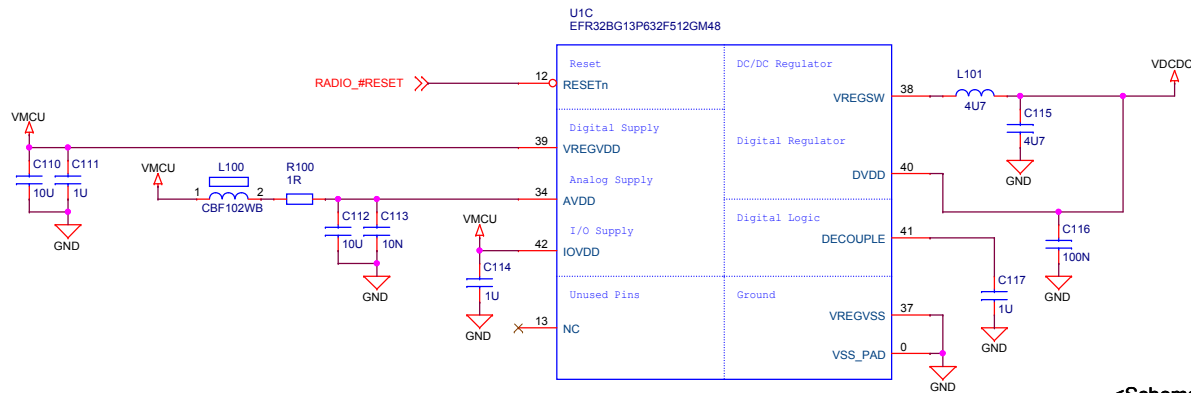
Low Frequency Crystal



RF Shielding

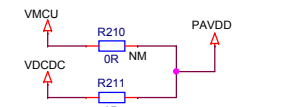


Power & Decoupling




PAVDD Configuration

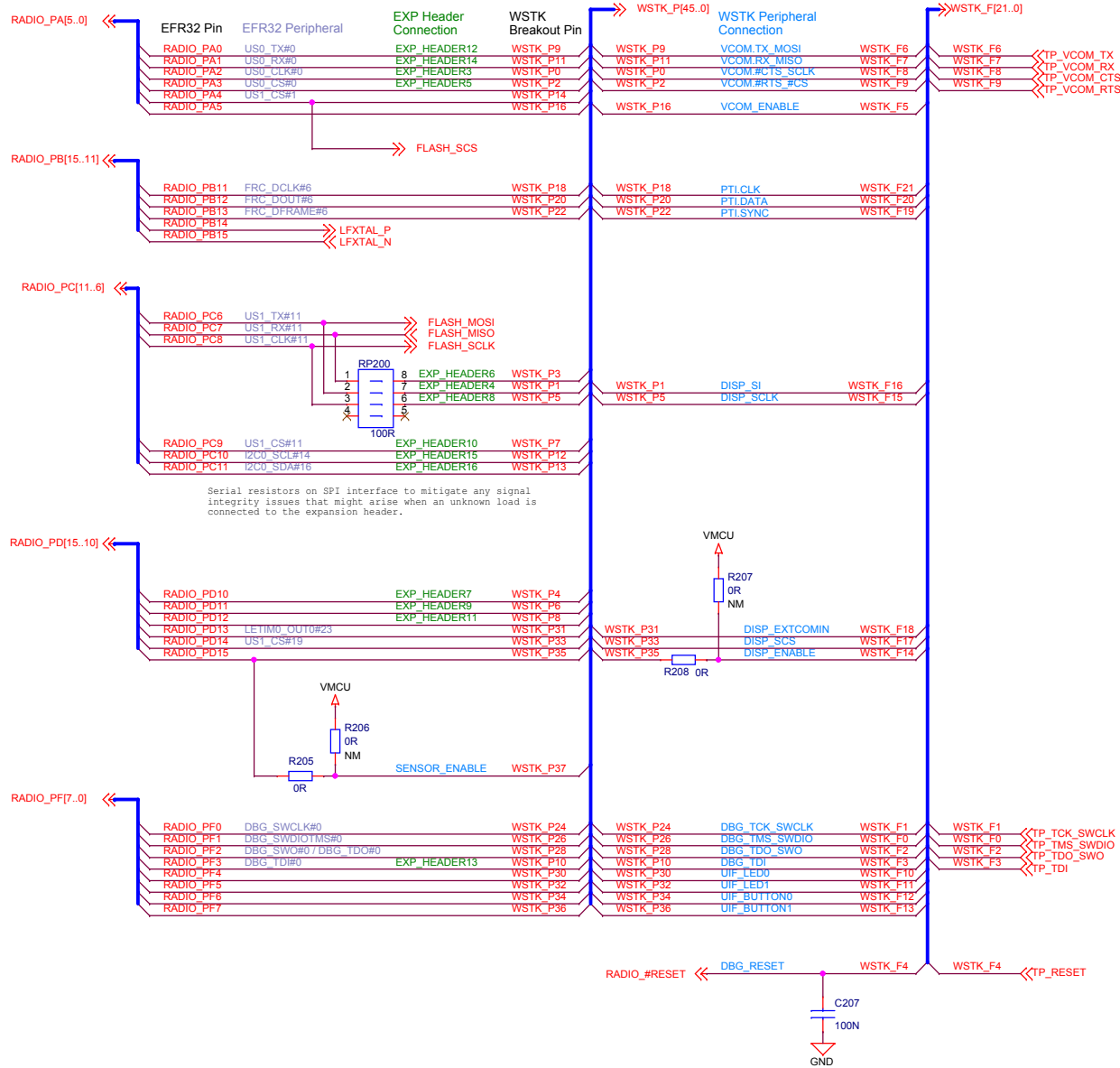
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



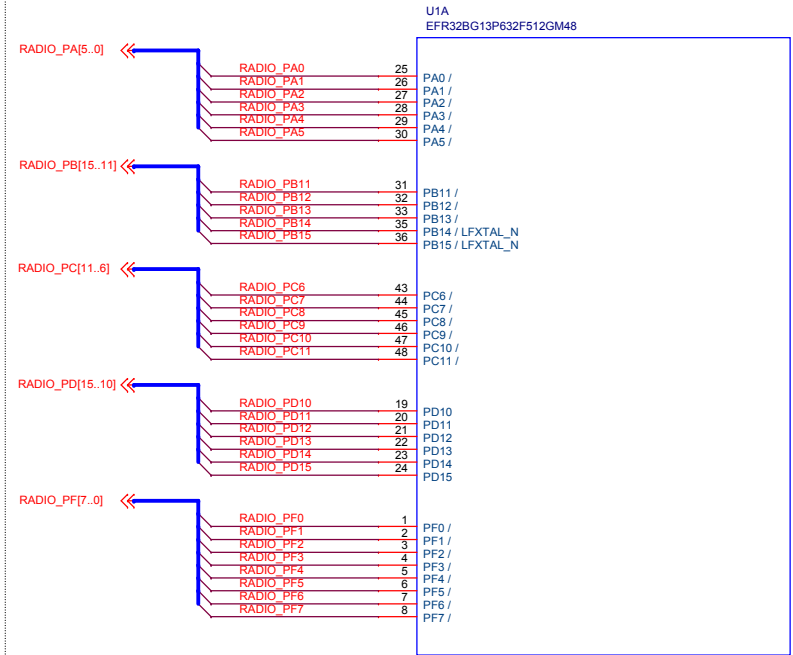
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		RF, Antenna and Power	
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Design Created Date: Monday, April 02, 2012	Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Wednesday, March 01, 2017	Sheet 2 of 4

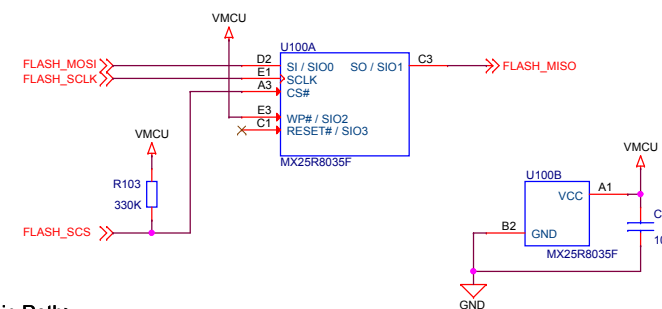
EFR32 Pin Mapping



EFR32 I/O Port Pins



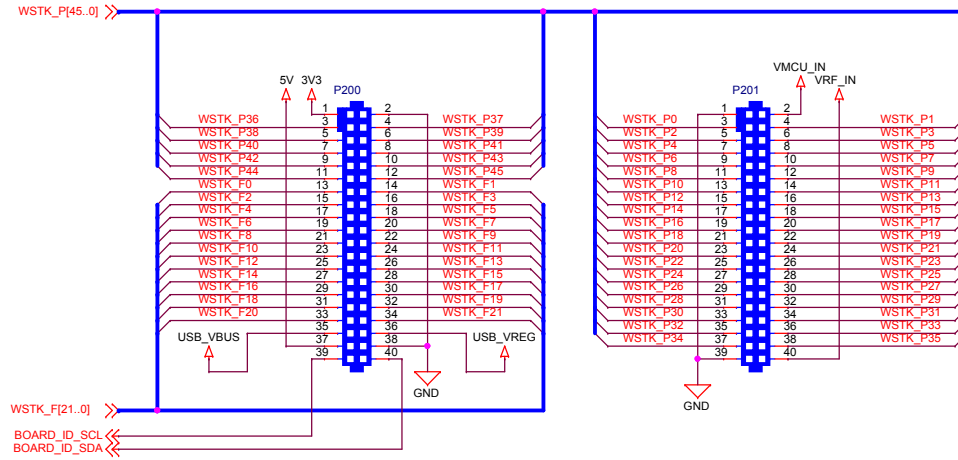
Serial Flash



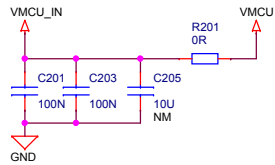
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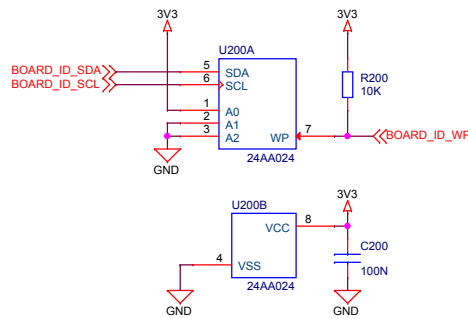
WSTK Connectors



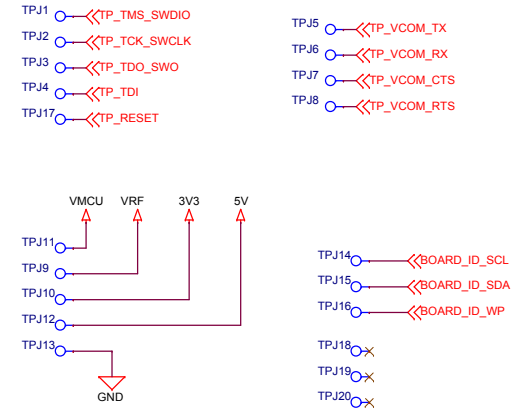
WSTK Power Decoupling



Board Identification



Test Points



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Size: A3	BOM Doc No: <Cage Code>	Document number	Revision
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