EFR32BG Blue Gecko Radio Board

2.4 GHz 10 dBm, VDCDC to PAVDD

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Revision History

Rev.	Description
A00	Initial revision.
A01	Updated EFR32 revision to Rev. C. Fixing 4.7uF cap Voltage rating.
A02	Updated EFR32 revision to Rev. D.

PCB1

PCB4104A

SCHEMATIC1

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SILICON LABS

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Schematic Title: **EFR32BG13 2.4 GHz 10 dBm Radio Board**

Page Title: **Title Page**

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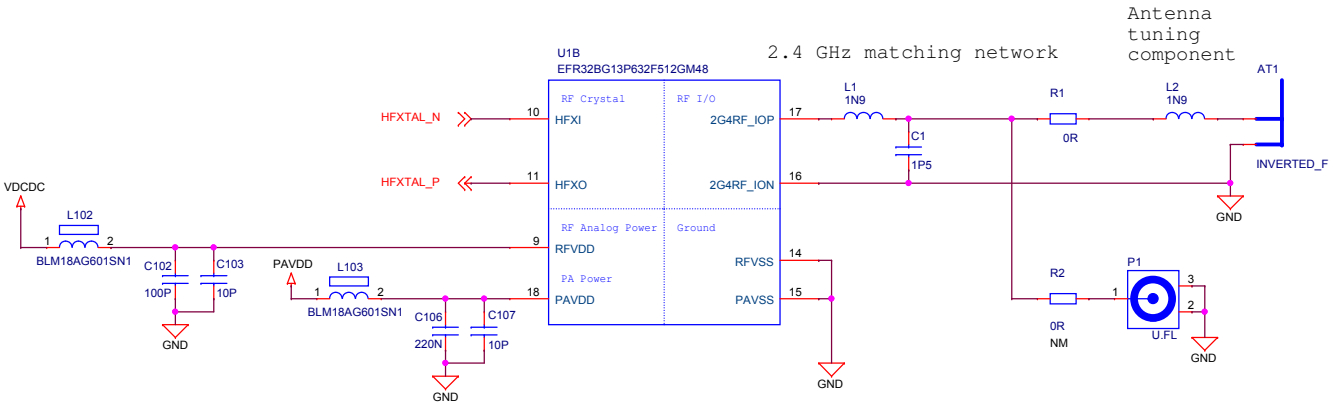
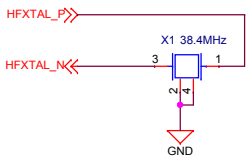
Sheet Created Date: Monday, April 02, 2012	Sheet Modified Date: Wednesday, November 07, 2018	Sheet: 1 of 4

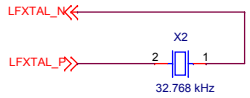
Antenna & Radio Interface



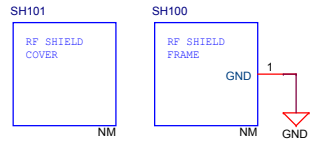
High Frequency Crystal



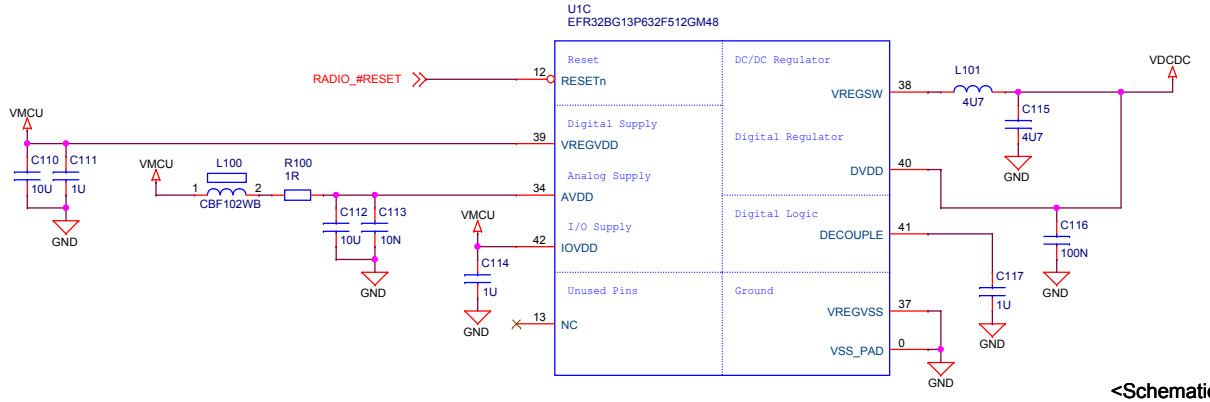
Low Frequency Crystal



RF Shielding

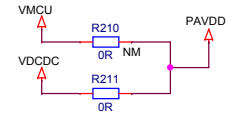


Power & Decoupling



PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



<Schematic Path>
SCHEMATIC1

SILICON LABS

Designed: JSH	Approved: JNO
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EFR32BG13 2.4 GHz 10 dBm Radio Board

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RF, Antenna and Power

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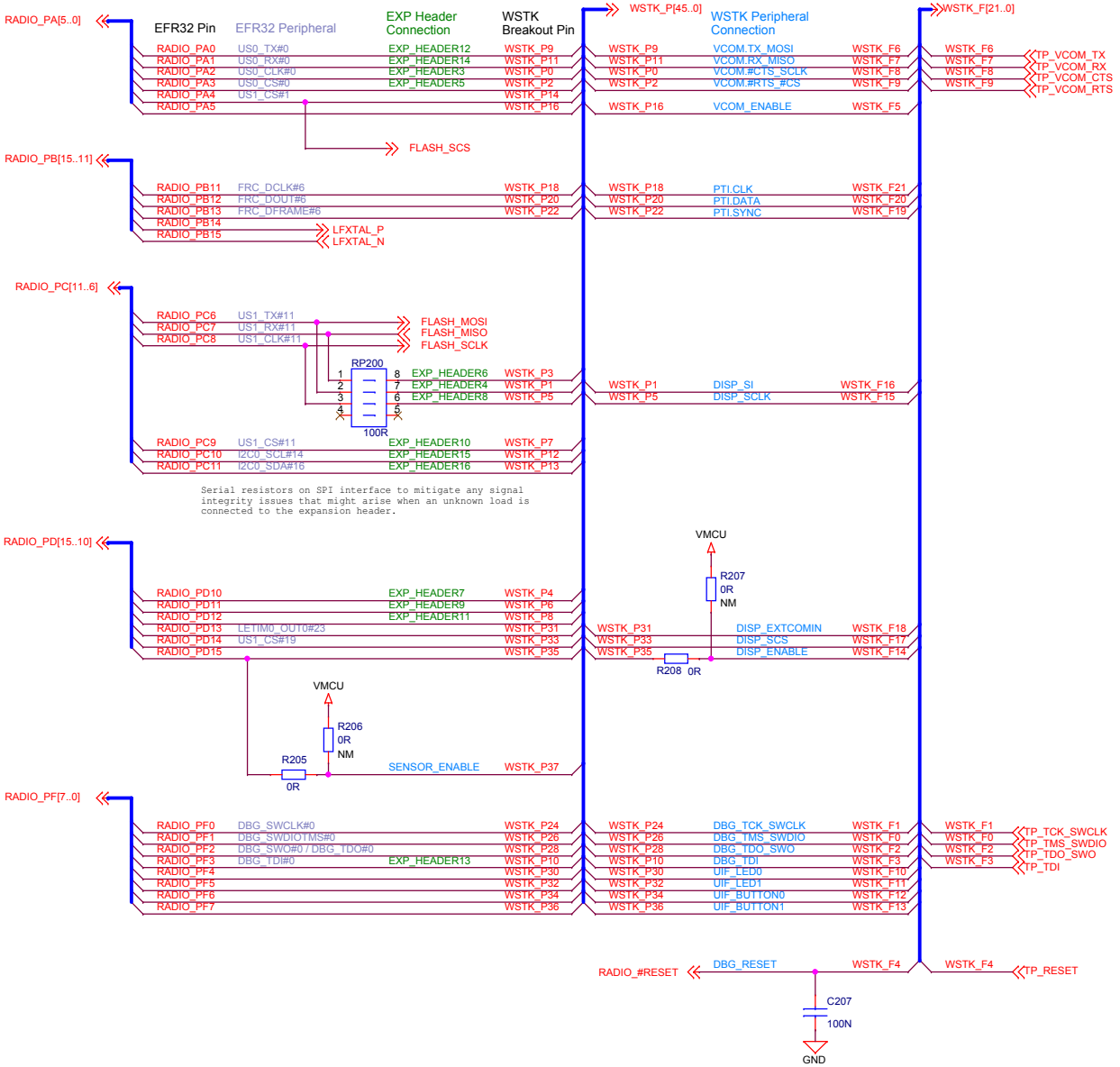
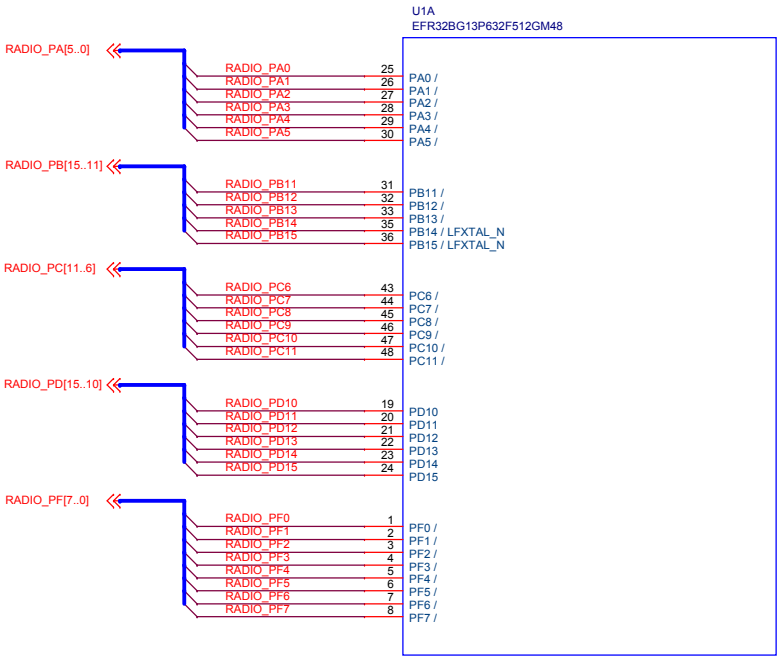
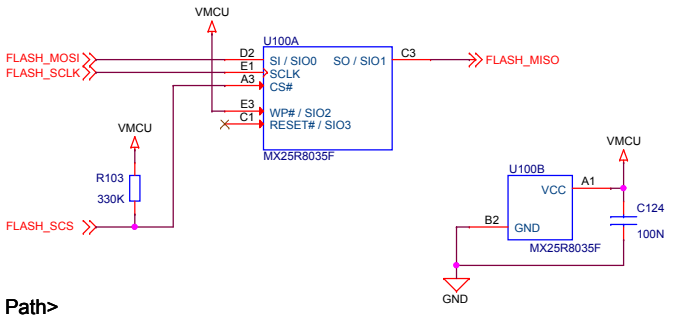
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



<Schematic Path>
SCHEMATIC1

SILICON LABS

Designed: JSH	Approved: JNO
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Schematic Title: **EFR32BG13 2.4 GHz 10 dBm Radio Board**

Page Title: **EFR32 I/O Port Connections**

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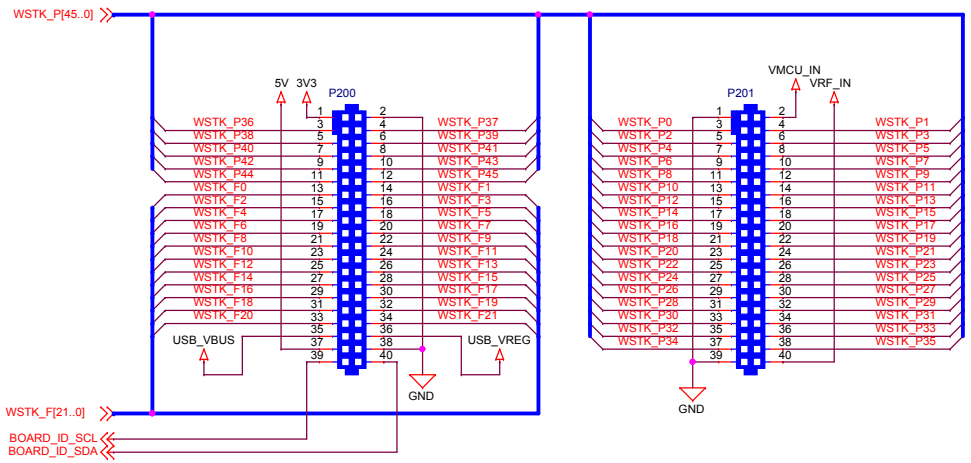
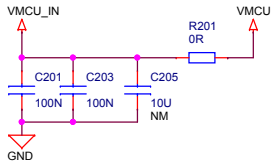
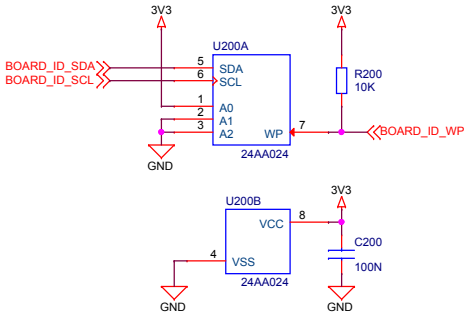
WSTK Connectors



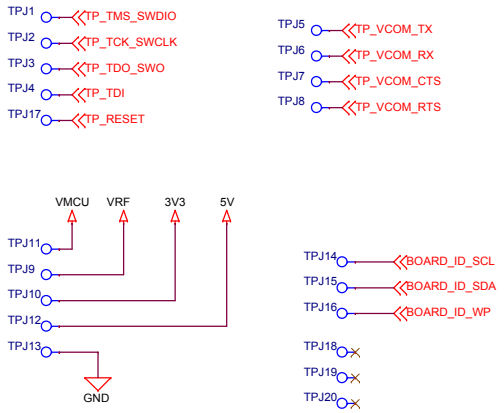
WSTK Power Decoupling



Board Identification



Test Points



<Schematic Path>
SCHEMATIC1

SILICON LABS

Designed: JSH	Approved: JNO
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Page Title: **WSTK Connectors & Board ID**

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