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BG22 Explorer Kit EFR32BG22C224F512IM40	
Board Function	Page
Title Page	1
EFR32BG22	2
User Interface	3
Board Power	4
On-Board Debugger	5

Revision History	
Rev.	Description
A00	Initial production release.
A01	Added U901, R916 changed to 10k, updated RF section and EEPROM.
A02	Matching update.
A03	Updated matching network for IC and antenna.

 <b>SILICON LABS</b>		Board Name <b>BG22 Explorer Kit</b>	
		Page Title <b>Title Page</b>	
Designed <b>MAH</b>	Approved <b>RGU</b>	Board Number <b>BRD4108A</b>	Revision <b>A03</b>
Size <b>A3</b>	Sheet Modified Date <b>Monday, August 09, 2021</b>	Sheet <b>1 of 5</b>	
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**EFR32BG22 Power Section**

SI\_DBG\_RESET, DBG\_RESET, BREAKOUT\_RIGHT1, SW301 TS3401, R1 100R, C1 100N, VMCU, GND, RESETn, VREGVDD, AVDD, IOVDD, DC/DC Regulator, Digital Regulator, Digital Logic, Ground, VREGVSS, VSS\_PAD, VREGSW, DVDD, DECOUPLE, L3 2U2, C7 4U7, C20 1U, VDCDC, C8 10U, C16 1U, GND.

**EFR32BG22 RF Section**

U1B EFR32BG22C224F512IM40, RF Crystal, HFXTAL\_I, HFXTAL\_O, RF I/O, RF2G4\_IO, RF Analog Power, RFVDD, RFVSS, PA Power, PAVDD, 2.4 GHz Matching Network, Antenna Matching, ANT1 2450AT18D0100, L1 2N7, L2 3N0, C1 18P, Z7 1P6, Z8 NM, Z9 16N, C3 0P1, C4 1P2, C5 NM, C11 100N, C12 120P, C18 100N, C19 120P, R4 0R, R5 0R, GND.

**EFR32BG22 Power Section**

The power section circuit includes a DC/DC Regulator (U1C) and various supply regulators. The DC/DC Regulator is connected to VDCDC and VREGSW. The Digital Regulator is connected to VREGVDD and DVDD. The Analog Regulator is connected to AVDD and DVDD. The I/O Supply is connected to IOVDD and DVDD. The circuit also includes a Reset pin (RESETn) and a VSS\_PAD pin. The power section is connected to the RF section via VDCDC, VREGVDD, DVDD, IOVDD, and VSS\_PAD.

**EFR32BG22 RF Section**

The RF section circuit includes a 38.4 MHz crystal (X1) and a 2.4 GHz matching network. The crystal is connected to the RF I/O pins (RF I/O). The matching network consists of inductors L1, L2, L3, L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, L16, L17, L18, L19, L20, L21, L22, L23, L24, L25, L26, L27, L28, L29, L30, L31, L32, L33, L34, L35, L36, L37, L38, L39, L40, L41, L42, L43, L44, L45, L46, L47, L48, L49, L50, L51, L52, L53, L54, L55, L56, L57, L58, L59, L60, L61, L62, L63, L64, L65, L66, L67, L68, L69, L70, L71, L72, L73, L74, L75, L76, L77, L78, L79, L80, L81, L82, L83, L84, L85, L86, L87, L88, L89, L90, L91, L92, L93, L94, L95, L96, L97, L98, L99, L100. The matching network is connected to the RF I/O pins (RF I/O). The antenna (ANT1) is connected to the matching network.

**EFR32BG22 IOs & Signal Assignments**

U1A  
EFR32BG22C224F512IM40

**PA00 - PA08**

- 21 EFR32\_PA00 → EFR32\_PA0[8..0]
- 22 EFR32\_PA01
- 23 EFR32\_PA02
- 24 EFR32\_PA03
- 25 EFR32\_PA04
- 26 EFR32\_PA05
- 27 EFR32\_PA06
- 28 EFR32\_PA07
- 29 EFR32\_PA08

**PB00 - PB04**

- 20 EFR32\_PB00 → EFR32\_PB0[4..0]
- 19 EFR32\_PB01
- 18 EFR32\_PB02
- 17 EFR32\_PB03
- 16 EFR32\_PB04

**PC00 - PC07**

- 1 EFR32\_PC00 → EFR32\_PC0[7..0]
- 2 EFR32\_PC01
- 3 EFR32\_PC02
- 4 EFR32\_PC03
- 5 EFR32\_PC04
- 6 EFR32\_PC05
- 7 EFR32\_PC06
- 8 EFR32\_PC07

**PD00 - PD03**

- 40 EFR32\_PD00
- 39 EFR32\_PD01
- 38 EFR32\_PD02
- 37 EFR32\_PD03

**Signal Assignments**

- EFR32\_PA0[8..0] → EFR32\_PA00, EFR32\_PA01, EFR32\_PA02, EFR32\_PA03, EFR32\_PA04, EFR32\_PA05, EFR32\_PA06, EFR32\_PA07, EFR32\_PA08
- EFR32\_PB0[4..0] → EFR32\_PB00, EFR32\_PB01, EFR32\_PB02, EFR32\_PB03, EFR32\_PB04
- EFR32\_PC0[7..0] → EFR32\_PC00, EFR32\_PC01, EFR32\_PC02, EFR32\_PC03, EFR32\_PC04, EFR32\_PC05, EFR32\_PC06, EFR32\_PC07
- EFR32\_PD0[3..2] → EFR32\_PD00, EFR32\_PD01, EFR32\_PD02, EFR32\_PD03
- EFR32\_PA00 → BREAKOUT\_RIGHT2
- EFR32\_PA01 → DBG\_SWCLK
- EFR32\_PA02 → DBG\_SWDIO
- EFR32\_PA03 → DBG\_SWO
- EFR32\_PA04 → SI\_DBG\_SWCLK
- EFR32\_PA05 → SI\_DBG\_SWDIO
- EFR32\_PA06 → SI\_DBG\_SWO
- EFR32\_PA07 → SI\_UART\_RX
- EFR32\_PA08 → SI\_UART\_TX
- EFR32\_PB00 → BREAKOUT\_RIGHT10
- EFR32\_PB01 → BREAKOUT\_LEFT7
- EFR32\_PB02 → MIKROE\_UART\_TX
- EFR32\_PB03 → MIKROE\_UART\_RX
- EFR32\_PB04 → MIKROE\_PWM
- EFR32\_PC00 → MIKROE\_I2C\_SCL
- EFR32\_PC01 → MIKROE\_I2C\_SDA
- EFR32\_PC02 → MIKROE\_I2C\_SCL
- EFR32\_PC03 → MIKROE\_I2C\_SDA
- EFR32\_PC04 → MIKROE\_I2C\_SCL
- EFR32\_PC05 → MIKROE\_I2C\_SDA
- EFR32\_PC06 → MIKROE\_I2C\_SCL
- EFR32\_PC07 → MIKROE\_I2C\_SDA
- EFR32\_PD00 → MIKROE\_I2C\_SCL
- EFR32\_PD01 → MIKROE\_I2C\_SDA
- EFR32\_PD02 → MIKROE\_I2C\_SCL
- EFR32\_PD03 → MIKROE\_I2C\_SDA

**Board Name**  
**BG22 Explorer Kit**

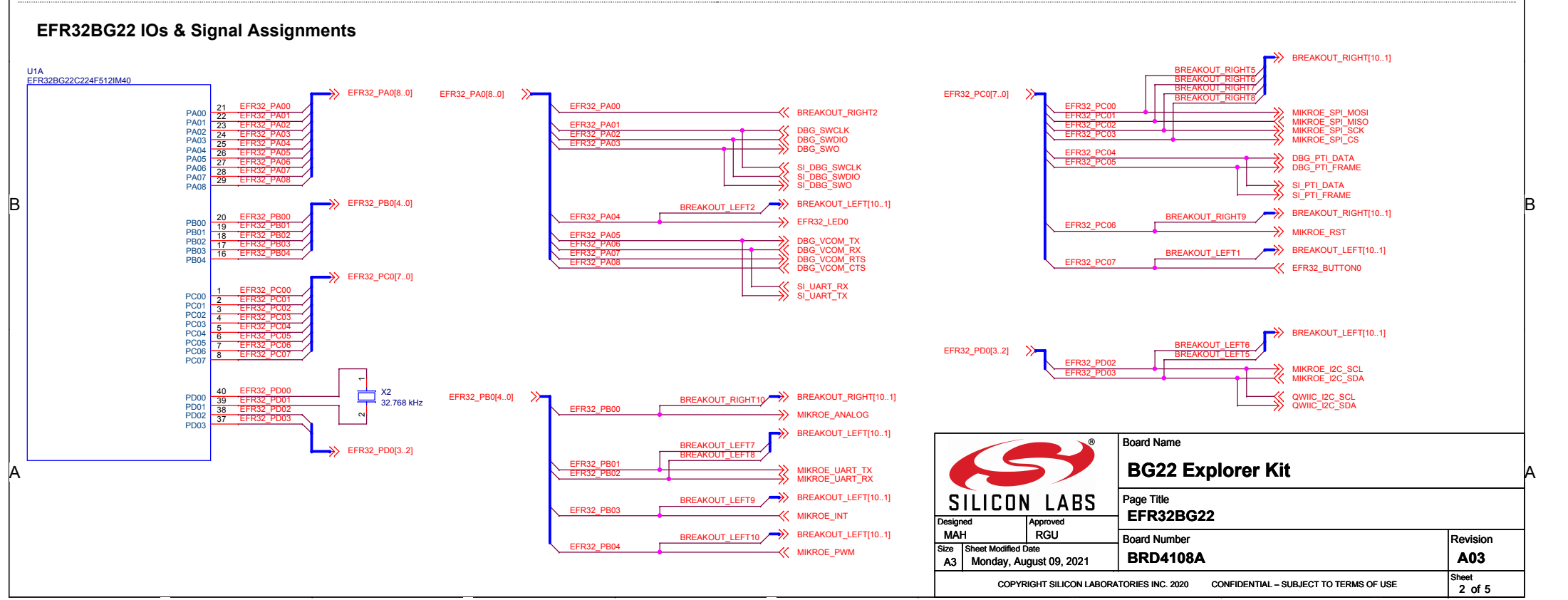
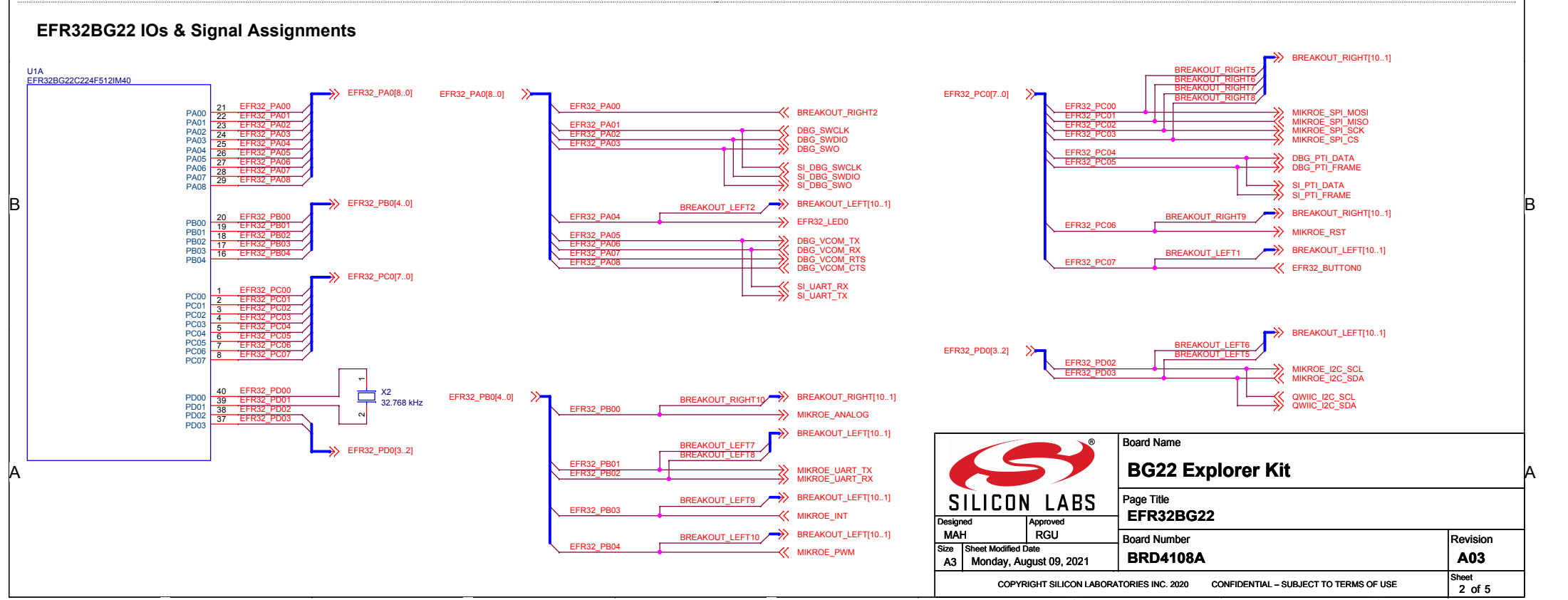
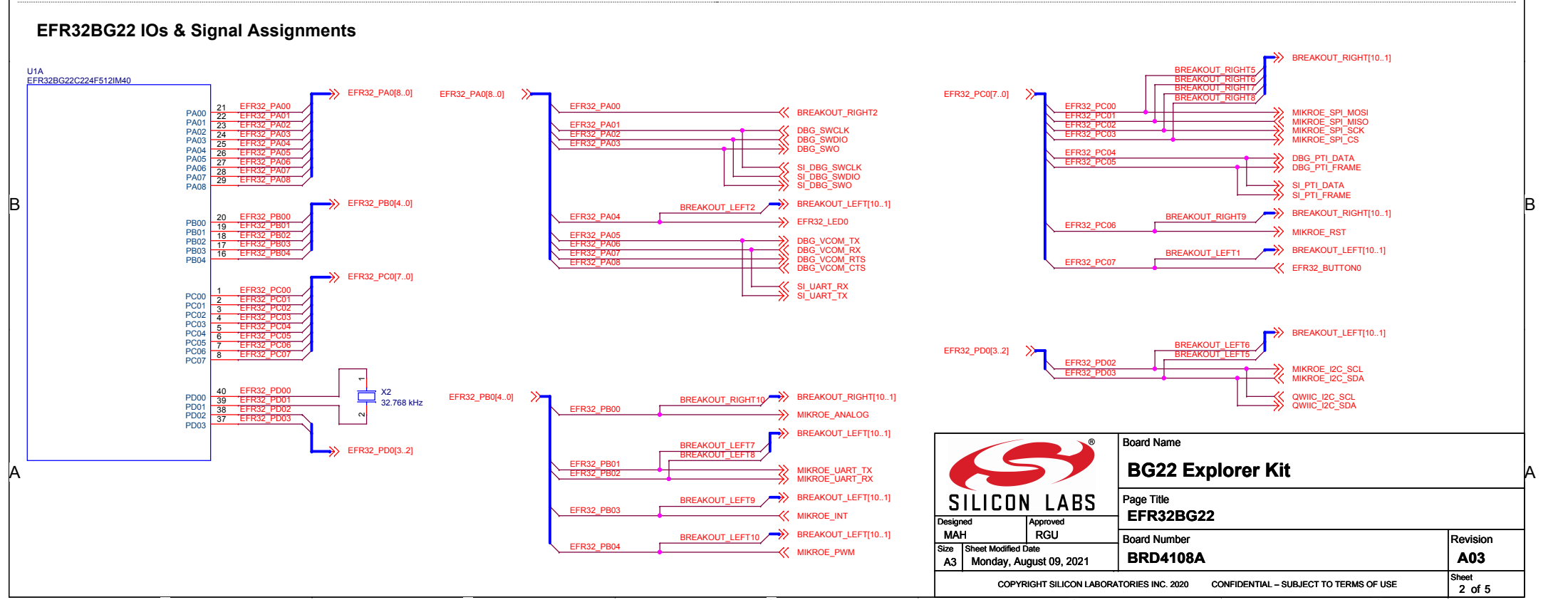
**Page Title**  
**EFR32BG22**

**Board Number**  
**BRD4108A**

**Revision**  
**A03**

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**EFR32BG22 IOs & Signal Assignments**

U1A  
EFR32BG22C224F512IM40

**PA Pins:**

- PA00: 21 EFR32\_PA00 → EFR32\_PA0[8..0]
- PA01: 22 EFR32\_PA01
- PA02: 23 EFR32\_PA02
- PA03: 24 EFR32\_PA03
- PA04: 25 EFR32\_PA04
- PA05: 26 EFR32\_PA05
- PA06: 27 EFR32\_PA06
- PA07: 28 EFR32\_PA07
- PA08: 29 EFR32\_PA08

**PB Pins:**

- PB00: 20 EFR32\_PB00 → EFR32\_PB0[4..0]
- PB01: 19 EFR32\_PB01
- PB02: 18 EFR32\_PB02
- PB03: 17 EFR32\_PB03
- PB04: 16 EFR32\_PB04

**PC Pins:**

- PC00: 1 EFR32\_PC00 → EFR32\_PC0[7..0]
- PC01: 2 EFR32\_PC01
- PC02: 3 EFR32\_PC02
- PC03: 4 EFR32\_PC03
- PC04: 5 EFR32\_PC04
- PC05: 6 EFR32\_PC05
- PC06: 7 EFR32\_PC06
- PC07: 8 EFR32\_PC07

**PD Pins:**

- PD00: 40 EFR32\_PD00
- PD01: 39 EFR32\_PD01
- PD02: 38 EFR32\_PD02
- PD03: 37 EFR32\_PD03


**Crystal:**

- X2: 32.768 kHz

**Signal Assignments:**

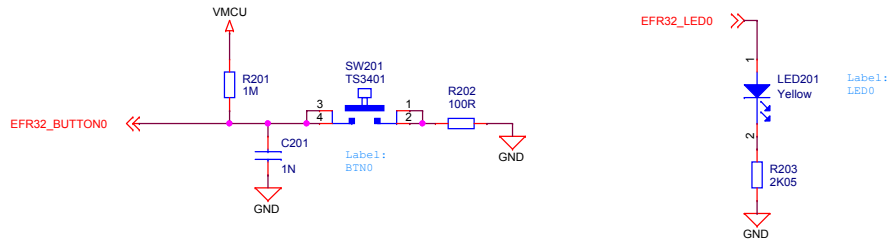
- EFR32\_PA00 → BREAKOUT\_RIGHT2
- EFR32\_PA01 → DBG\_SWCLK
- EFR32\_PA02 → DBG\_SWDIO
- EFR32\_PA03 → DBG\_SWO
- EFR32\_PA04 → BREAKOUT\_LEFT2 → BREAKOUT\_LEFT[10..1]
- EFR32\_PA05 → EFR32\_LED0
- EFR32\_PA06 → DBG\_VCOM\_TX
- EFR32\_PA07 → DBG\_VCOM\_RX
- EFR32\_PA08 → DBG\_VCOM\_RTS
- EFR32\_PA08 → DBG\_VCOM\_CTS
- EFR32\_PA08 → SI\_UART\_RX
- EFR32\_PA08 → SI\_UART\_TX
- EFR32\_PB00 → BREAKOUT\_RIGHT10 → BREAKOUT\_RIGHT[10..1]
- EFR32\_PB01 → BREAKOUT\_LEFT7 → BREAKOUT\_LEFT[10..1]
- EFR32\_PB02 → MIKROE\_UART\_TX
- EFR32\_PB02 → MIKROE\_UART\_RX
- EFR32\_PB03 → BREAKOUT\_LEFT9 → BREAKOUT\_LEFT[10..1]
- EFR32\_PB03 → MIKROE\_INT
- EFR32\_PB04 → BREAKOUT\_LEFT10 → BREAKOUT\_LEFT[10..1]
- EFR32\_PB04 → MIKROE\_PWM
- EFR32\_PC00 → EFR32\_PC00
- EFR32\_PC01 → EFR32\_PC01
- EFR32\_PC02 → EFR32\_PC02
- EFR32\_PC03 → EFR32\_PC03
- EFR32\_PC04 → EFR32\_PC04
- EFR32\_PC05 → EFR32\_PC05
- EFR32\_PC06 → EFR32\_PC06
- EFR32\_PC07 → EFR32\_PC07
- EFR32\_PC07 → BREAKOUT\_RIGHT9 → BREAKOUT\_RIGHT[10..1]
- EFR32\_PC07 → BREAKOUT\_LEFT1 → BREAKOUT\_LEFT[10..1]
- EFR32\_PC07 → EFR32\_BUTTON0
- EFR32\_PD02 → BREAKOUT\_LEFT6 → BREAKOUT\_LEFT[10..1]
- EFR32\_PD03 → BREAKOUT\_LEFT5 → BREAKOUT\_LEFT[10..1]
- EFR32\_PD03 → MIKROE\_I2C\_SCL
- EFR32\_PD03 → MIKROE\_I2C\_SDA
- EFR32\_PD03 → QWIIIC\_I2C\_SCL
- EFR32\_PD03 → QWIIIC\_I2C\_SDA

**Board Information:**

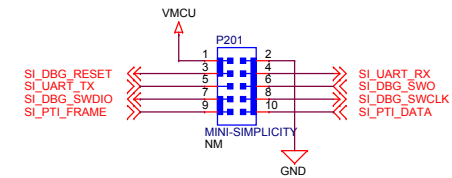
 <b>SILICON LABS</b>		Board Name <b>BG22 Explorer Kit</b>	
Designed MAH		Approved RGU	
Size A3		Sheet Modified Date Monday, August 09, 2021	
Page Title <b>EFR32BG22</b>		Board Number <b>BRD4108A</b>	
Revision <b>A03</b>		Sheet 2 of 5	

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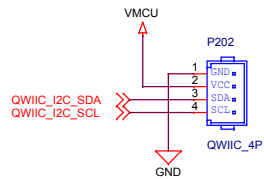
## Push Button & LED



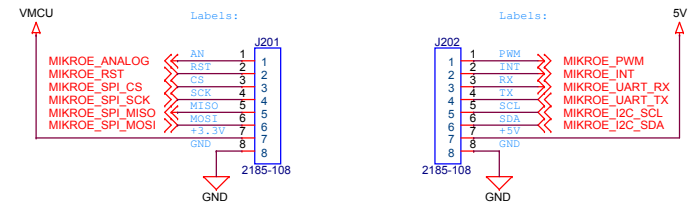
## Mini Simplicity Connector



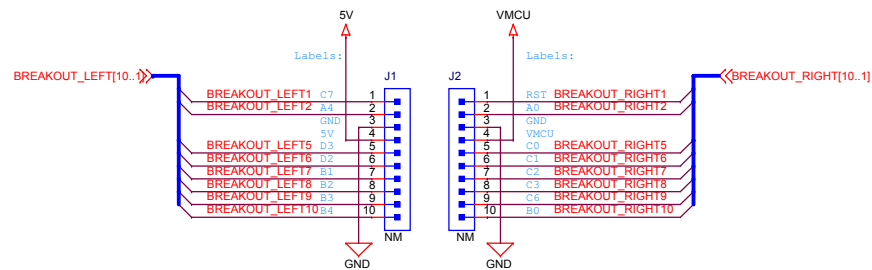
## Qwiic Connector



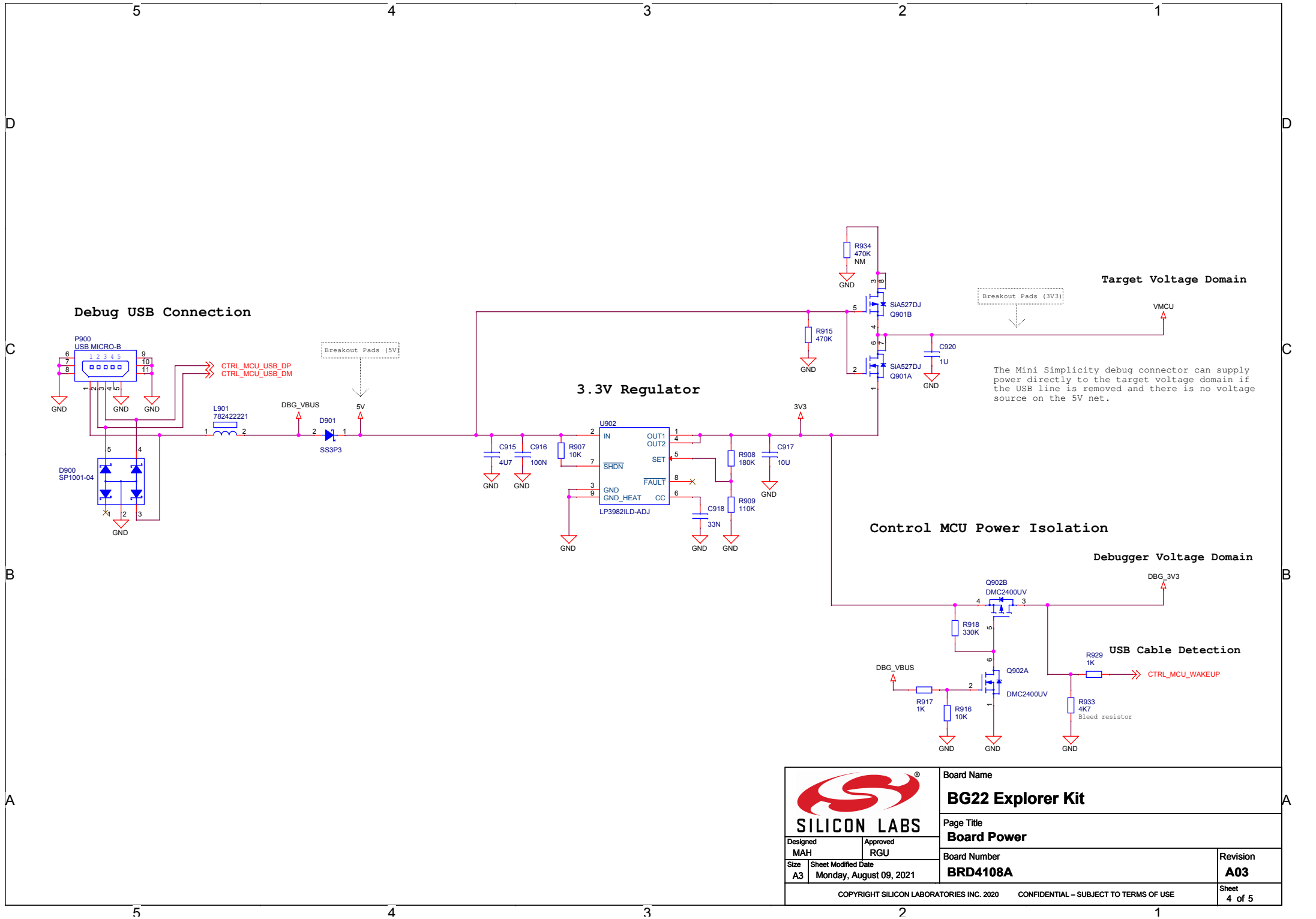
## MikroE Socket



## Breakout Pads



 <b>SILICON LABS</b>	Board Name	
	<b>BG22 Explorer Kit</b>	
Designed MAH		Page Title
Approved RGU		<b>User Interface</b>
Size A3	Sheet Modified Date Monday, August 09, 2021	Board Number <b>BRD4108A</b>
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 <b>SILICON LABS</b>		Board Name <b>BG22 Explorer Kit</b>	
		Page Title <b>Board Power</b>	
Designed MAH	Approved RGU	Board Number <b>BRD4108A</b>	Revision <b>A03</b>
Size A3	Sheet Modified Date Monday, August 09, 2021		
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**On-board Debugger**

The figure illustrates the connection of an on-board debugger to three different microcontroller units (MCUs): U900A, U900B, and U900C. The connections are as follows:

- U900A:**
  - DBG\_SWCLK: R919, 470R, CTRL\_MCU\_PA0, C2
  - DBG\_SWIO: R920, 470R, CTRL\_MCU\_PA1, C1
  - DBG\_RESET: D2, CTRL\_MCU\_PA2, D1
  - DBG\_SWO: E2, CTRL\_MCU\_PA3, E1
  - CTRL\_MCU\_WAKEUP: E2, CTRL\_MCU\_PA4, E1
  - CTRL\_MCU\_PA5: E1, CTRL\_MCU\_PA6, E1
  - CTRL\_MCU\_PA7: L3, CTRL\_MCU\_PA8, M3
  - CTRL\_MCU\_PA9: M4, CTRL\_MCU\_PA10, N4
  - CTRL\_MCU\_PA11: N5, CTRL\_MCU\_PA12, M5
  - CTRL\_MCU\_PA13: M6, CTRL\_MCU\_PA14, M6
  - CTRL\_MCU\_PA15: B1, CTRL\_MCU\_PA16, B1
  - CTRL\_MCU\_PA17: F1, CTRL\_MCU\_PA18, F2
  - CTRL\_MCU\_PA19: F3, CTRL\_MCU\_PA20, G1
  - CTRL\_MCU\_PA21: G2, CTRL\_MCU\_PA22, H1
  - CTRL\_MCU\_PA23: H2, CTRL\_MCU\_PA24, H1
  - CTRL\_MCU\_PA25: M1, CTRL\_MCU\_PA26, M1
  - CTRL\_MCU\_PA27: N1, CTRL\_MCU\_PA28, N1
  - CTRL\_MCU\_PA29: L8, CTRL\_MCU\_PA30, L8
  - CTRL\_MCU\_PA31: L9, CTRL\_MCU\_PA32, L9
  - CTRL\_MCU\_PA33: N6, CTRL\_MCU\_PA34, N6
  - CTRL\_MCU\_PA35: N7, CTRL\_MCU\_PA36, N7
  - CTRL\_MCU\_PA37: N9, CTRL\_MCU\_PA38, N9
  - CTRL\_MCU\_PA39: N10, CTRL\_MCU\_PA40, N10
  - CTRL\_MCU\_PA41: D3, CTRL\_MCU\_PA42, D3
- U900B:**
  - DBG\_VCOM\_RX: R921, 470R, CTRL\_MCU\_PC0, K1
  - DBG\_VCOM\_TX: R921, 470R, CTRL\_MCU\_PC1, K2
  - DBG\_PTL\_FRAME: D2, CTRL\_MCU\_PC2, L1
  - BOARD\_ID\_SDA: E2, CTRL\_MCU\_PC3, L2
  - BOARD\_ID\_SCL: E2, CTRL\_MCU\_PC4, M2
  - BOARD\_ID\_WP: E2, CTRL\_MCU\_PC5, J2
  - DBG\_VCOM\_RTS: D2, CTRL\_MCU\_PC6, H3
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC7, H3
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC8, F12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC9, E12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC10, E13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC11, D12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC12, D13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC13, C12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC14, C13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC15, C13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC16, L10
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC17, N12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC18, M12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC19, L12
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC20, N13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC21, M13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC22, L13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC23, K13
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC24, A6
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC25, B6
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC26, A5
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC27, B5
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC28, C4
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC29, J1
  - DBG\_VCOM\_CTS: R922, 470R, CTRL\_MCU\_PC30, D2
- U900C:**
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE0, G11
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE1, H12
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE2, G13
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE3, C11
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE4, D11
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE5, E11
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE6, F11
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE7, B4
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE8, A4
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE9, C3
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE10, B3
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE11, A3
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE12, B2
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE13, A2
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE14, A1
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE15, A1
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE16, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE17, B10
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  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE30, B10
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  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE50, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE51, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE52, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE53, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE54, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE55, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE56, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE57, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE58, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE59, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE60, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE61, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE62, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE63, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE64, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE65, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE66, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE67, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE68, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE69, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE70, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE71, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE72, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_MCU\_PE73, B10
  - CTRL\_MCU\_TEST\_MODE: R931, 1K, CTRL\_M

[illegible]

## Board ID

Board ID

DBG\_3V3

BOARD\_ID\_SDA

BOARD\_ID\_SCL

U903A

SDA

SCL

A0

A1

A2

WP

M24C02

DBG\_3V3

R812 10K

BOARD\_ID\_WP

U903B

VCC

VSS

M24C02

DBG\_3V3

C919 100N

GND

## Serial Flash

DBG\_3V3

DBG\_3V3

CTRL\_MCU\_SPI\_MOSI

CTRL\_MCU\_SPI\_SCLK

CTRL\_MCU\_SPI\_CS

CTRL\_MCU\_SPI\_MISO

U901A

MX25R8035F

D2

E1

A3

E3

C1

SO / SIO1

CS#

WP# / SIO2

RESET# / SIO3

C3

U901B

MX25R8035F

VCC

GND

A1

B2

DBG\_3V3

C914 100N

GND

R906 330K

### Test Points

TPJ1 CTRL\_MCU\_SWDIO

TPJ2 CTRL\_MCU\_SWCLK

TPJ3 CTRL\_MCU\_SWO

TPJ17 CTRL\_MCU\_RESET

TPJ5 CTRL\_MCU\_TXD

TPJ6 CTRL\_MCU\_RXD

TPJ14 BOARD\_ID\_SCL

TPJ15 BOARD\_ID\_SDA

TPJ16 BOARD\_ID\_WP

TPJ18 CTRL\_MCU\_USB\_DP

TPJ19 CTRL\_MCU\_USB\_DM

TPJ11 CTRL\_MCU\_TEST\_MODE

TPJ12 DBG\_VBUS


TPJ13 GND

### Mechanical

M900  
MECH RB FIXTURE

M901  
2.2 MM

M902  
2.2 MM

 <b>SILICON LABS</b>		Board Name	
		<b>BG22 Explorer Kit</b>	
Designed MAH		Page Title	
		<b>On-Board Debugger</b>	
Sheet Modified Date Monday, August 09, 2021		Board Number	Revision
		<b>BRD4108A</b>	<b>A03</b>
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### Test Points

TPJ1 CTRL\_MCU\_SWDIO

TPJ2 CTRL\_MCU\_SWCLK

TPJ3 CTRL\_MCU\_SWO

TPJ17 CTRL\_MCU\_RESET

TPJ5 CTRL\_MCU\_TXD

TPJ6 CTRL\_MCU\_RXD

TPJ14 BOARD\_ID\_SCL

TPJ15 BOARD\_ID\_SDA

TPJ16 BOARD\_ID\_WP

TPJ18 CTRL\_MCU\_USB\_DP

TPJ19 CTRL\_MCU\_USB\_DM

TPJ11 CTRL\_MCU\_TEST\_MODE

TPJ12 DBG\_VBUS


TPJ13 GND

### Mechanical


M900  
MECH RB FIXTURE

M901  
2.2 MM

M902  
2.2 MM

 <b>SILICON LABS</b>		Board Name	
		<b>BG22 Explorer Kit</b>	
Designed MAH		Page Title	
		<b>On-Board Debugger</b>	
Size A3		Sheet Modified Date Monday, August 09, 2021	Board Number <b>BRD4108A</b>
		Revision <b>A03</b>	Sheet 5 of 5

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 <b>SILICON LABS</b>		Board Name	
		<b>BG22 Explorer Kit</b>	
Designed <b>MAH</b>		Approved <b>RGU</b>	
Page Title		<b>On-Board Debugger</b>	
Size <b>A3</b>	Sheet Modified Date <b>Monday, August 09, 2021</b>	Board Number <b>BRD4108A</b>	Revision <b>A03</b>
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