



EFR32BG27 Radio Board

2.4 GHz 4 dBm

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Revision History

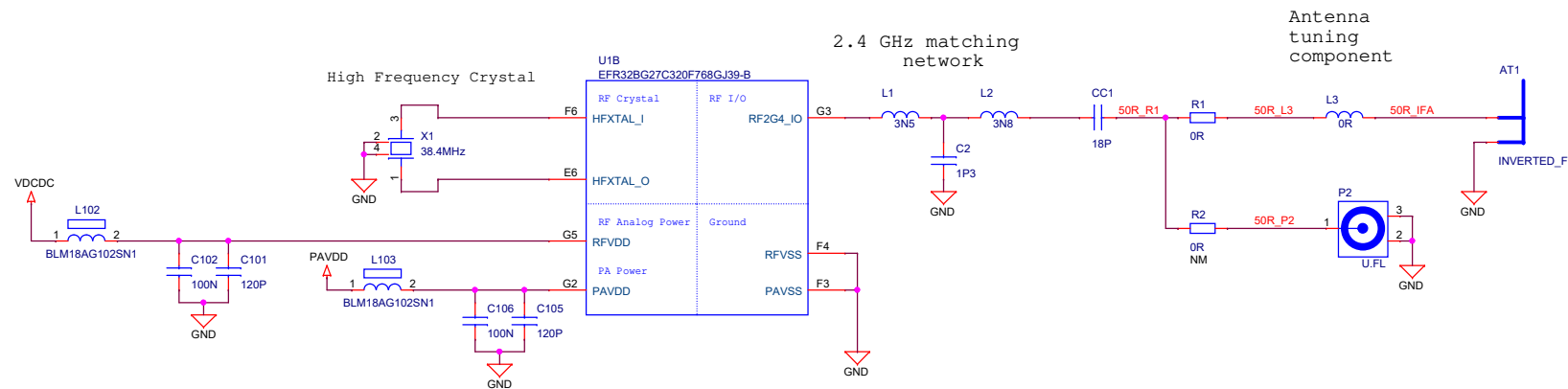
Rev.	Description
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A00	Initial release
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A01	Updated supply filtering and minor PCB fixes
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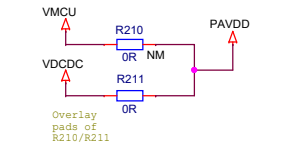
		Board Name	
SILICON LABS		BG27 2.4 GHz 4 dBm DCDC Boost CSP Radio Board	
Designed SZL		Page Title	
Approved RGU		Title Page	
Size A3		Board Number	
Sheet Modified Date Thursday, April 27, 2023		BRD4111B	
		Revision	
		A01	
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		Sheet 1 of 4	

Antenna & Radio Interface



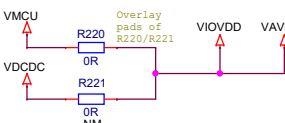
PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



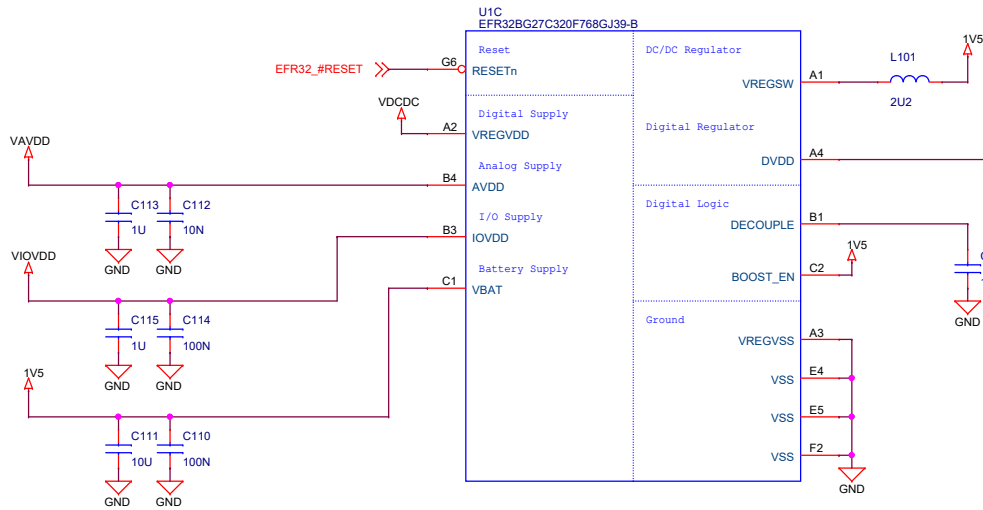
AVDD/IOVDD Configuration

	Power Config 1 VMCU to IO	Power Config 2 DCDC to IO
R220	Mount	Not mount
R221	Not mount	Mount

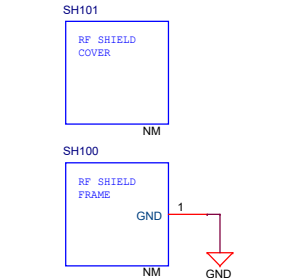



Mounting R221 cause the board to be incompatible with the motherboard due to the different VMCU and VDCDC voltages.

Power & Decoupling



RF Shielding





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Designed SZL Approved RGU
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Board Name
BG27 2.4 GHz 4 dBm DCDC Boost CSP Radio Board

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RF, Antenna and Power

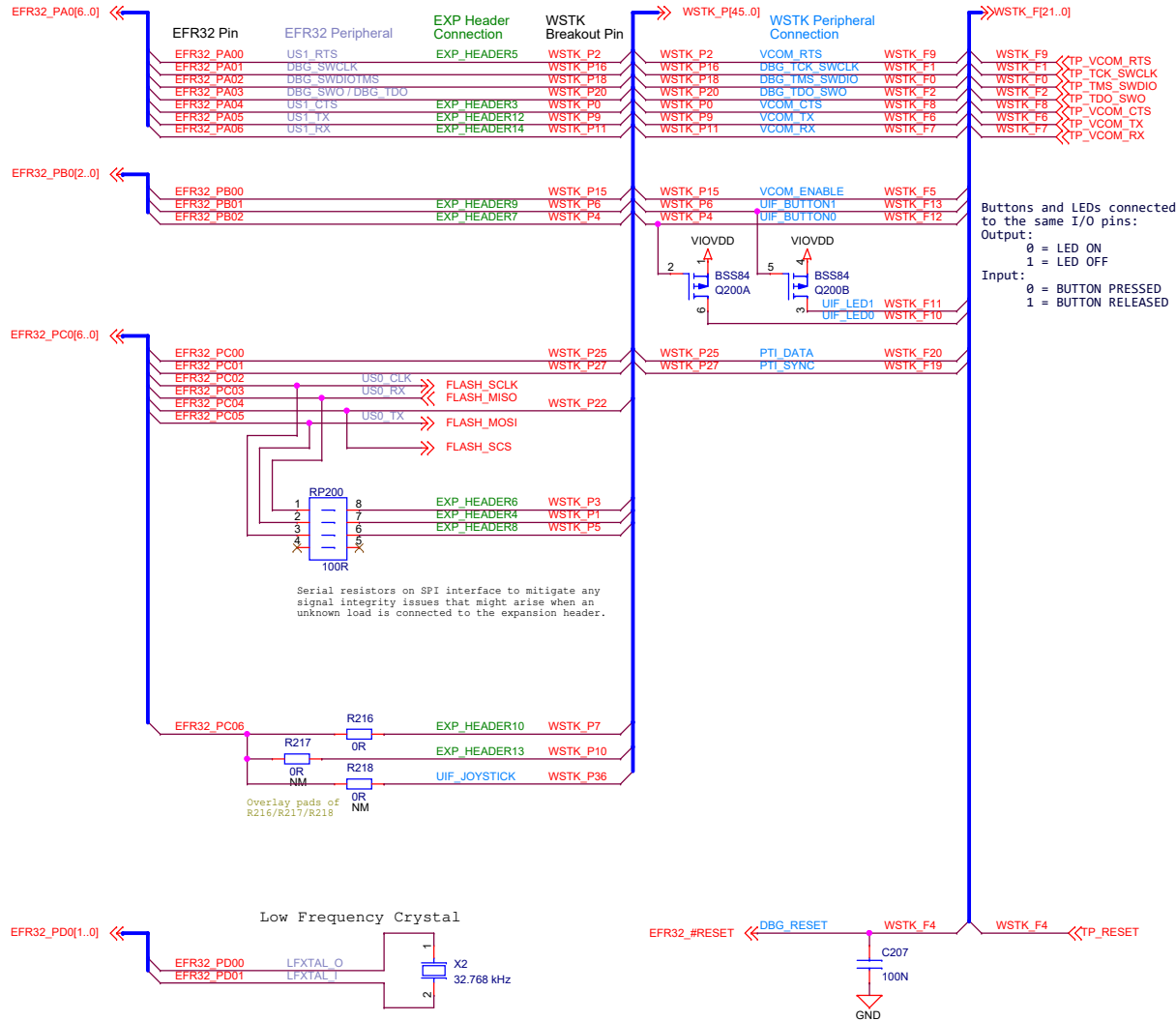
Board Number
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A01

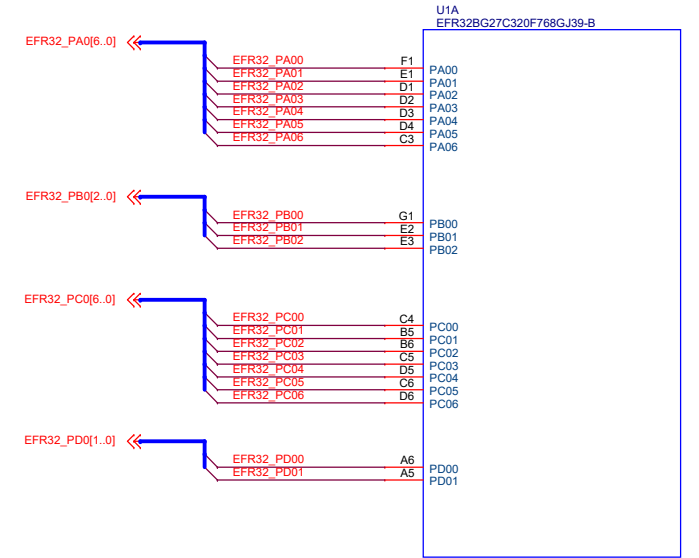
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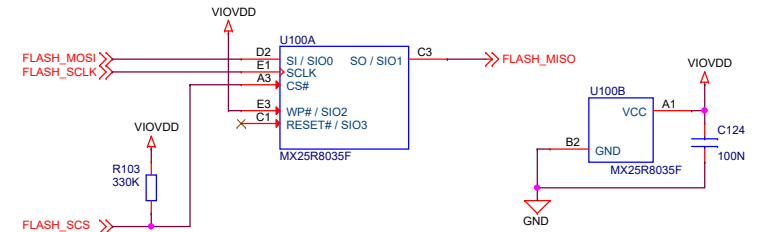
EFR32 Pin Mapping




EFR32 I/O Port Pins



Serial Flash





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Board Name
BG27 2.4 GHz 4 dBm DCDC Boost CSP Radio Board

Page Title
I/O Port Connections

Board Number
BRD4111B

Revision
A01

Designed
SZL

Approved
RGU

Size
A3

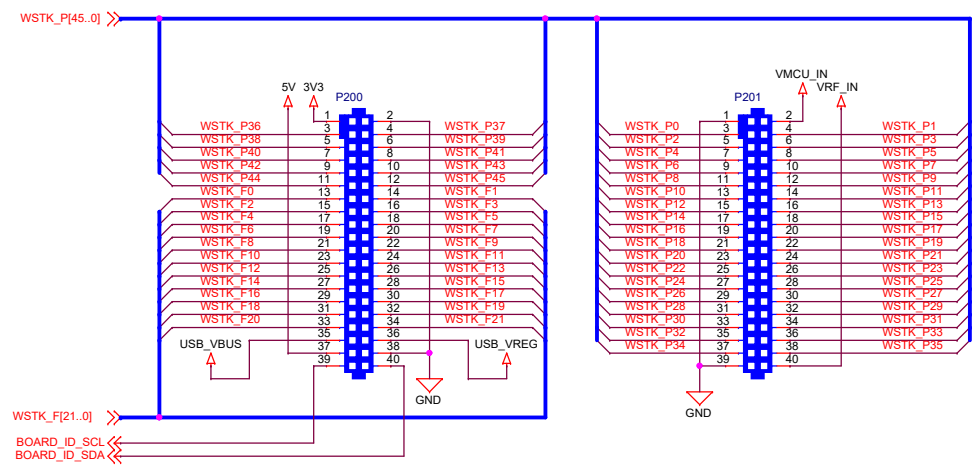
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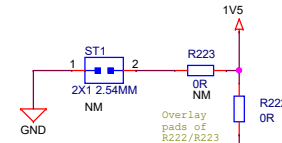
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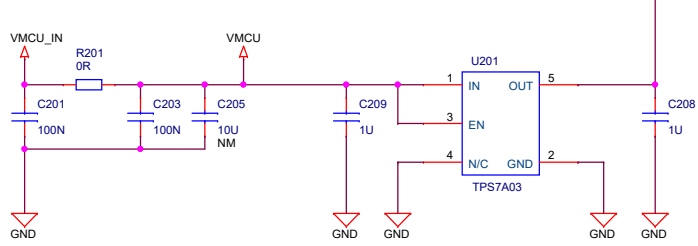
WSTK Connectors



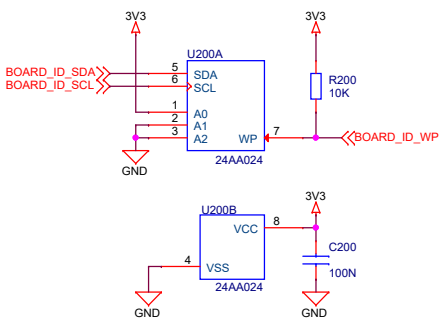
1.5 V External Power Connector



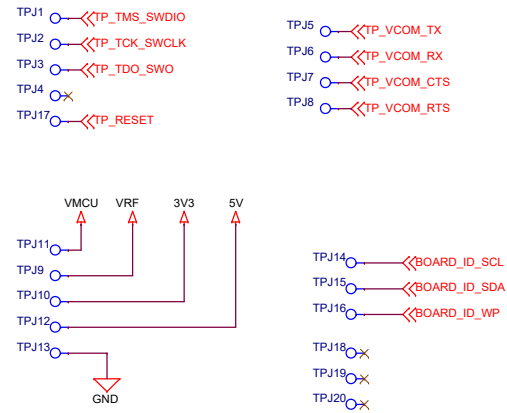
WSTK Power Decoupling and Regulator




Board Identification



Test Points



 SILICON LABS		Board Name BG27 2.4 GHz 4 dBm DCDC Boost CSP Radio Board	
		Page Title WSTK Connectors & Board ID	
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