



**EFR32 Mighty Gecko Dual Band Radio Board**  
2.4 GHz / 434 MHz 10 dBm, DCDC to PAVDD


| Board Function             | Page |
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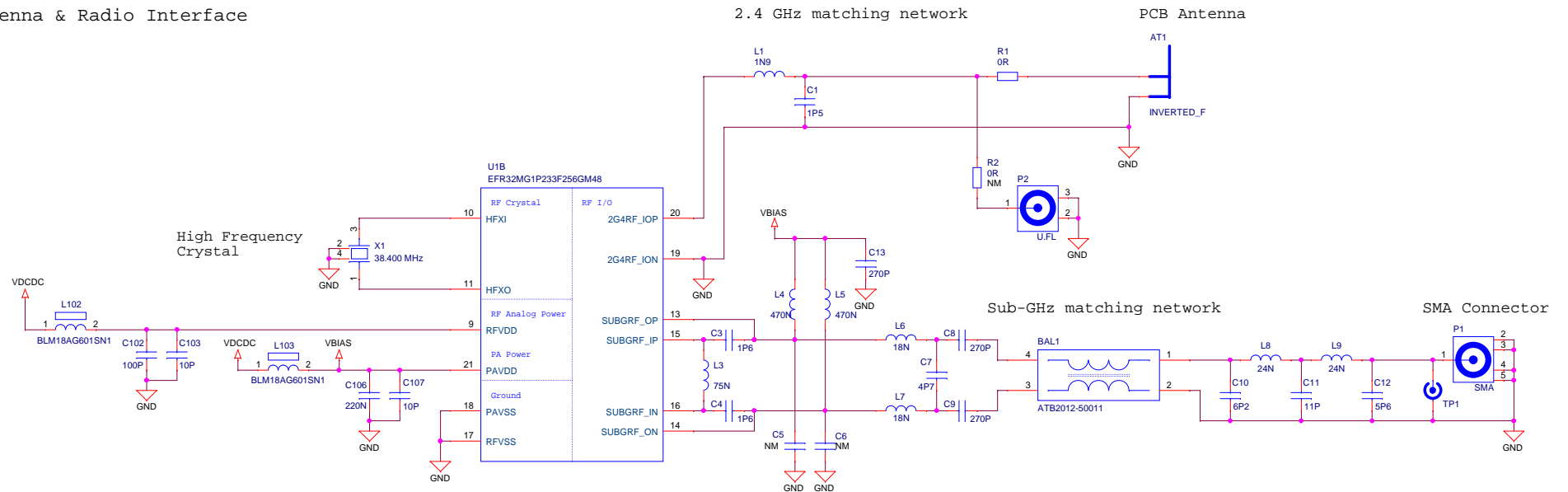


| Revision History |   |
|------------------|---|
| Rev.             | Description                             |
| A00              | Initial release.                        |
| A01              | SubGHz PA supplied from filtered PAVDD. |
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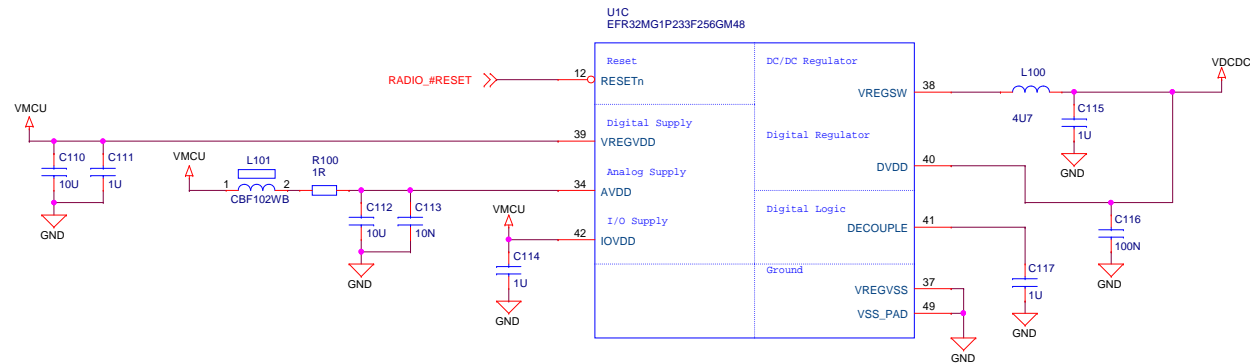
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SCHEMATIC1

|  |                            |   |  |
|--|----------------------------|---|--|
| <br><b>SILICON LABS</b> |                            | Schematic Title                                   |  |
|  |                            | <b>EFR32MG Dual Band 2400/434 MHz Radio Board</b> |  |
| Designed:<br>JSH   |                            | Page Title  |  |
| Approved:<br>JNO   |                            | <b>Title Page</b>                                 |  |
| Size<br>A3   | BOM Doc No:<br><Cage Code> | Document number<br><b>BRD4152A</b>                | Revision<br>A01                                |
| Design Created Date:<br>Monday, April 02, 2012   |                            | Sheet Created Date<br>Monday, April 02, 2012      | Sheet Modified Date<br>Tuesday, April 19, 2016 |
|  |                            | Sheet<br>1 of 4                                   |  |

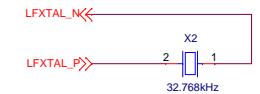
## Antenna & Radio Interface



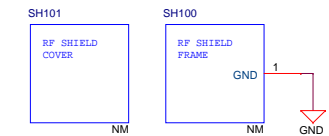
## Power & Decoupling




## Low Frequency Crystal



## RF Shielding



<Schematic Path>  
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|---|--|--|--|
|  |  | Schematic Title                            |  |
| Size A3   |  | EFR32MG Dual Band 2400/434 MHz Radio Board |  |
| Design: JSH   |  | Page Title                                 |  |
| Approved: JNO   |  | RF, Antenna & Power                        |  |
| BOM Doc No: <Cage Code>   |  | Document number                            |  |
| Design Created Date: Monday, April 02, 2012   |  | BRD4152A                                   |  |
| Sheet Created Date: Monday, April 02, 2012  |  | Revision                                   |  |
| Sheet Modified Date: Tuesday, April 19, 2016  |  | A01  |  |
| Sheet 2 of 4  |  |  |  |

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2

## EFR32 I/O Port Pins

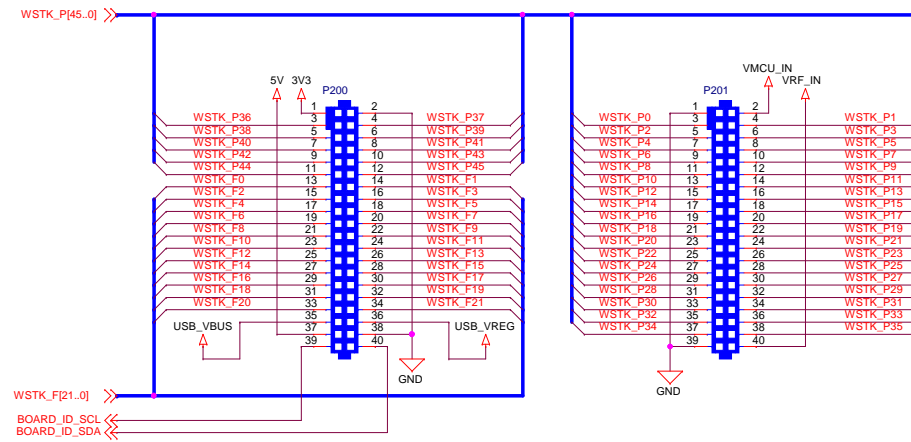
**Serial Flash**

<Schematic Path>  
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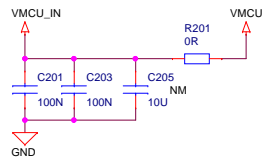
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| <br><b>SILICON LABS</b>                        |  | Schematic Title                                 |  |
|  |  | <b>EFR32MG Dual Band 2400/434 MHz Radio B</b>   |  |
| Designed: JSH<br>Approved: JNO                 |  | Page Title                                      |  |
|  |  | <b>EFR32 I/O Port Connections</b>               |  |
| Size A3<br>BOM Doc No: <Cage Code>             |  | Document number<br><b>BRD4152A</b>              |  |
| Design Created Date:<br>Monday, April 02, 2012 |  | Sheet Created Date<br>Wednesday, April 11, 2012 | Sheet Modified Date<br>Tuesday, April 19, 2016 |
|  |  | Revision<br><b>A01</b><br>Sheet 3 of 4          |  |

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1

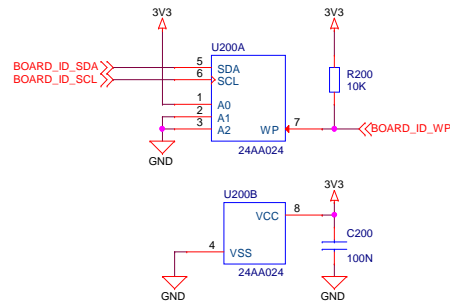
## WSTK Connectors



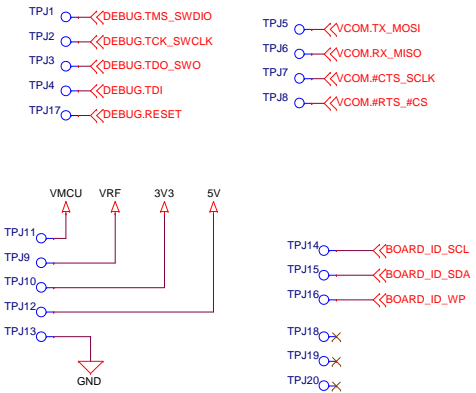
## WSTK Power Decoupling




## Board Identification



## Test Points



<Schematic Path>  
SCHEMATIC1

|   |  |  |              |
|---|--|--|--------------|
|  |  | Schematic Title                              |              |
|   |  | EFR32MG Dual Band 2400/434 MHz Radio Boa     |              |
| Designed: JSH   |  | Page Title                                   |              |
| Size A3   |  | WSTK Connectors & Board ID                   |              |
| BOM Doc No: <Cage Code>   |  | Document number                              | Revision     |
| Design Created Date: Monday, April 02, 2012   |  | BRD4152A                                     | A01          |
| Sheet Created Date: Tuesday, April 03, 2012   |  | Sheet Modified Date: Tuesday, April 19, 2016 | Sheet 4 of 4 |