EFR32MG12 Dual Band Radio Board

2.4 GHz / 490 MHz 19 dBm, VMCU to PAVDD

Revision History

Rev.	Description
A00	Initial revision.

PCB1

PCB4172A

SCHEMATIC1

Designed: <OrgAddr1>	Approved: <OrgAddr2>
Size: A3	BOM Doc No: <Cage Code>
Design Created Date: Friday, November 03, 2017	

Schematic Title: **EFR32MG12 Dual Band 2400/490 MHz Radio Boar**

Page Title: **Title Page**

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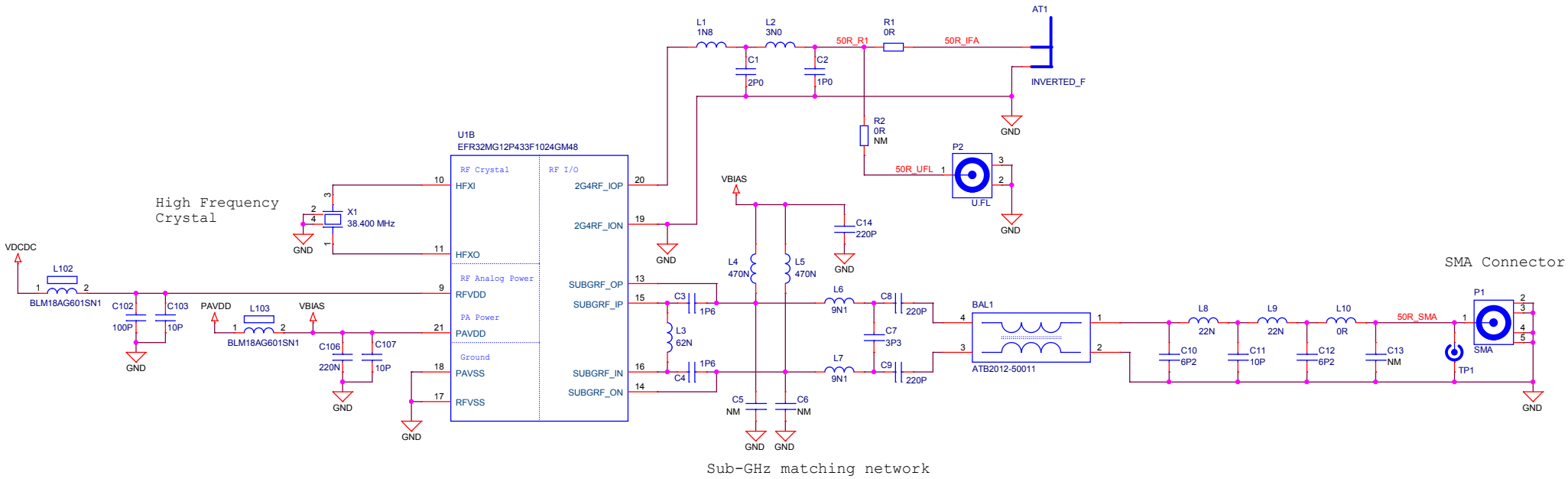
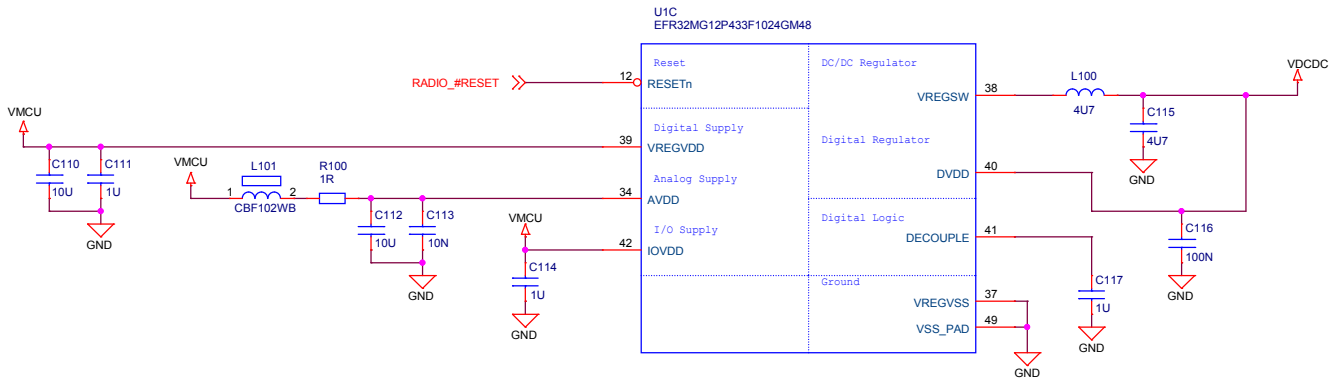
Antenna & Radio Interface

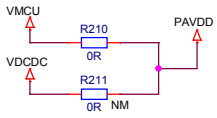


Power & Decoupling

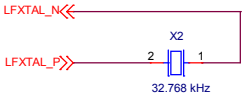


PAVDD Configuration

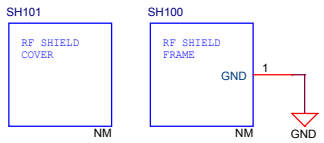
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



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SILICON LABS

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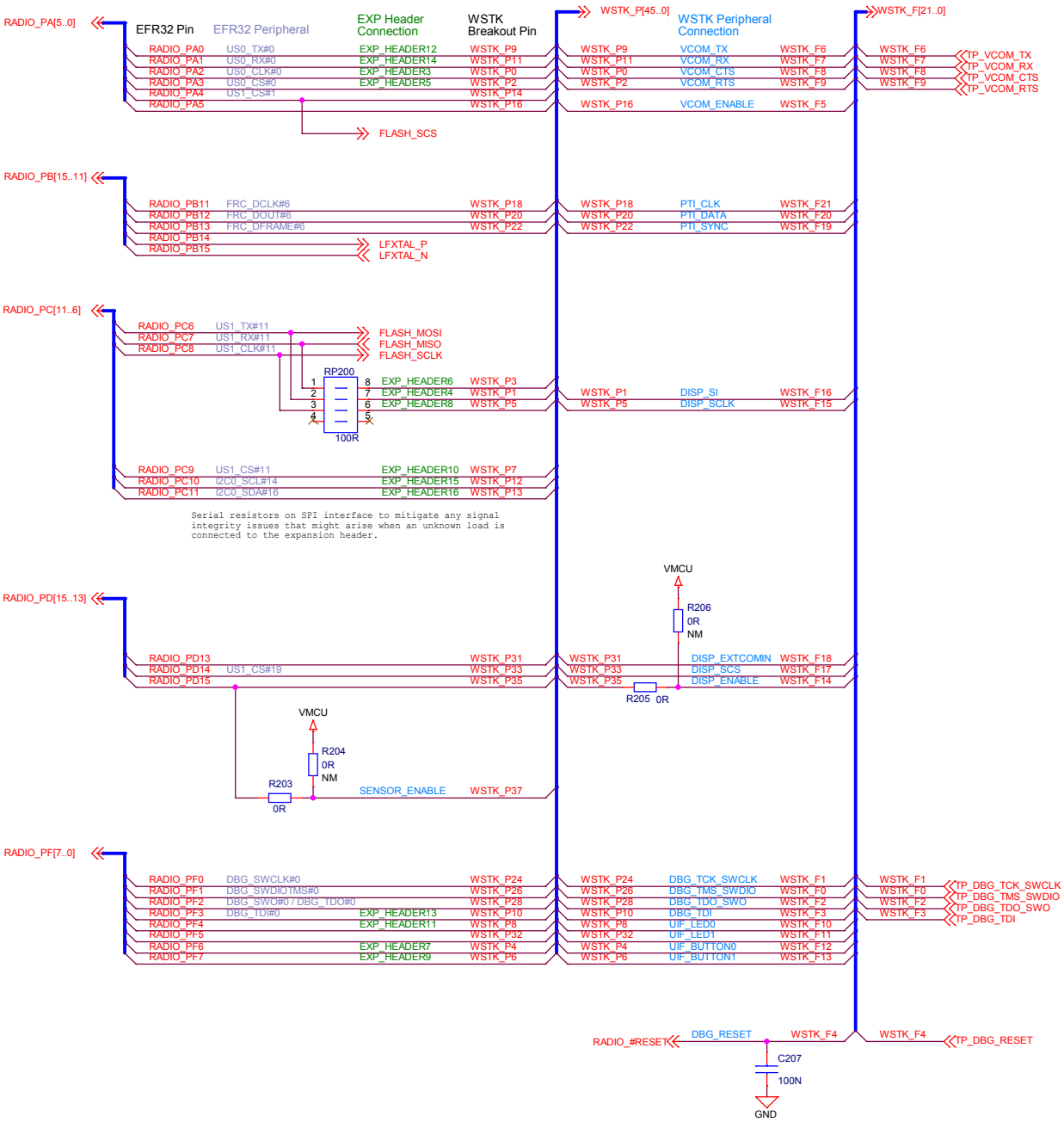
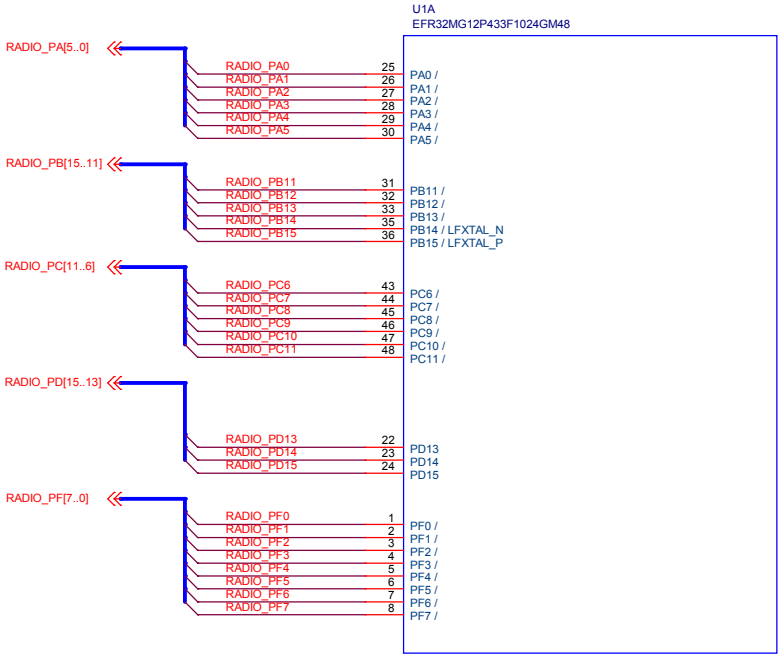
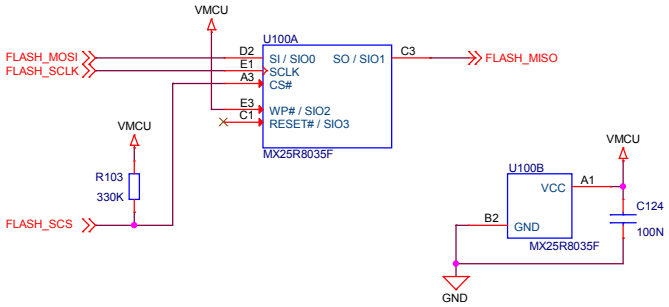
Pin Mapping



I/O Port Pins



Serial Flash



SCHEMATIC1

SILICON LABS	Schematic Title: **EFR32MG12 Dual Band 2400/490 MHz Radio Board**
Designed: <OrgAddr1> / Approved: <OrgAddr2>	Page Title: **I/O Port Connections**
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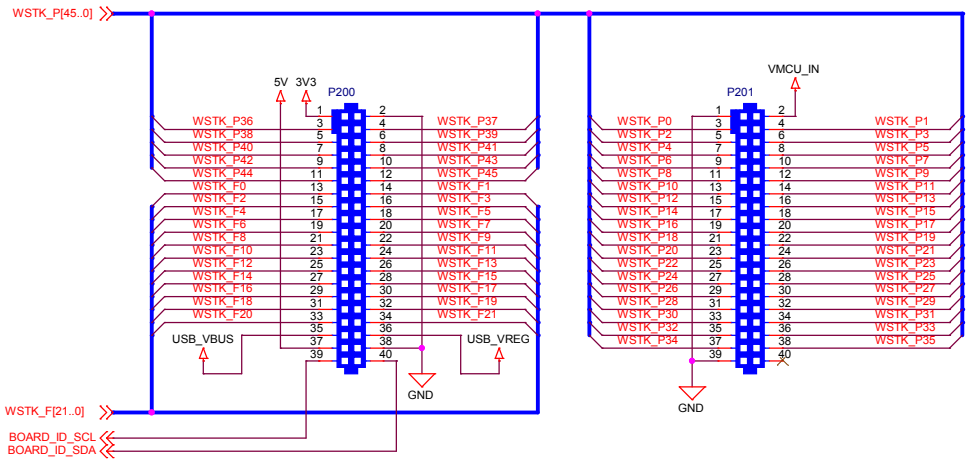
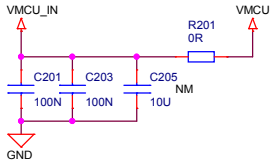
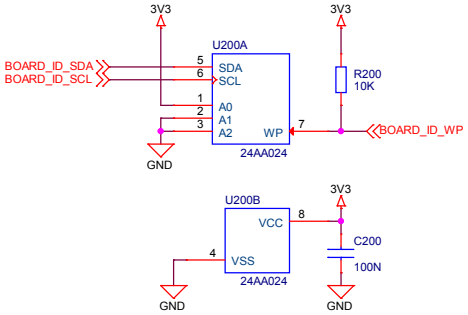
WSTK Connectors



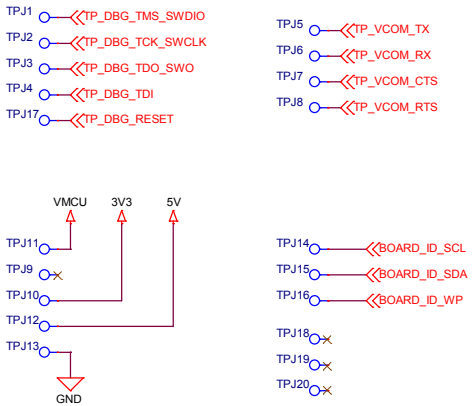
WSTK Power Decoupling



Board Identification



Test Points



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Page Title: **WSTK Connectors & Board ID**

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