



EFR32MG12 Dual Band Radio Board
2.4 GHz / 169 MHz 19 dBm, VMCU to PAVDD

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Revision History	
Rev.	Description
A00	Initial revision.

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Approved: <OrgAddr2>

Size A3

BOM Doc No: <Cage Code>

Design Created Date: Friday, November 03, 2017

Schematic Title

EFR32MG12 Dual Band 2400/169 MHz Radio Board

Page Title

Title Page

Document number

BRD4172B

Sheet Created Date: Friday, November 03, 2017

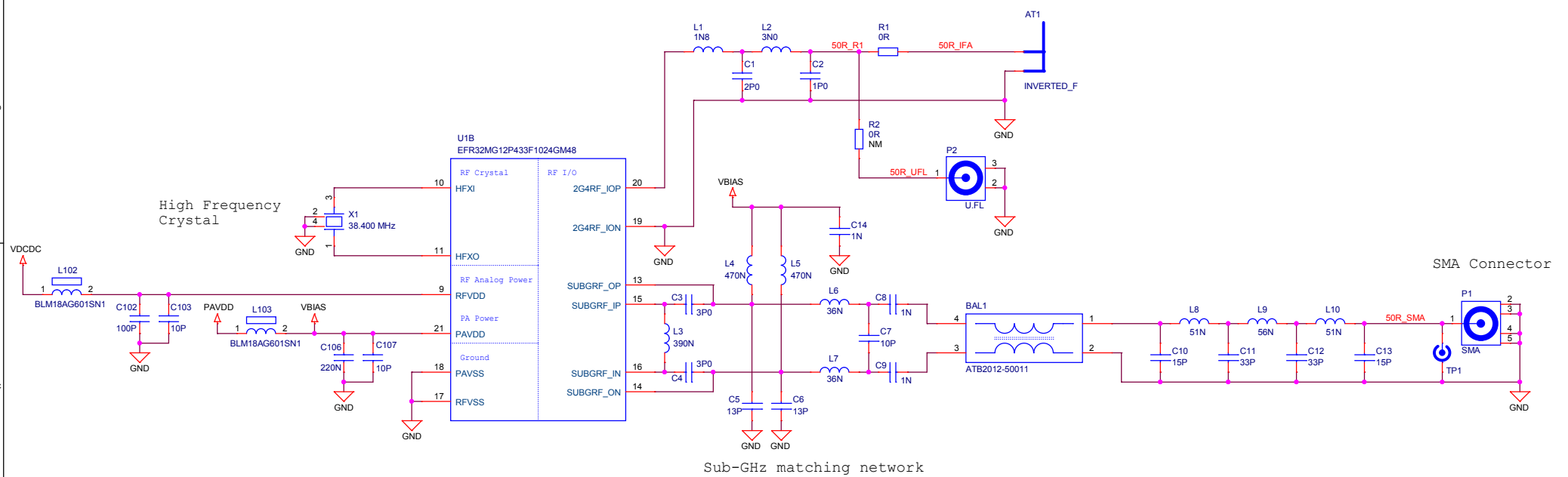
Sheet Modified Date: Tuesday, March 13, 2018

Revision

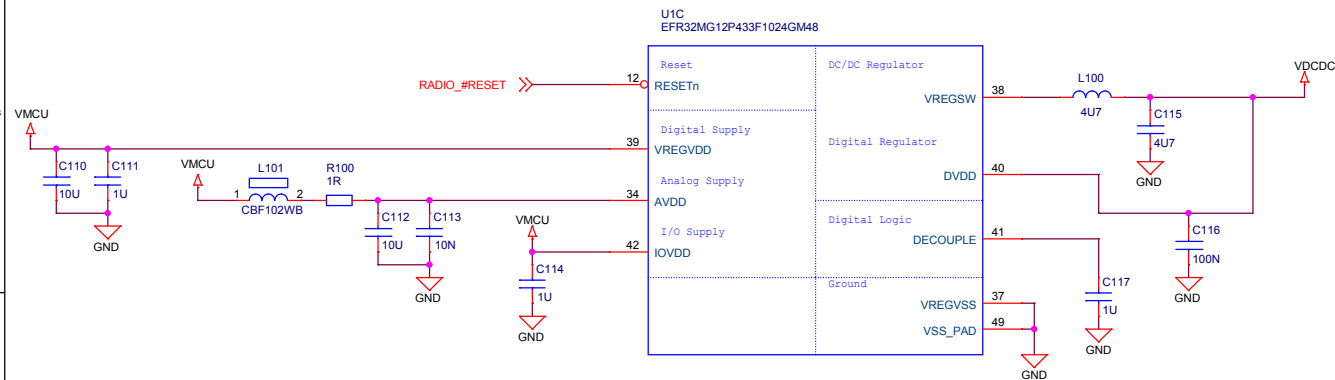
A00

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Antenna & Radio Interface

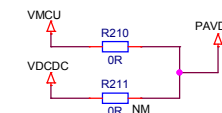


Power & Decoupling

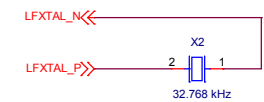


PAVDD Configuration

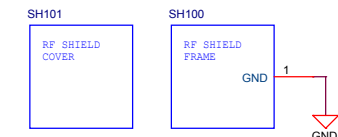
	Power Config 1 VMCU to PAVDD	Power Config DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



RF Shielding



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EFR32MG12 Dual Band 2400/169 MHz Radio Board

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RF, Antenna and Power

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Sheet Created Date
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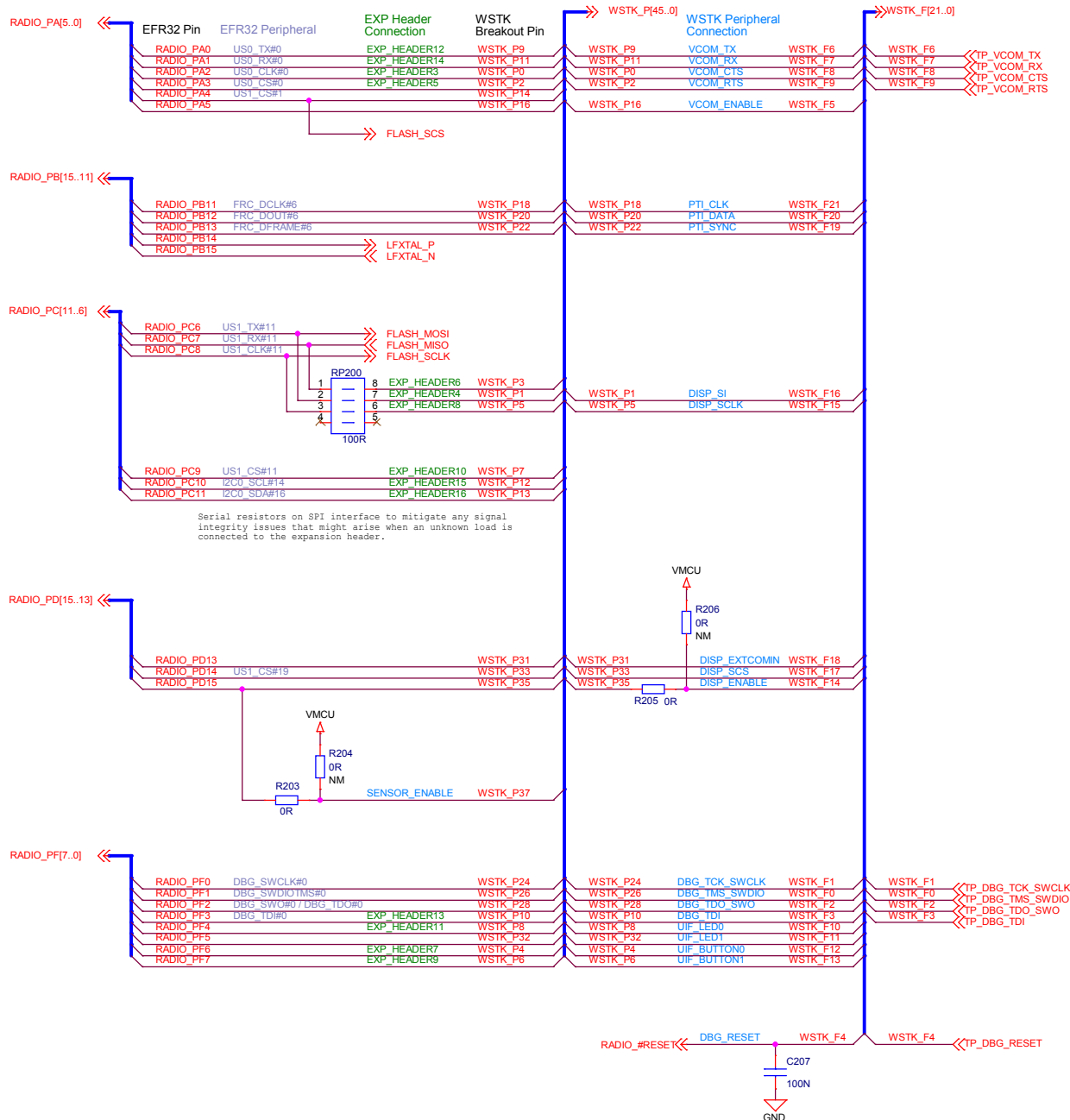
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	Revision
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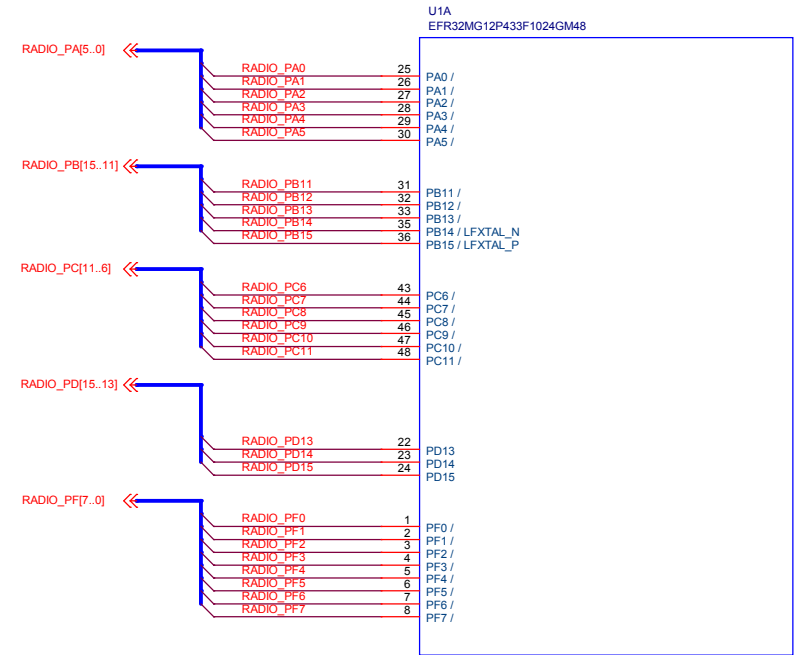
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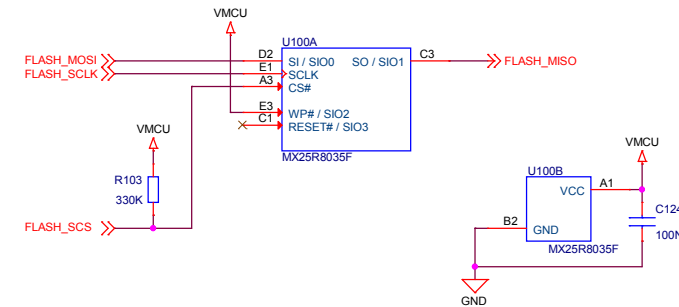
Pin Mapping



I/O Port Pins



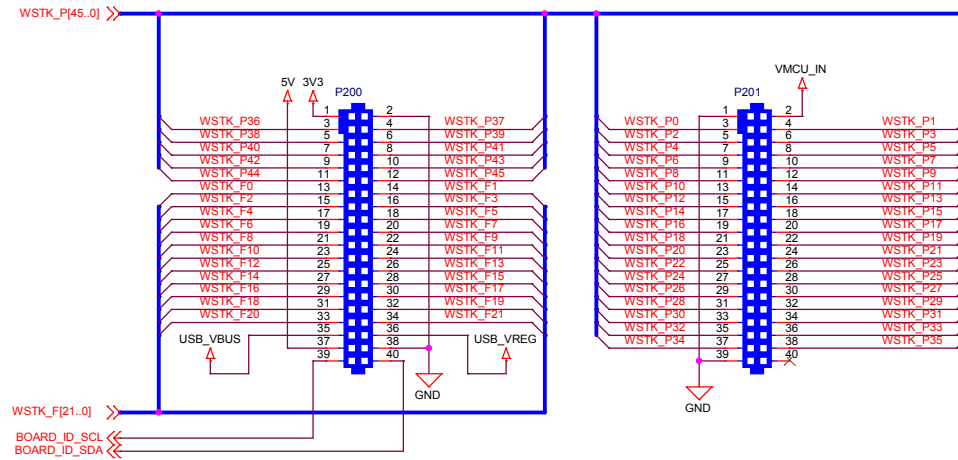
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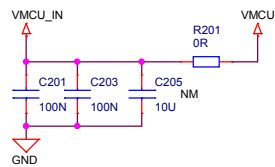
SCHEMATIC1

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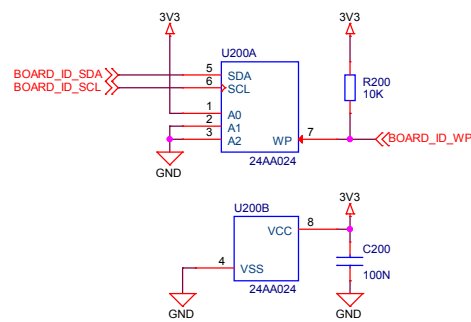
WSTK Connectors



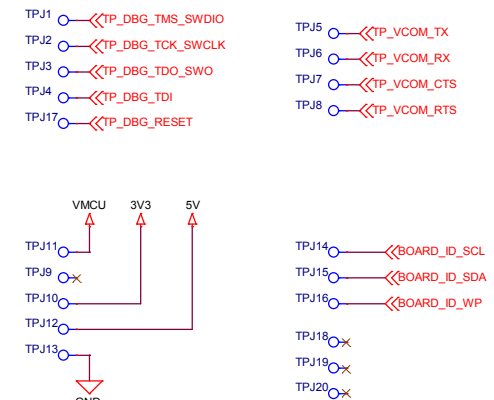
WSTK Power Decoupling



Board Identification



Test Points



SCHEMATIC1

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