



EFR32xG21 Radio Board

2.4 GHz 20 dBm, QFN32

Board Function	Page
Title Page	1
RF, Antenna & Power	2
EFR32 Signal Assignments	3
WSTK Connectors & Board ID	4

LEGAL NOTICE:
SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC. IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.

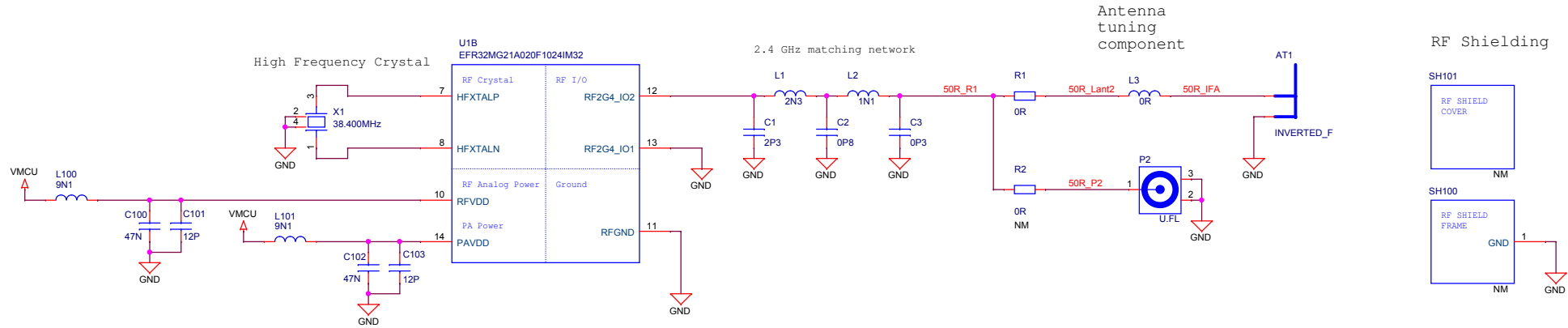


Revision History

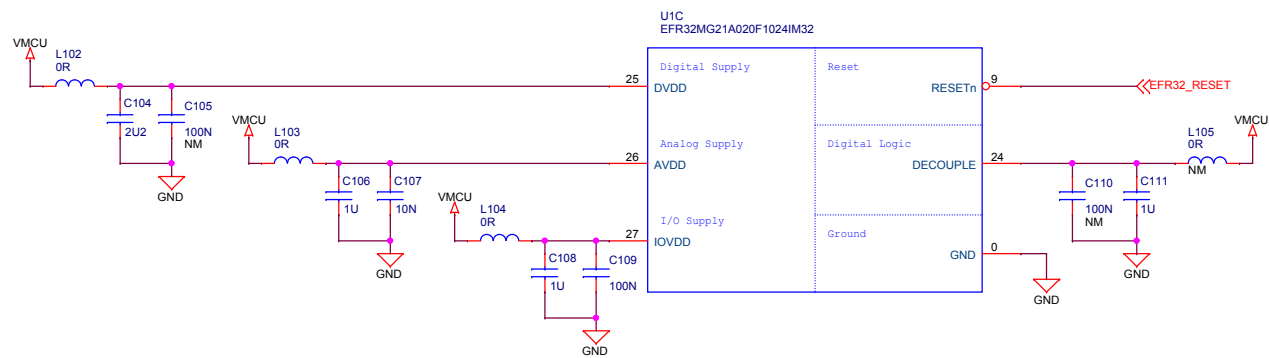
Rev.	Description
A00	Initial production release.
A01	Removed R3, U1 Pin 13 connected directly to GND. Improved PCB layout.

		Schematic Title	
Designed: JSH		EFR32xG21 2.4 GHz 20 dBm Radio Board	
Approved: JNO		Page Title	
Size A3		Title Page	
BOM Doc No:		Document number	Revision
Design Created Date: Friday, November 03, 2017		BRD4180A	A01
Sheet Created Date: Friday, November 03, 2017		Sheet Modified Date: Wednesday, December 12, 2018	Sheet 1 of 4

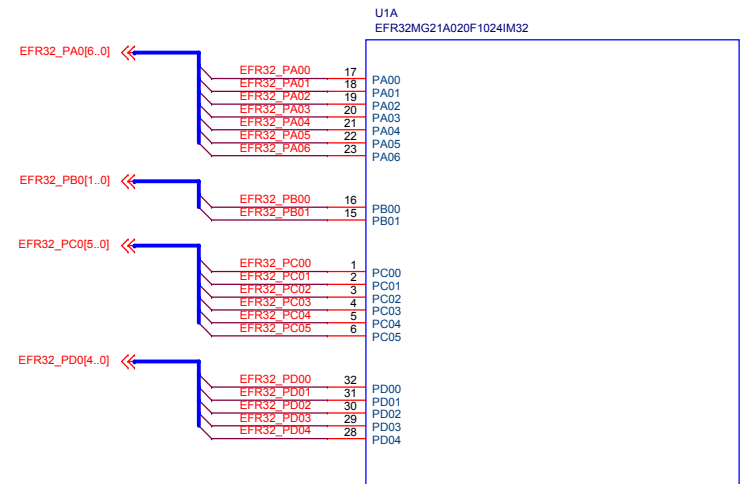
Antenna & Radio Interface




Power & Decoupling

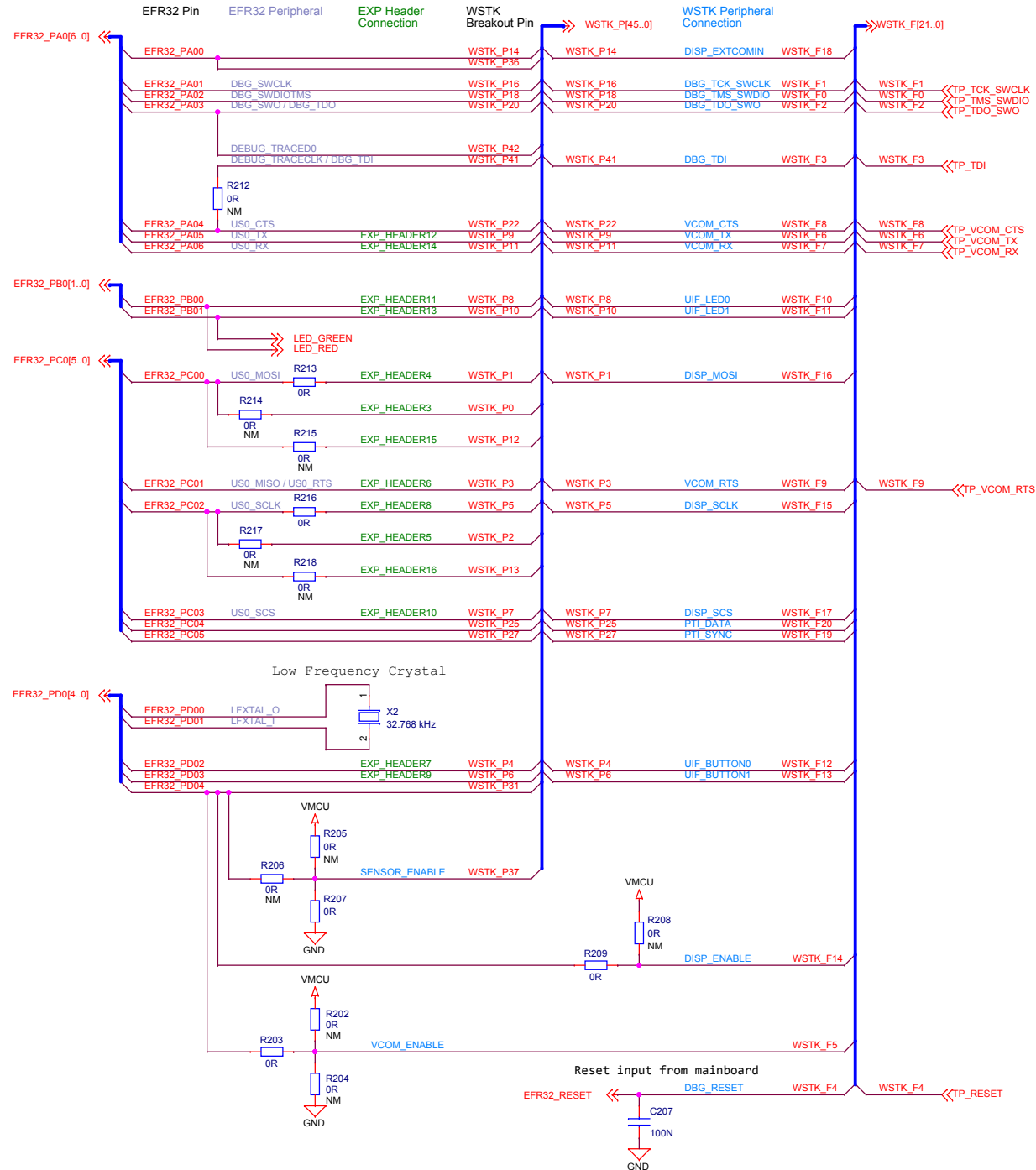


I/O Port Pins

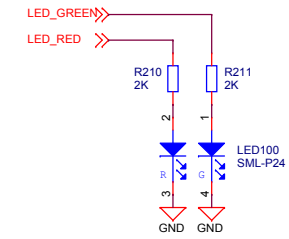


 SILICON LABS		Schematic Title	
		EFR32xG21 2.4 GHz 20 dBm Radio Board	
Designed: JSH		Approved: JNO	
Size A3		BOM Doc No:	
Design Created Date: Friday, November 03, 2017		Document number BRD4180A	
Sheet Created Date: Friday, November 03, 2017		Sheet Modified Date: Wednesday, December 12, 2018	
Revision A01		Sheet 2 of 4	

Pin Mapping



LEDs



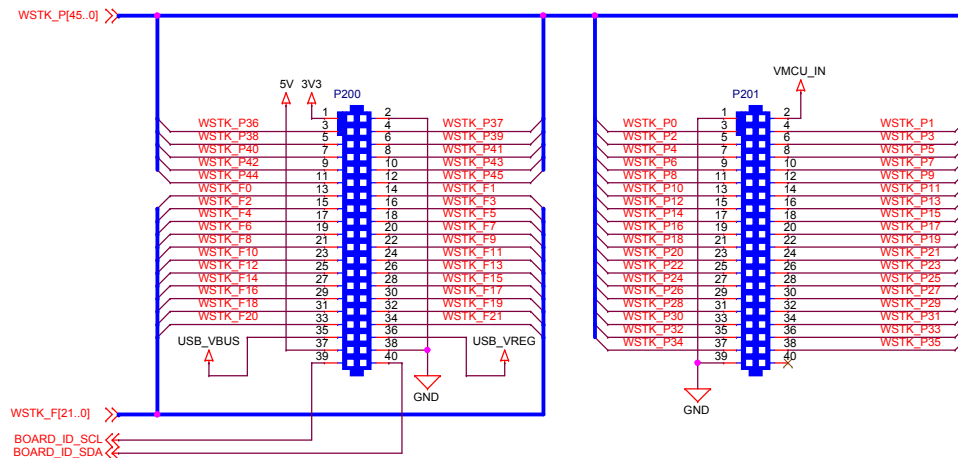
Designed: JSH		Approved: JNO	
Size A3	BOM Doc No:		
Design Created Date: Friday, November 03, 2017			

Schematic Title
EFR32xG21 2.4 GHz 20 dBm Radio Board

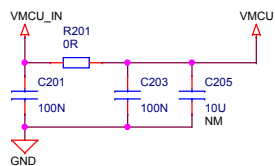
Page Title	I/O Port Connections
------------	-----------------------------

Document number BRD4180A		Revision A01
Sheet Created Date Friday, November 03, 2017	Sheet Modified Date Wednesday, December 12, 2018	Sheet 3 of 4

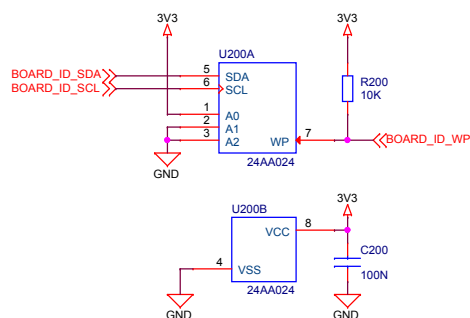
WSTK Connectors



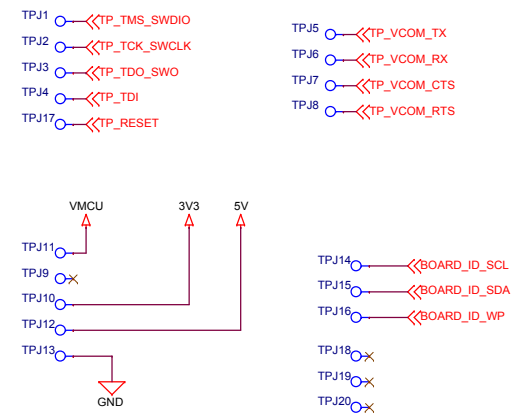
WSTK Power Decoupling



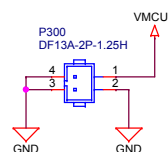
Board Identification




Test Points



Battery Connector



 SILICON LABS		Schematic Title	
		EFR32xG21 2.4 GHz 20 dBm Radio Board	
Designed: JSH		Page Title	
Size A3		WSTK Connectors & Board ID	
BOM Doc No:		Document number	Revision
Design Created Date: Friday, November 03, 2017		BRD4180A	A01
Sheet Created Date: Friday, November 03, 2017		Sheet Modified Date: Wednesday, December 12, 2018	Sheet 4 of 4