EFR32xG22 Radio Board

2.4 GHz 6 dBm

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Revision History

Rev.	Description
A00	Initial production release.
A01	Mounted U1. Updated C1, C2 P/N.

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PCB1

PCB4182A

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Design Created Date: Friday, November 03, 2017		Document number: BRD4182A	Revision: A01
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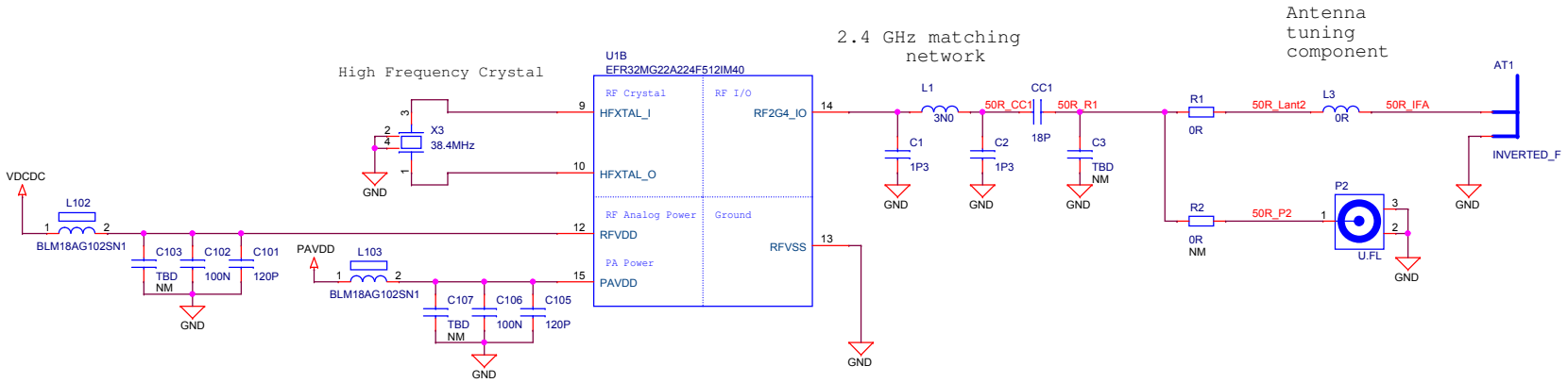
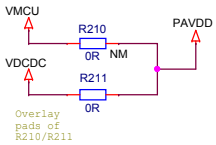
Antenna & Radio Interface

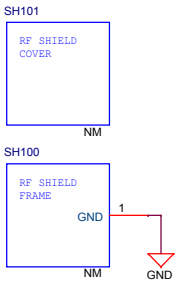


PAVDD Configuration

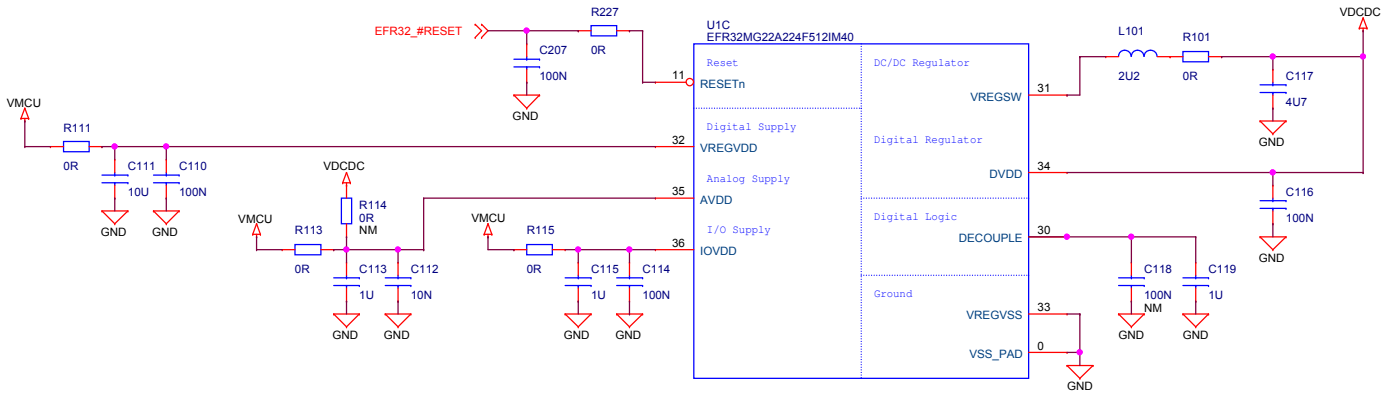
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



RF Shielding



Power & Decoupling



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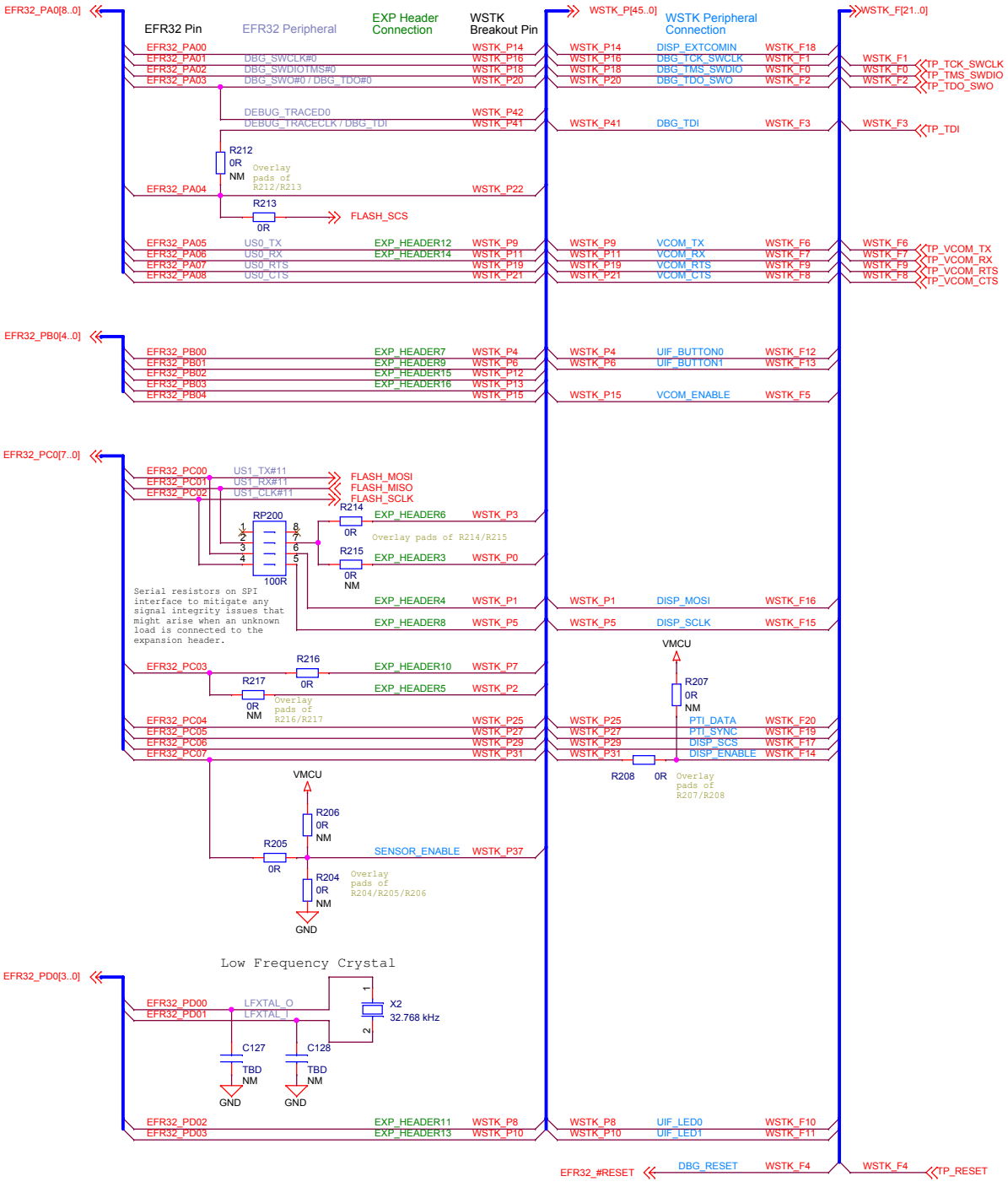
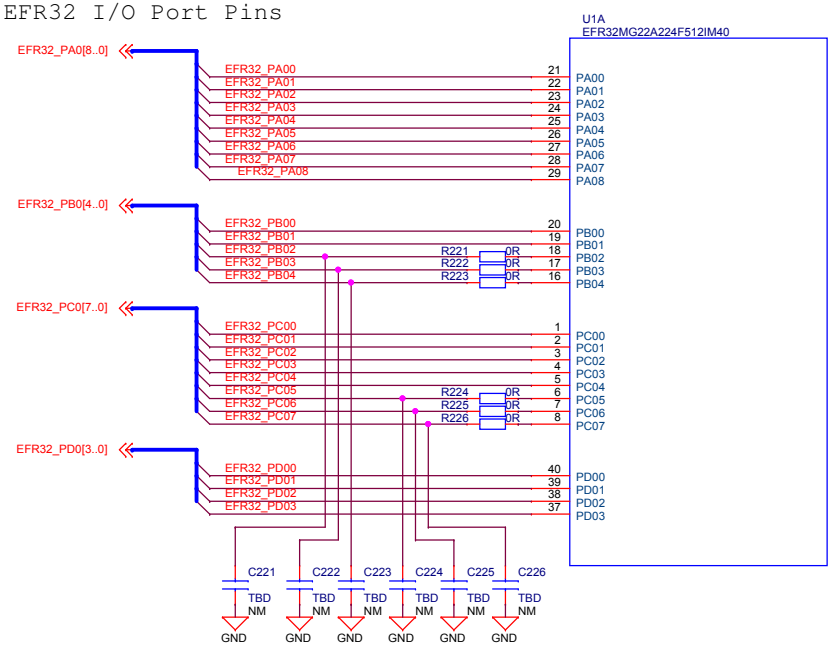
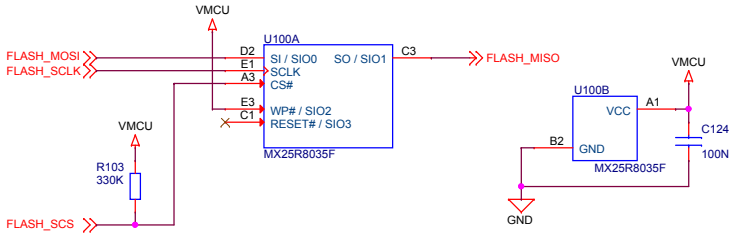
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



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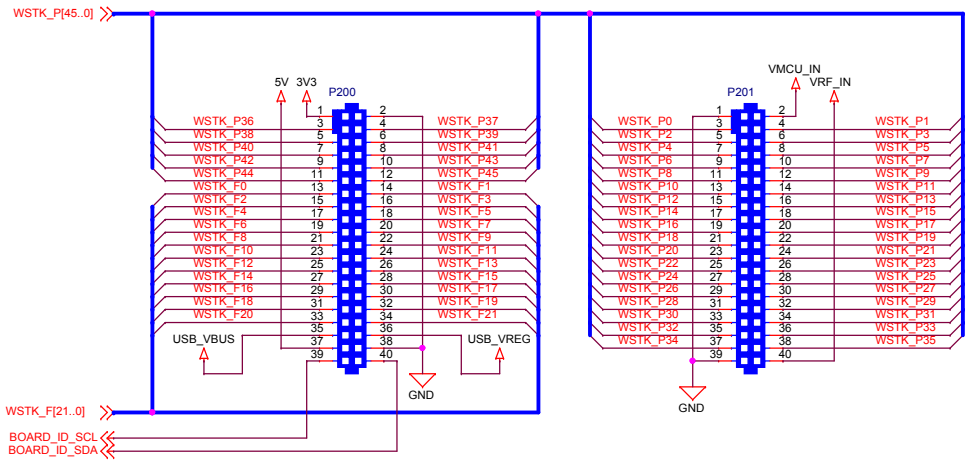
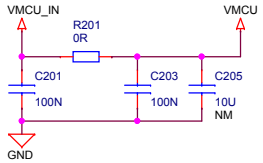
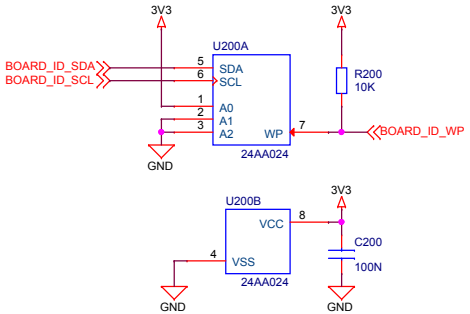
WSTK Connectors



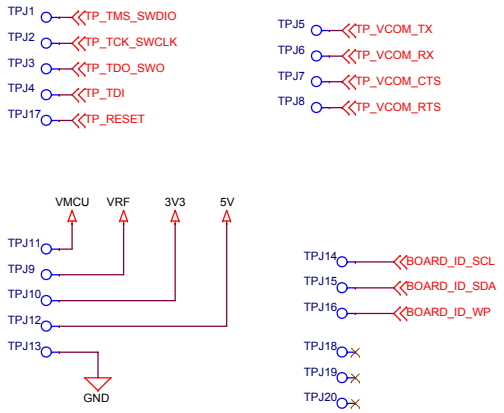
WSTK Power Decoupling



Board Identification



Test Points



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