EFR32xG22 Radio Board

2.4 GHz 6 dBm

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Revision History

Rev.	Description
A00	Initial production release.
A01	Mounted U1. Updated C1, C2 P/N.
A02	Matching layout improvement at C1 GND pad.
B00	X3-->X1. Updated U1, X1 P/N, matching and USART markings. PCB sikscreen fix.
B01	Updated matching network.
B02	Updated matching network.

PCB1

PCB4182A

SILICON LABS

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Design Created Date: Friday, November 03, 2017	

Schematic Title: **EFR32xG22 2.4 GHz 6 dBm Radio Board**

Page Title: **Title PAge**

Document number: BRD4182A	Revision: B02
Sheet Created Date: Friday, November 03, 2017	Sheet Modified Date: Thursday, June 27, 2019
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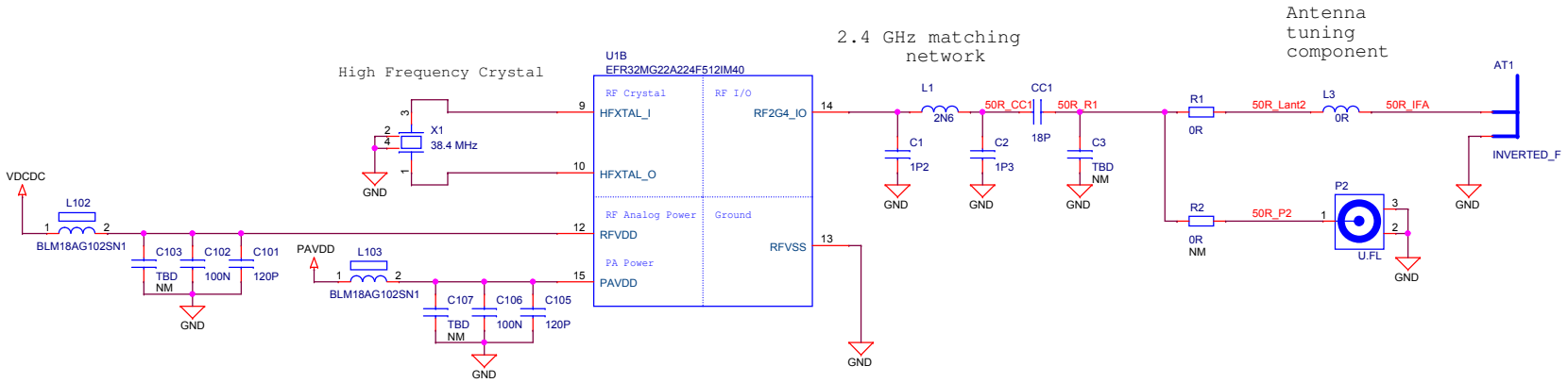
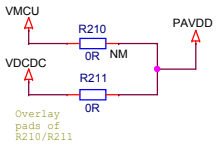
Antenna & Radio Interface

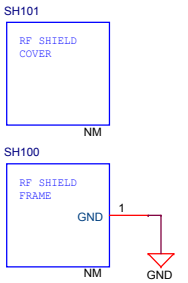


PAVDD Configuration

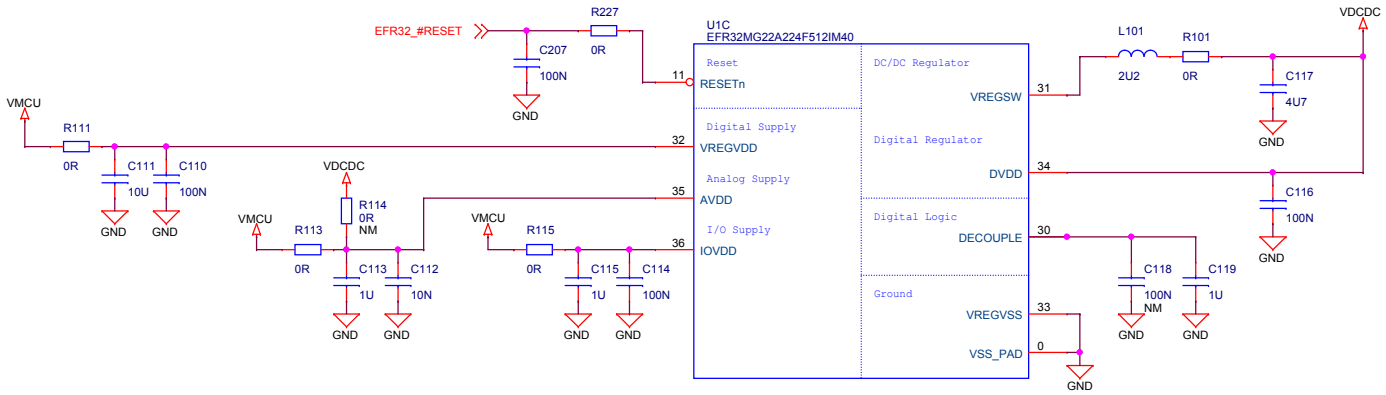
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



RF Shielding



Power & Decoupling



SILICON LABS	Schematic Title: EFR32xG22 2.4 GHz 6 dBm Radio Board		
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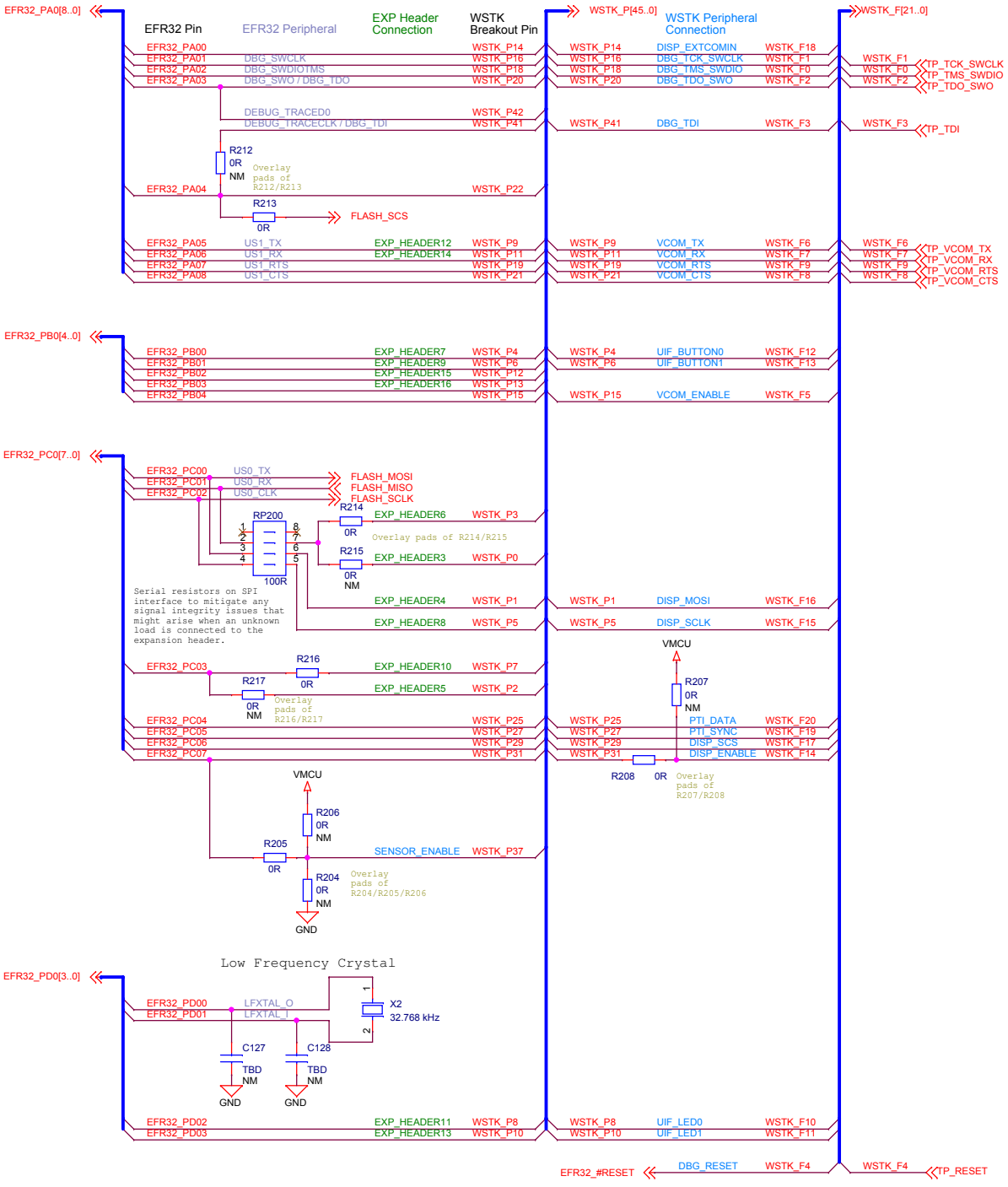
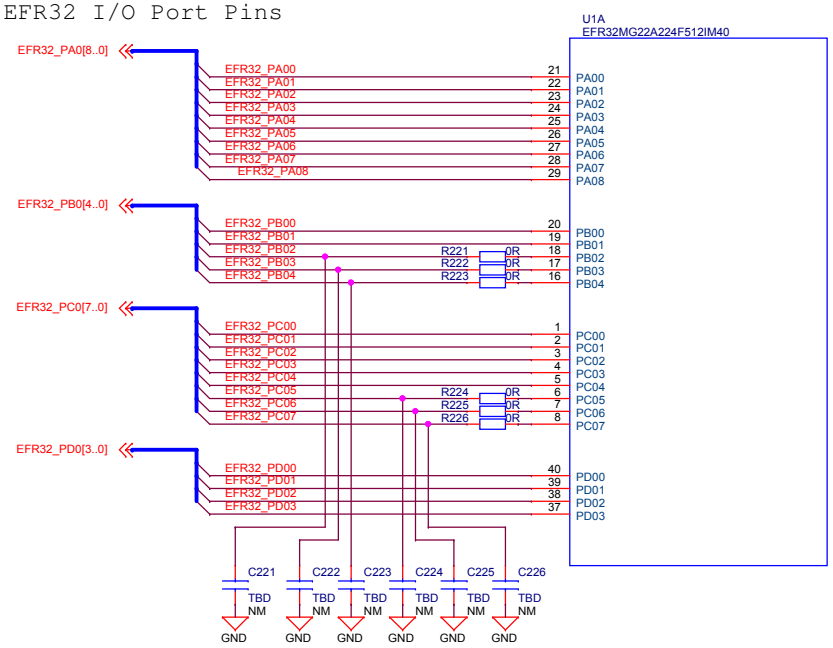
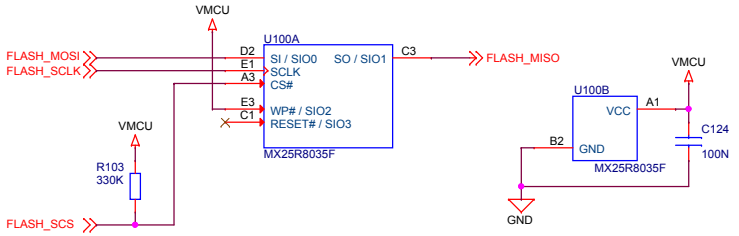
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



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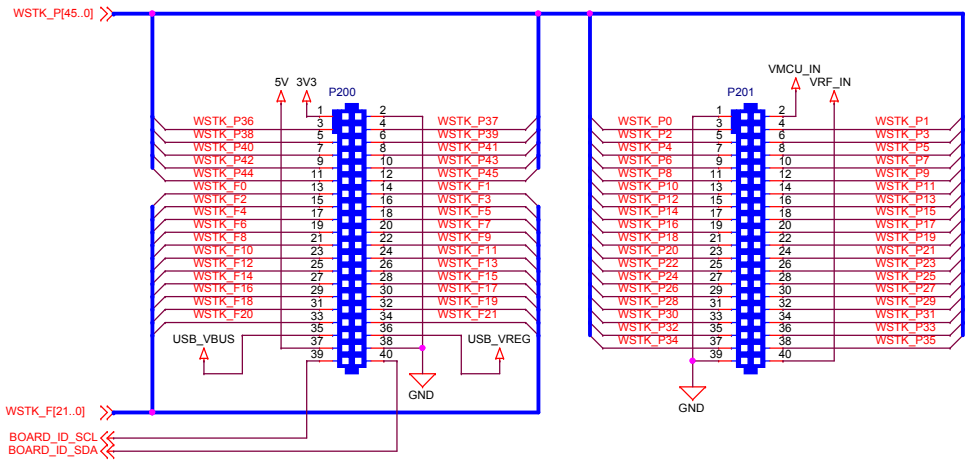
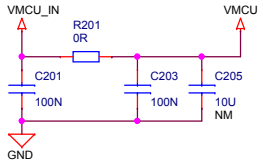
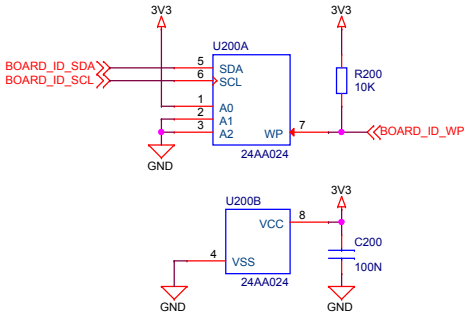
WSTK Connectors



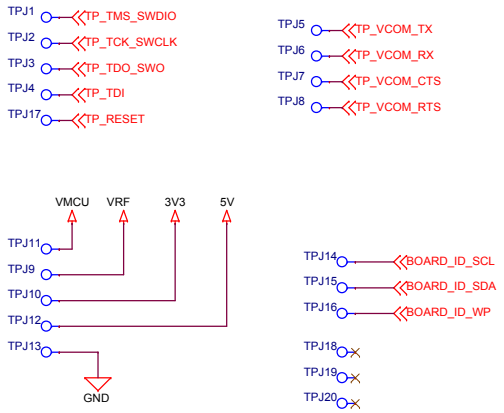
WSTK Power Decoupling



Board Identification



Test Points



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