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EFR32xG24B Radio Board 2.4 GHz 20 dBm	
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Revision History	
Rev.	Description
A00	Initial release.



Board Name

EFR32xG24B 2.4 GHz 20 dBm Radio Board

Page Title

Title Page

Board Number

BRD4187C

Revision

A00

Designed

TAB

Approved

RGU

Size

A3

Sheet Modified Date

Wednesday, September 08, 2021

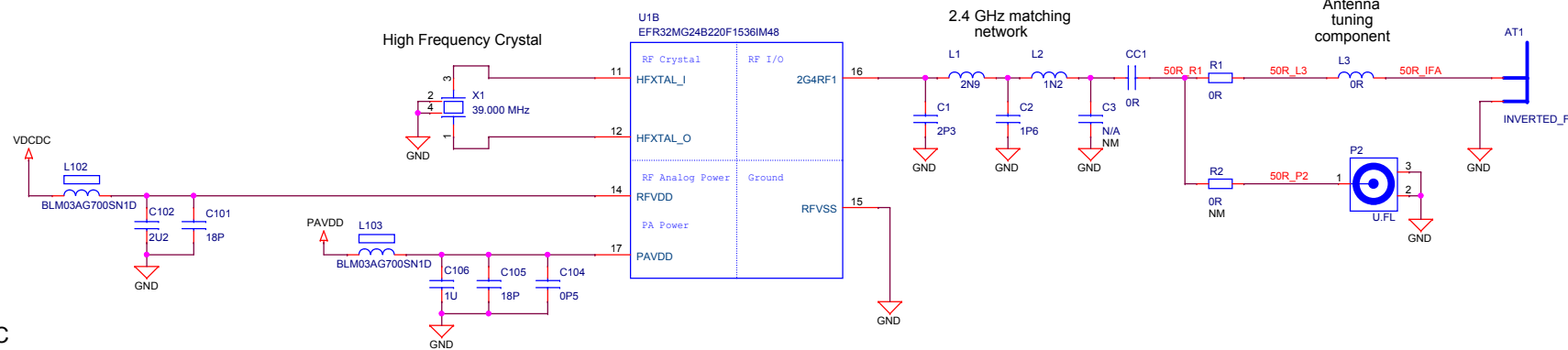
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Sheet

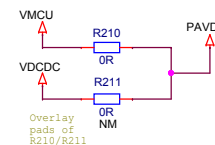
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## Antenna & Radio Interface

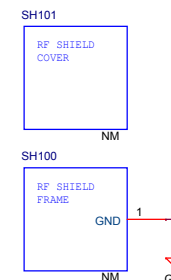


## PAVDD Configuration

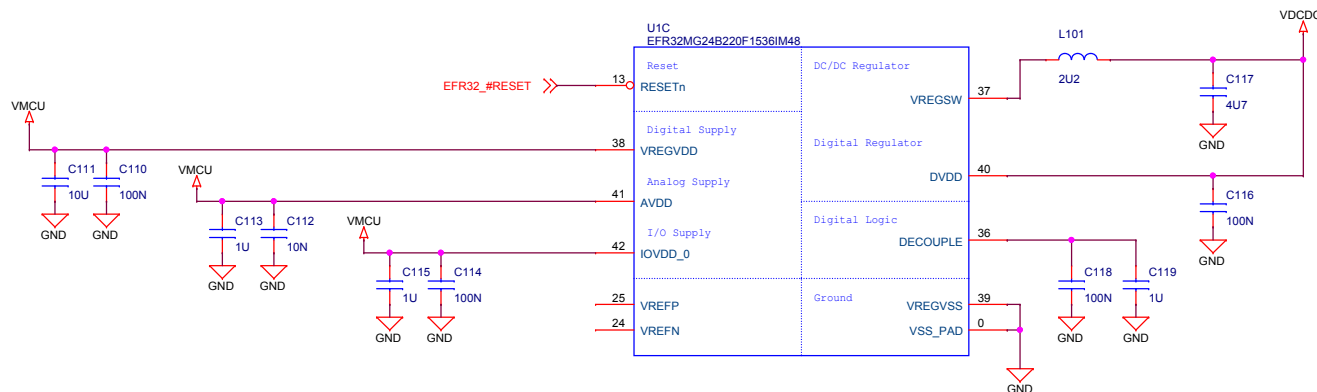
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount




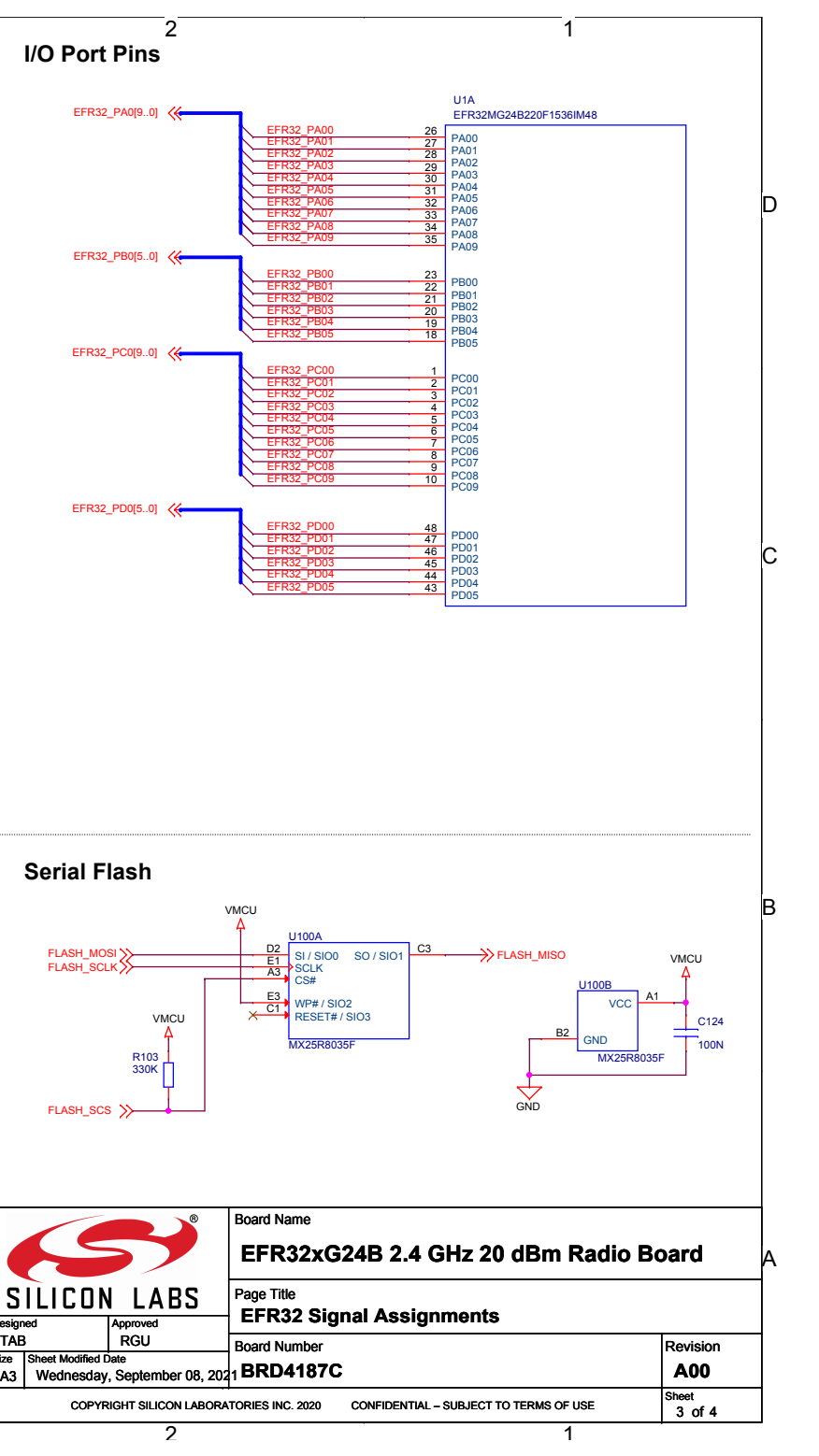
## RF Shielding



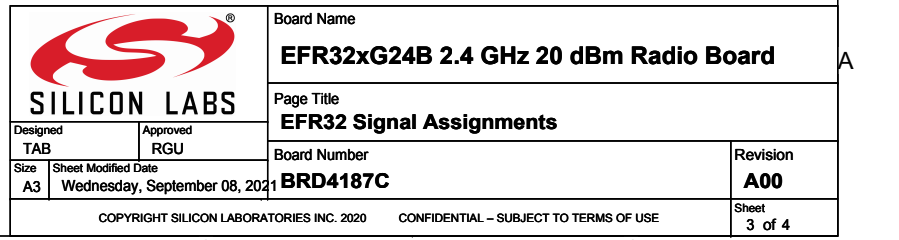
## Power & Decoupling



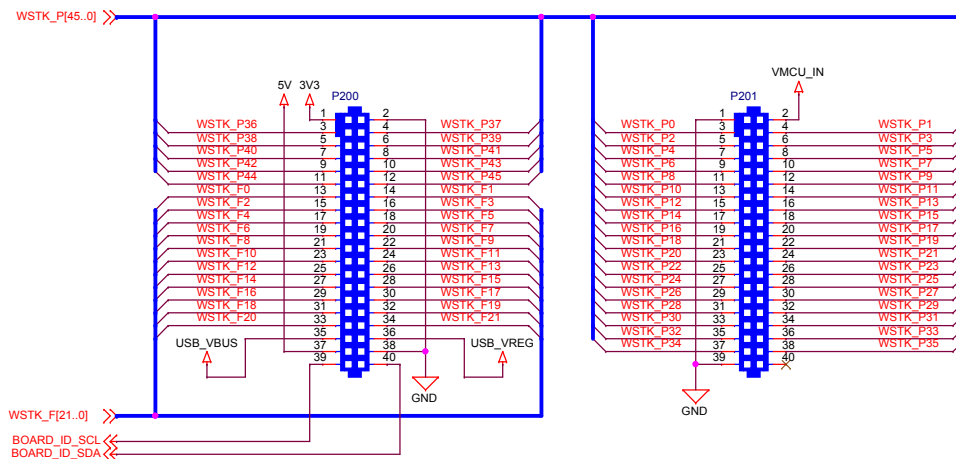
 <b>SILICON LABS</b>		Board Name <b>EFR32xG24B 2.4 GHz 20 dBm Radio Board</b>	
		Page Title <b>RF &amp; Power</b>	
Designed TAB		Approved RGU	
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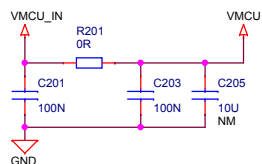
The schematic diagram illustrates the connection of the MX25R8035F SPI flash memory to the VMCU. It features two instances of the MX25R8035F, labeled U100A and U100B. U100A is connected to the VMCU and FLASH\_MISO. U100B is connected to the VMCU and GND. The circuit includes a 330K resistor (R103) and a 100nF capacitor (C124).



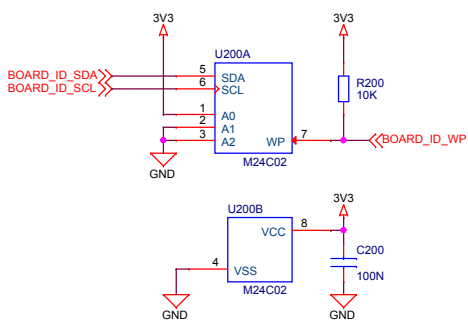
## WSTK Connectors



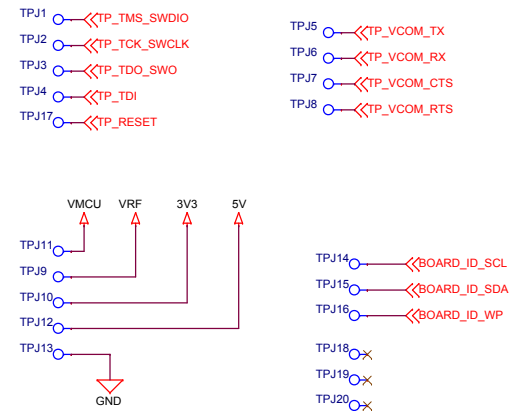
## WSTK Power Decoupling




## Board Identification



## Test Points



 <b>SILICON LABS</b>		Board Name	
		<b>EFR32xG24B 2.4 GHz 20 dBm Radio Board</b>	
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