



## EFR32xG27 Radio Board

2.4 GHz 8 dBm

Board Function	Page
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Title Page	1
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RF, Antenna & Power	2
---------------------	---

I/O Port Connections	3
----------------------	---

WSTK Connectors & Board ID	4
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## Revision History


Rev.	Description
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A00	Initial release, without U1 and matching.
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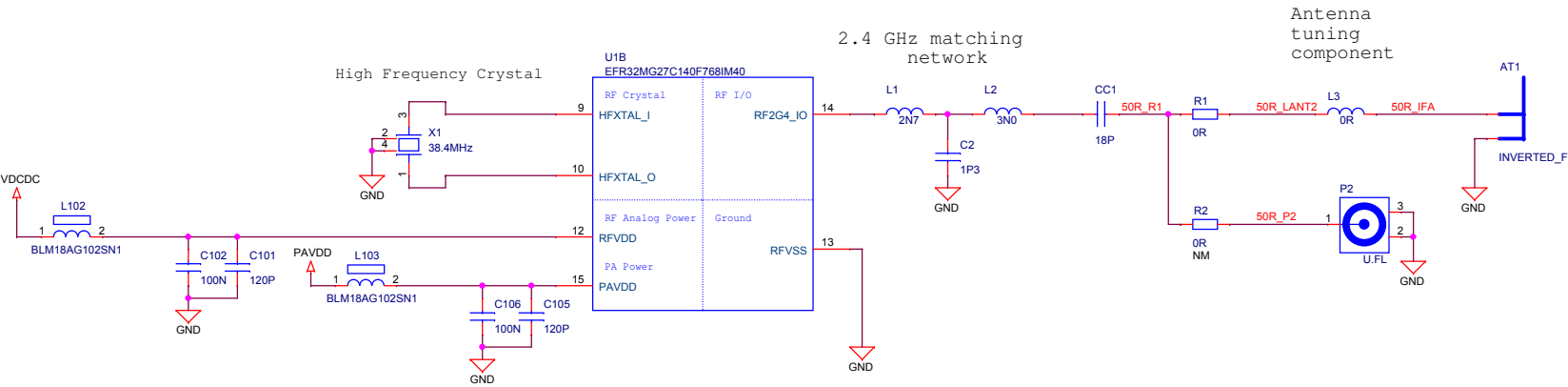
A01	Release without U1 but with matching.
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A02	Added U1.
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A03	U1 with additional factory calcs.
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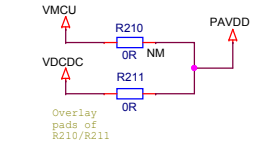
 <b>SILICON LABS</b>		Board Name <b>EFR32xG27 2.4 GHz 8 dBm Radio Board</b>	
Designed SZL		Approved RGU	
Page Title <b>Title Page</b>	Board Number <b>BRD4194A</b>		Revision <b>A03</b>
Size A3	Sheet Modified Date Friday, January 14, 2022	Sheet 1 of 4	
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Antenna & Radio Interface

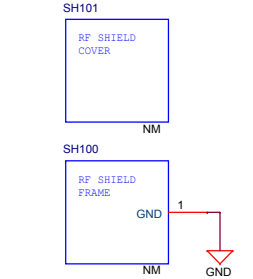


PAVDD Configuration

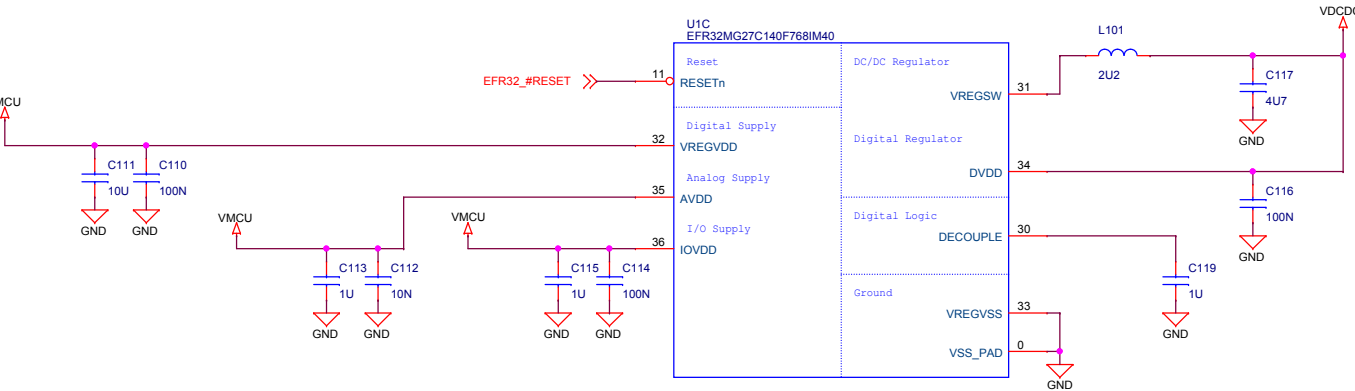
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount




RF Shielding



Power & Decoupling





**SILICON LABS**

Designed SZL Approved RGU

Size A3 Sheet Modified Date Friday, January 14, 2022

Board Name

**EFR32xG27 2.4 GHz 8 dBm Radio Board**

Page Title

**RF, Antenna and Power**

Board Number

**BRD4194A**

Revision

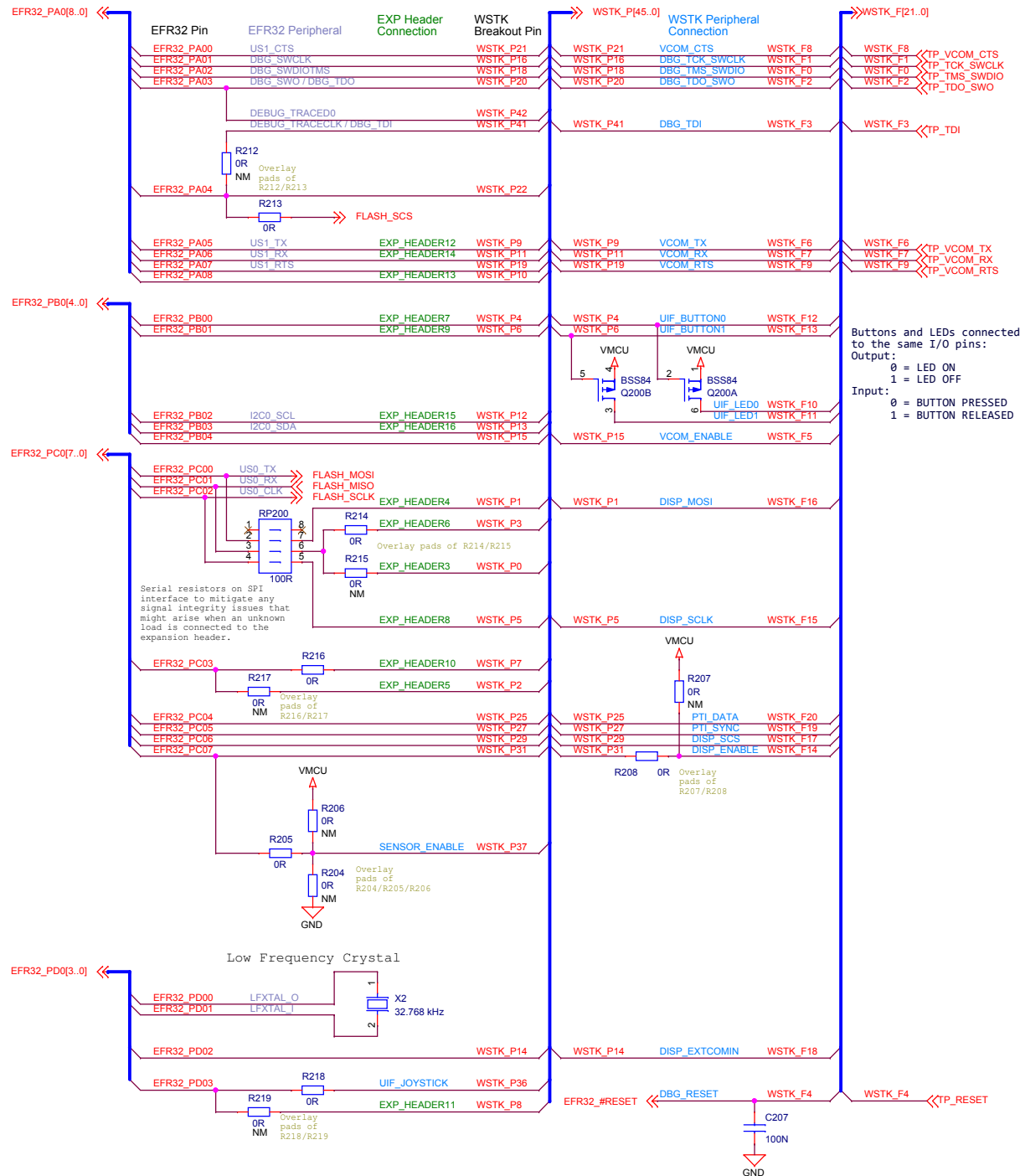
**A03**

Sheet 2 of 4

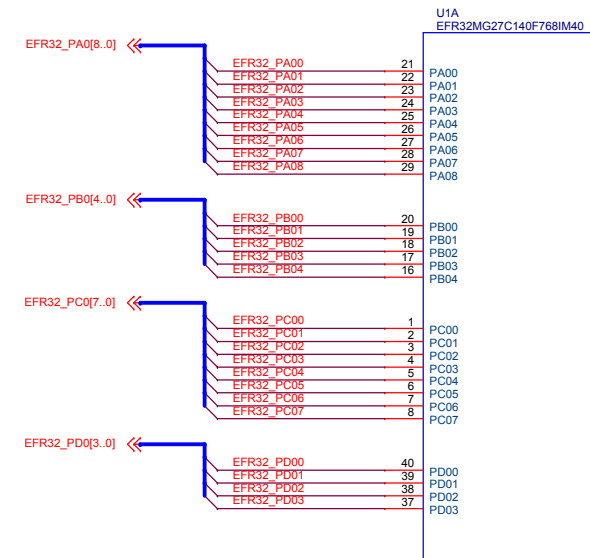
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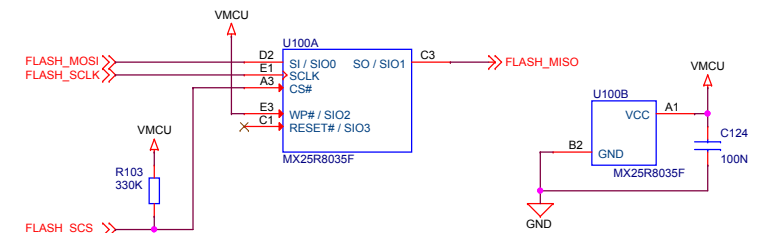
## EFR32 Pin Mapping



## EFR32 I/O Port Pins



## Serial Flash



Board Name
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## EFR32xG27 2.4 GHz 8 dBm Radio Board

Page Title

## I/O Port Connections

	Board Number
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BRD4194A

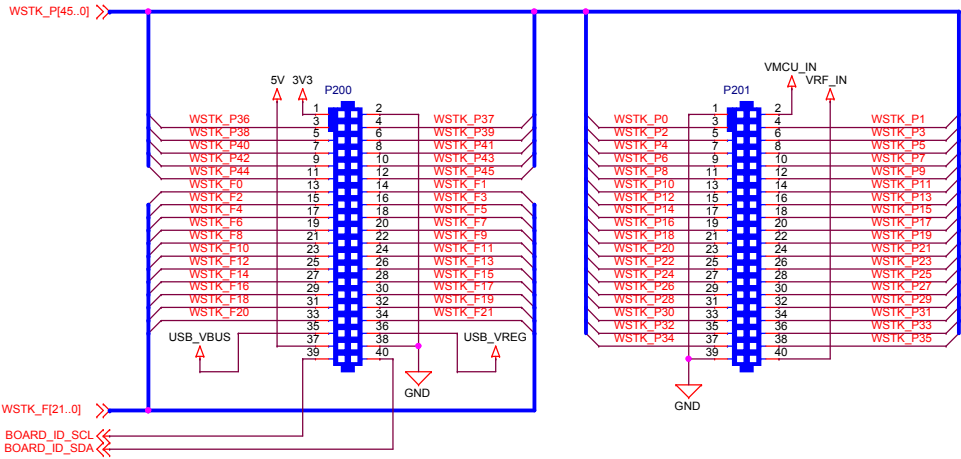
Revision

A03

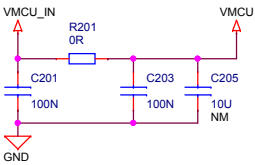
Sheet  
3 of 4

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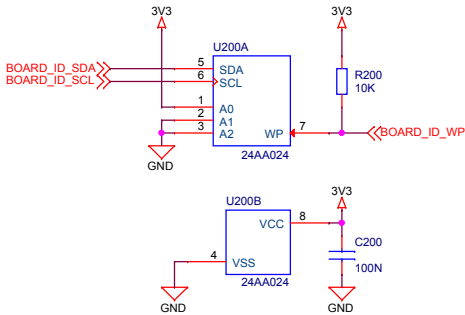
WSTK Connectors



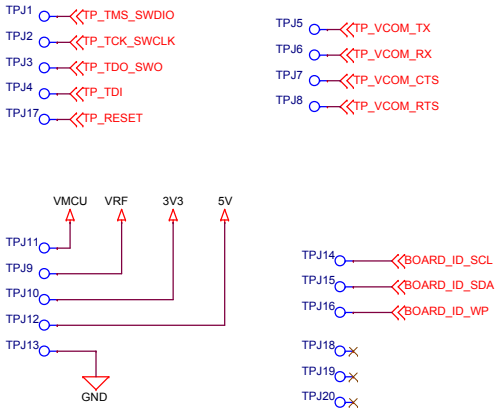
WSTK Power Decoupling



Board Identification



Test Points



Designed		Approved		WSLK Connectors & Board ID	
SZL		RGU			
Size	Sheet Modified Date			Board Number	Revision
A3	Friday, January 14, 2022			BRD4194A	A03
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