



EFR32xG27 Radio Board

2.4 GHz 8 dBm

Board Function	Page
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Title Page	1
------------	---

RF, Antenna & Power	2
---------------------	---

I/O Port Connections	3
----------------------	---

WSTK Connectors & Board ID	4
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Revision History

Rev.	Description
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A00	Initial release, without U1 and matching.
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A01	Release without U1 but with matching.
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A02	Added U1.
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A03	U1 with additional factory calcs.
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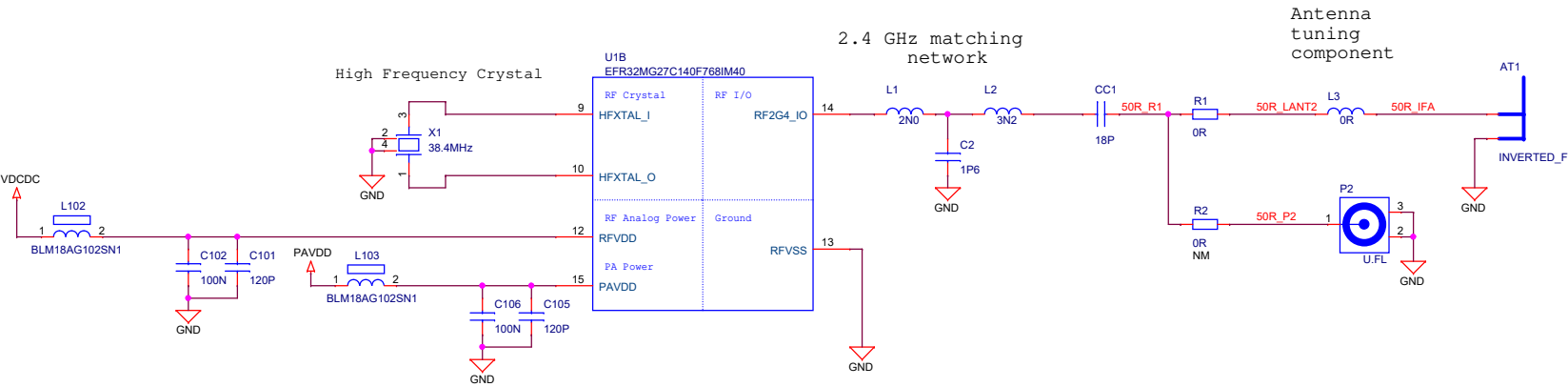
A04	U1 with additional factory tests.
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A05	Updated matching values and layout.
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A06	Updated U1 OPN.
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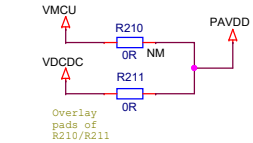
 SILICON LABS		Board Name	
		EFR32xG27 2.4 GHz 8 dBm Radio Board	
Designed SZL		Page Title	
Size A3		Title Page	
Sheet Modified Date Monday, November 14, 2022		Board Number	Revision
		BRD4194A	A06
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		Sheet 1 of 4	

Antenna & Radio Interface

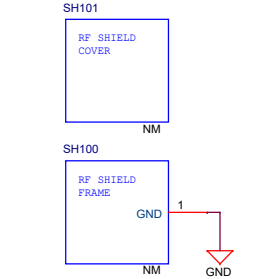


PAVDD Configuration

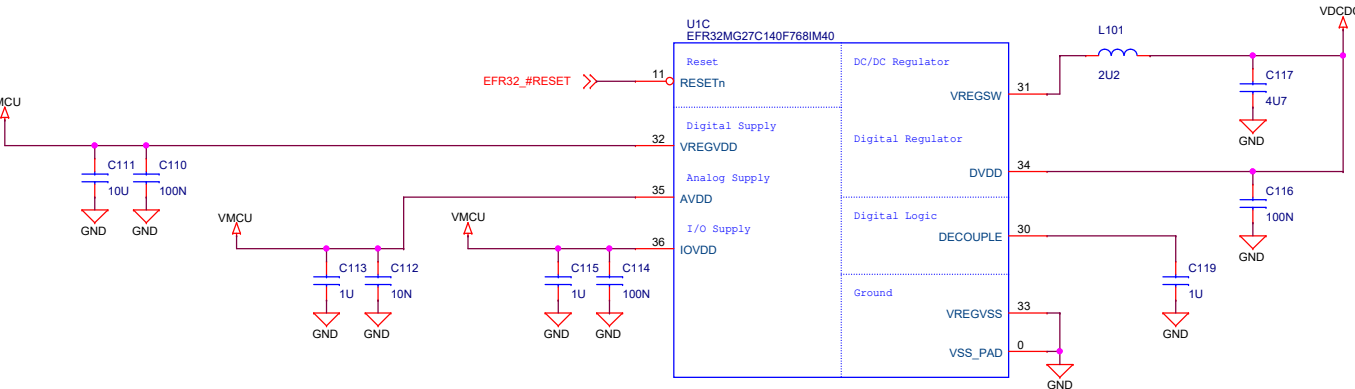
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount




RF Shielding



Power & Decoupling





SILICON LABS

Designed SZL Approved RGU
Size A3 Sheet Modified Date Monday, November 14, 2022

Board Name
EFR32xG27 2.4 GHz 8 dBm Radio Board

Page Title
RF, Antenna and Power

Board Number
BRD4194A

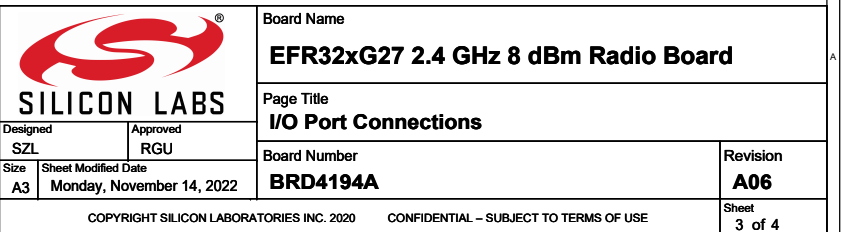
Revision
A06

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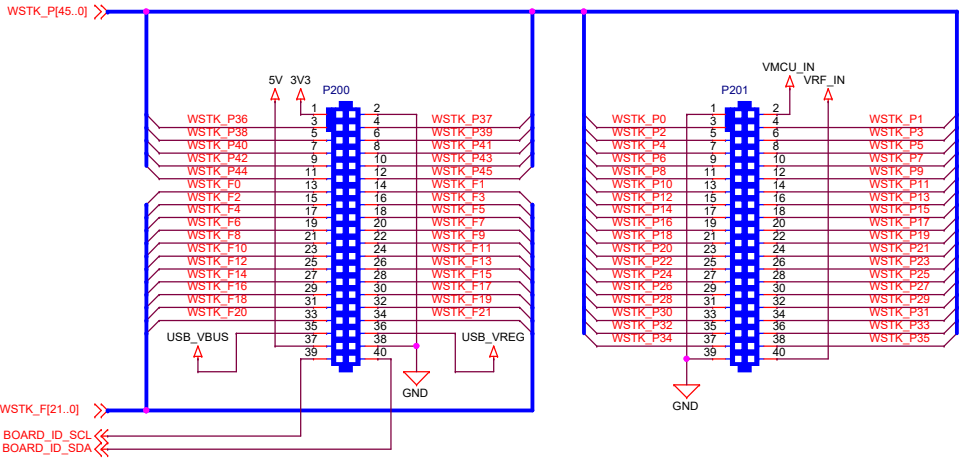
Sheet
2 of 4

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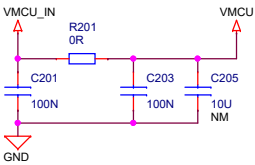
U1A		
EFR32MG27C140F768IM40		
EFR32_PA0[8..0]	21	PA00
	22	PA01
	23	PA02
	24	PA03
	25	PA04
	26	PA05
	27	PA06
	28	PA07
	29	PA08
EFR32_PB0[4..0]	20	PB00
	19	PB01
	18	PB02
	17	PB03
	16	PB04
EFR32_PC0[7..0]	1	PC00
	2	PC01
	3	PC02
	4	PC03
	5	PC04
	6	PC05
	7	PC06
	8	PC07
EFR32_PD0[3..0]	40	PD00
	39	PD01
	38	PD02
	37	PD03

[illegible]

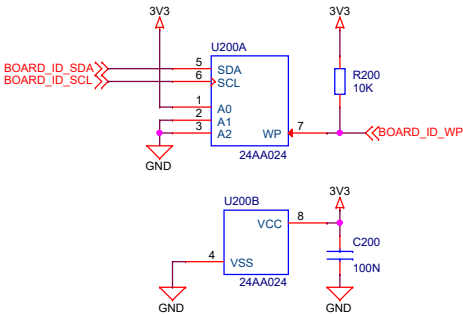
WSTK Connectors



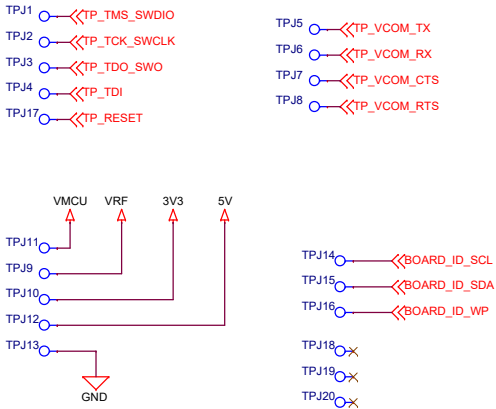
WSTK Power Decoupling



Board Identification



Test Points



Designed
SZL

Approved
RGU

Size
A3

Sheet Modified Date
Monday, November 14, 2022

Board Name EFR32xG27 2.4 GHz 8 dBm Radio Board	
Page Title WSTK Connectors & Board ID	
Board Number BRD4194A	Revision A06