



EFR32ZG14 Radio Board

SubGHz 13 dBm, VDCDC to PAVDD

Board Function	Page
Title Page	1
RF, Antenna and Power	2
I/O Port Connections	3
WSTK Connectors & Board ID	4

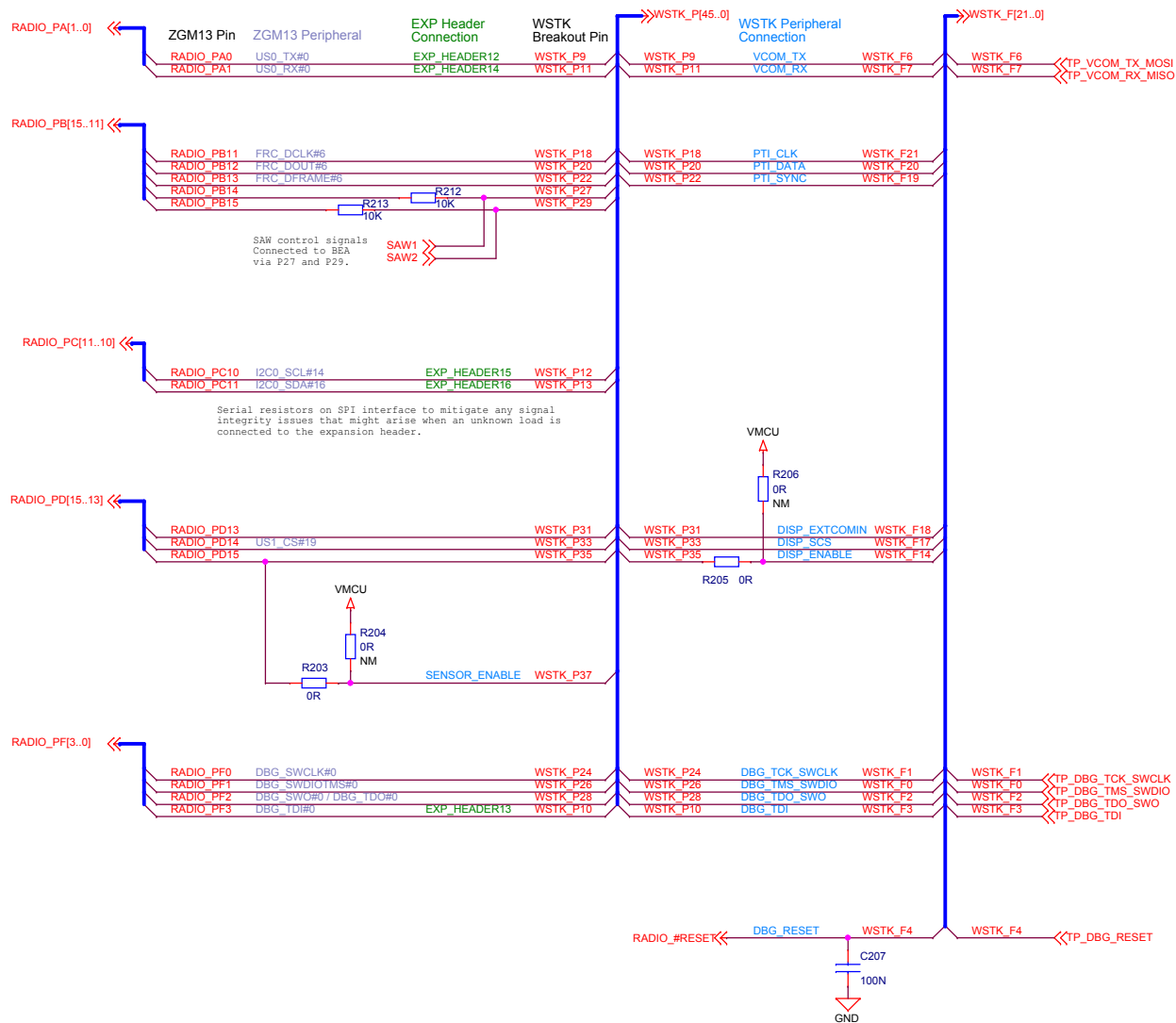
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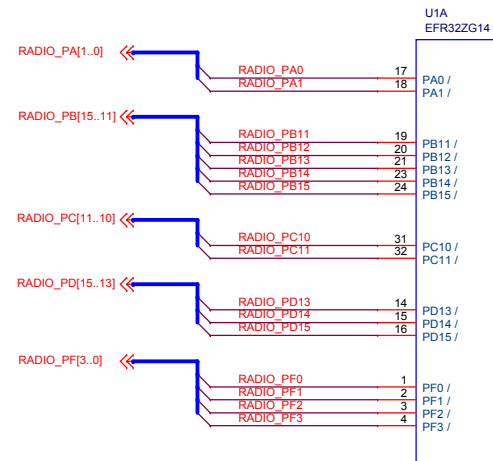
Revision History	
Rev.	Description
PA00	HWTools conversion of the Z-Wave SLWRBZW700G design. Initial revision.
PA01	Updating SAW & switch footprint names. Updating EFR P/N.
PA02	Fixing bypass path connecion at S2. R5-->C21
PA03	Removing L102. Updating ANT1 matching values. Fixing S1, S2 P/N.
PA04	R6 is mounted.
PA05	Updating filter cap values & ACTE P/Ns. Updating HF crystal P/N.
A00	Changed R1, R2 and C21 to 56pF. Initial release revision.
A01	Release for build with recommended PCB stackup.

 SILICON LABS		Schematic Title	
		EFR32ZG14 Radio Board	
Designed: TAB	Approved: JSH	Page Title	
Size A3	BOM Doc No:	Title Page	
Design Created Date: Monday, April 02, 2012		Document number BRD4201A	Revision A01
Sheet Created Date Monday, April 02, 2012		Sheet Modified Date Monday, July 16, 2018	Sheet 1 of 4

Pin Mapping

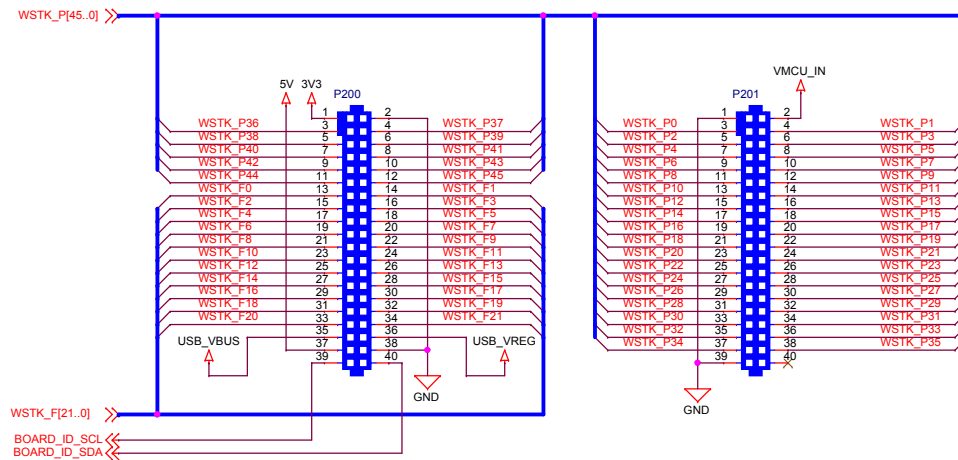


I/O Port Pins

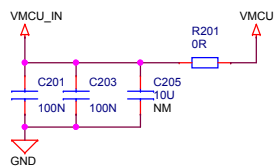


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Designed: TAB		Page Title	
Size A3		I/O Port Connections	
BOM Doc No:		Document number	Revision
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Sheet Created Date: Wednesday, April 11, 2012		Sheet Modified Date: Monday, July 16, 2018	Sheet 3 of 4

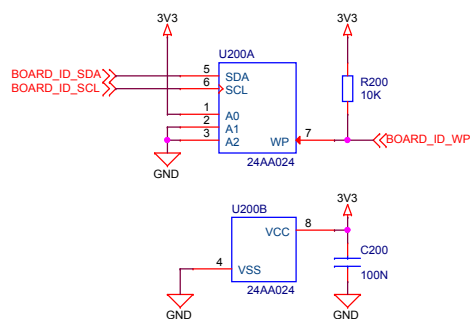
WSTK Connectors



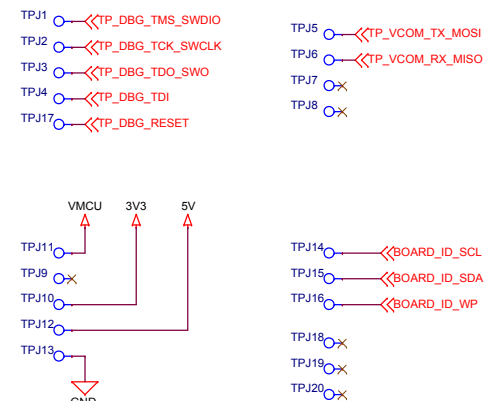
WSTK Power Decoupling



Board Identification



Test Points



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Designed: TAB		Page Title	
Size A3		WSTK Connectors and Board ID	
BOM Doc No:		Document number	Revision
Design Created Date: Monday, April 02, 2012		BRD4201A	A01
Sheet Created Date: Tuesday, April 03, 2012		Sheet Modified Date: Monday, July 16, 2018	Sheet 4 of 4