



EFR32ZG14 Radio Board

SubGHz 13 dBm, VDCDC to PAVDD

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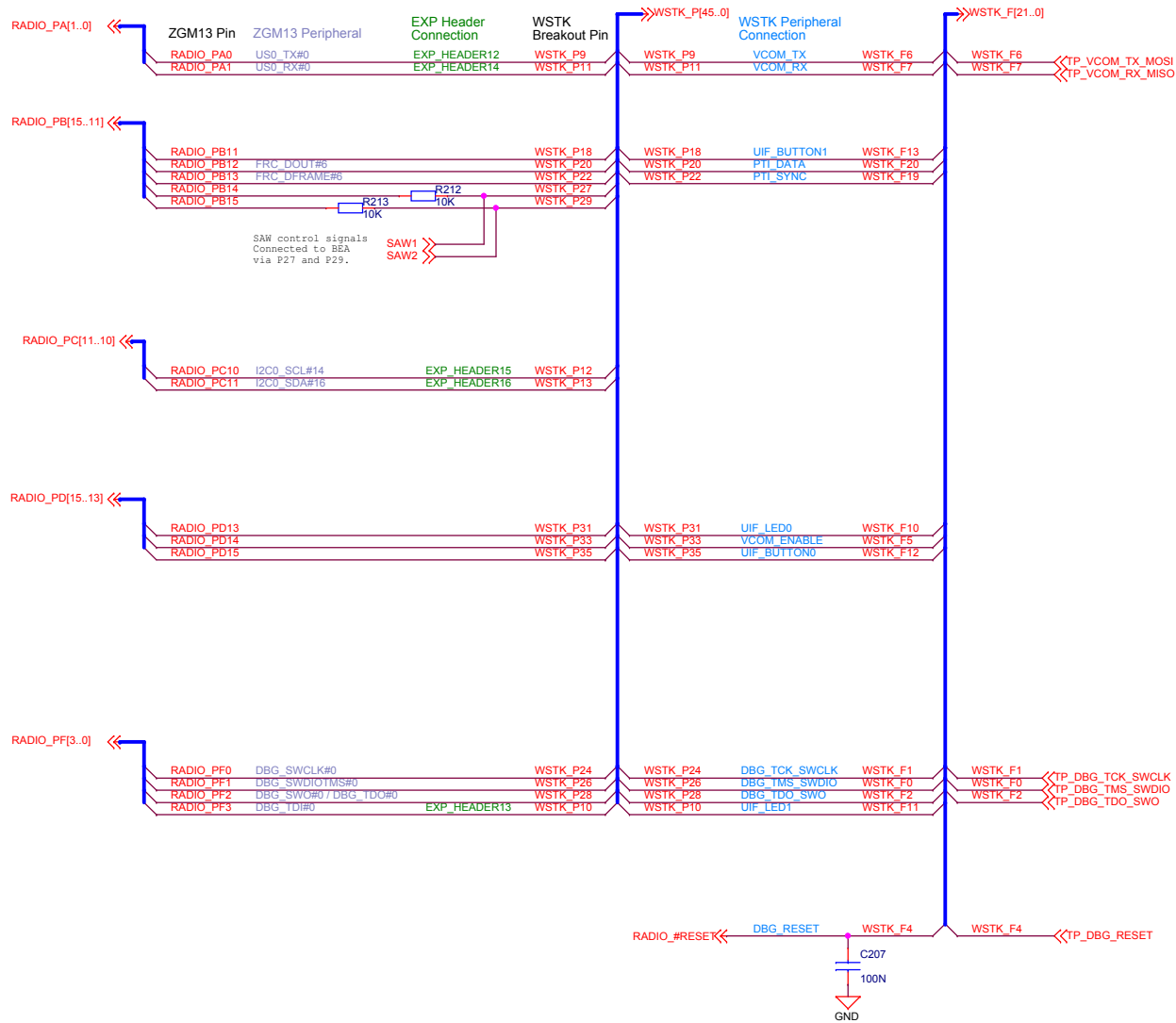
Revision History	
Rev.	Description
A00	Changed R1, R2 and C21 to 56pF. Initial release revision.
A01	Release for build with recommended PCB stackup.
A02	Fixing S3 symbol and conn. Updated pinmap, filter comps., ant. match.,
A03	RF output connected to the SMA.

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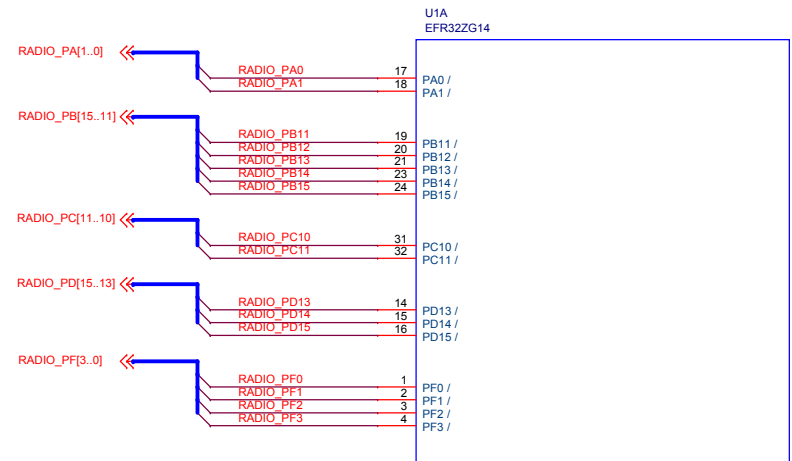


Designed: TAB		Approved: JSH		Title Page	
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Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012		Sheet Modified Date Tuesday, September 04, 2018	
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Pin Mapping

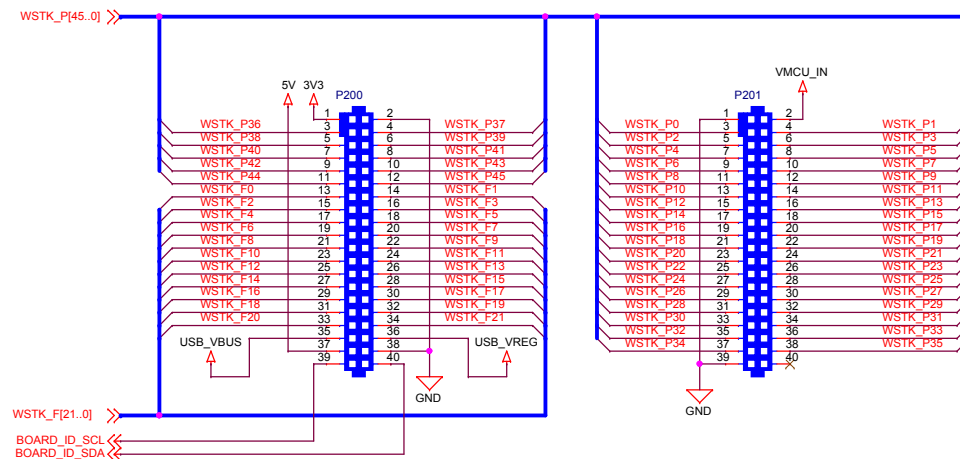


I/O Port Pins

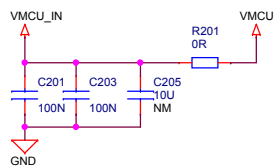


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		EFR32ZG14 Radio Board	
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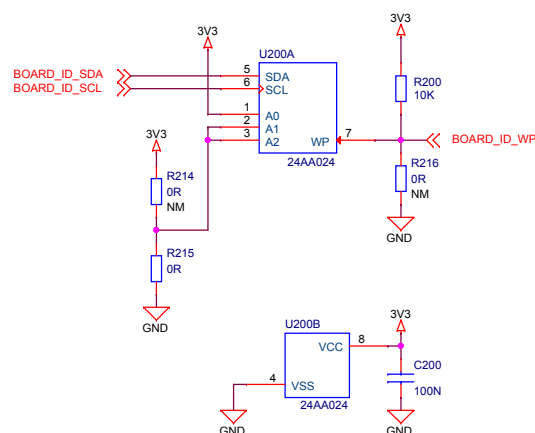
WSTK Connectors



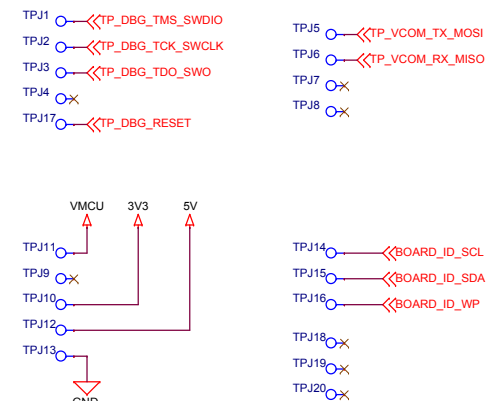
WSTK Power Decoupling



Board Identification



Test Points



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